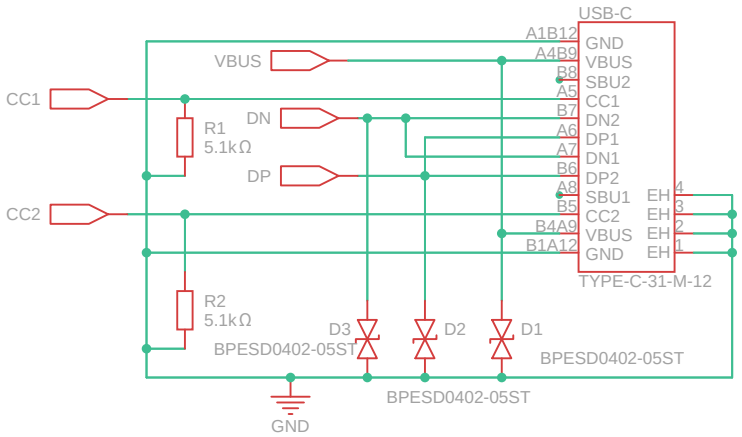
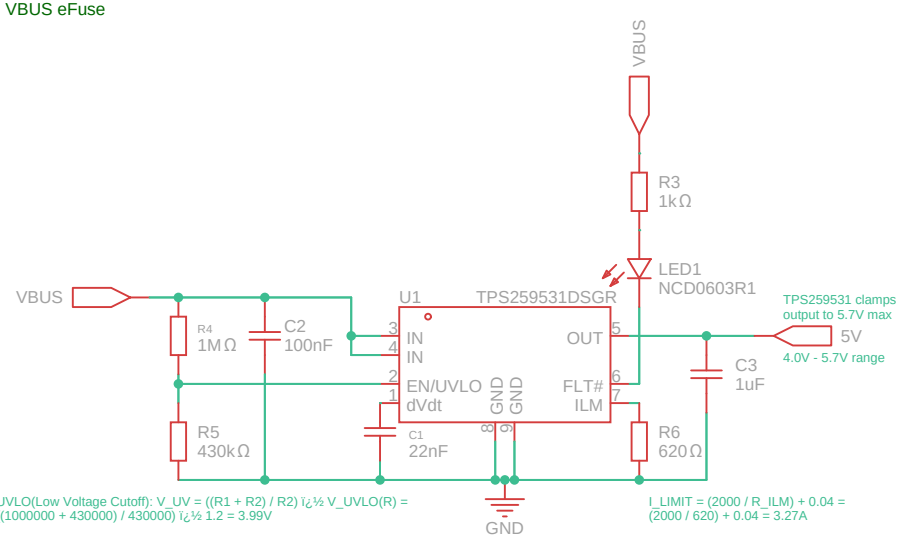
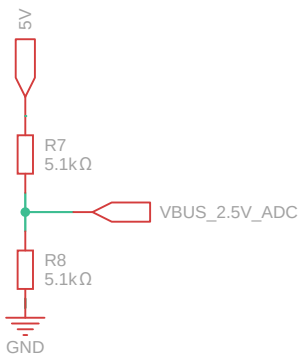
USB-C Port



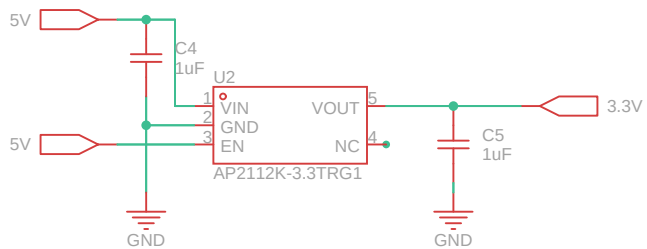
VBUS eFuse



VBUS 5V to 2.5V for ADC sampling

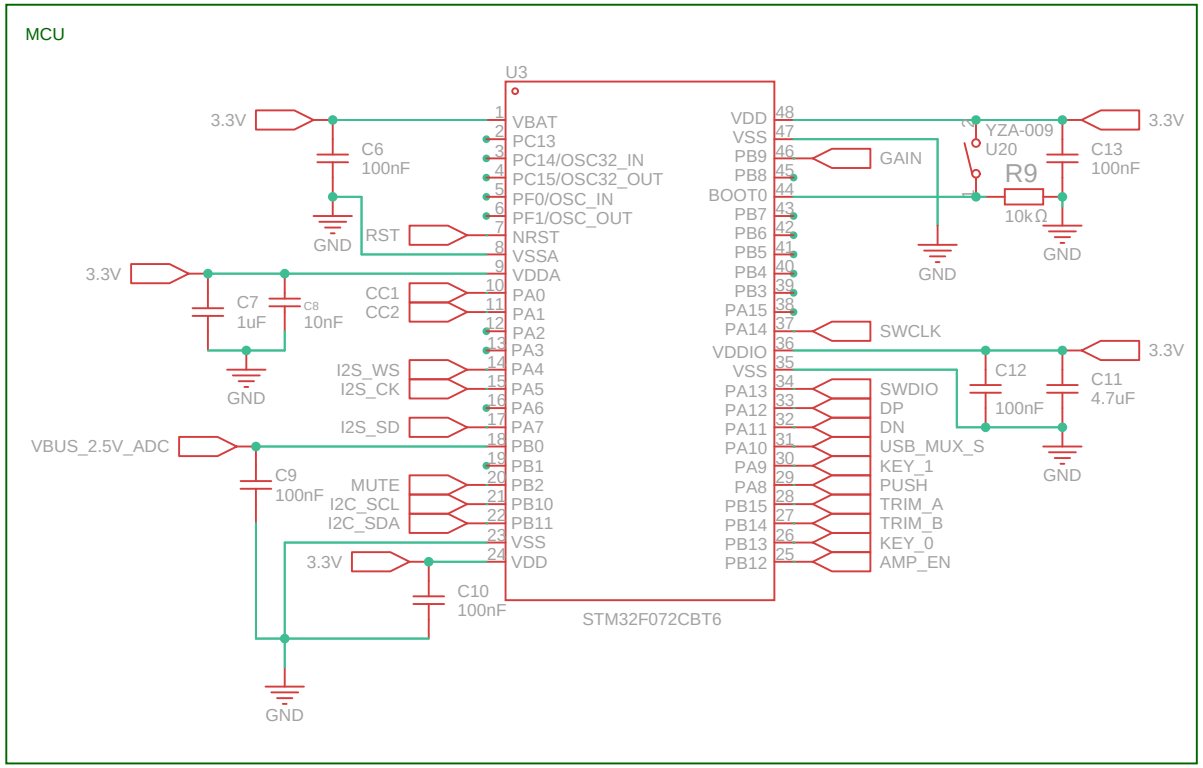


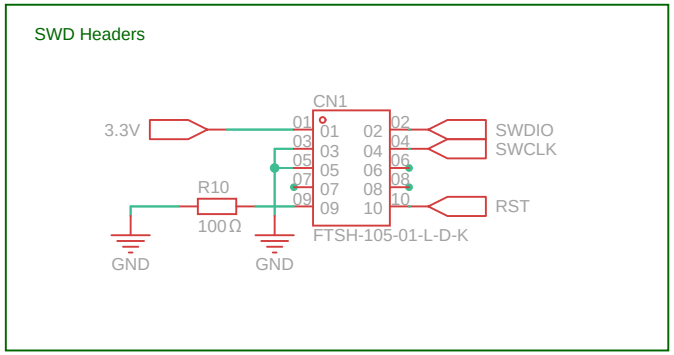
LDO eFuse to 3.3V

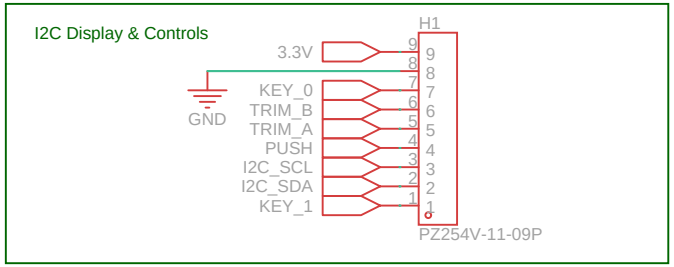


Schematic	DA15	Create at	2026-02-04
		Update at	2026-02-08
Board	DA15	Page	Power
Drawn		dac-amp	
Reviewed			
		Version	Size
EasyEDA		V1.0	A4
		Page 1 Total 3	
		EasyEDA.com	

ï¿½ 2026 Elia Chiarucci
CERN-OHL-S v2 | https://ohwr.org/cern_ohl_s_v2.txt
Source: https://github.com/eliachiarucci/DA15

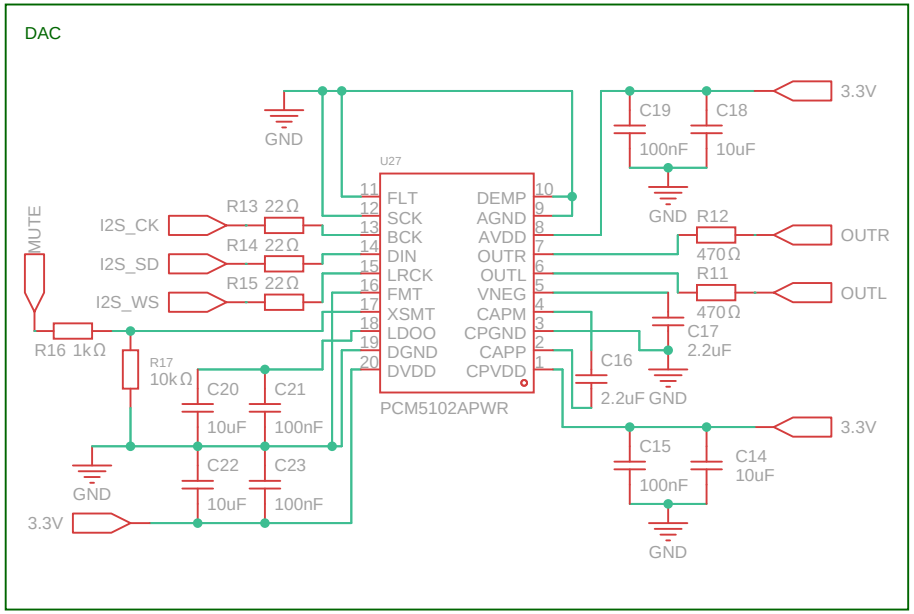


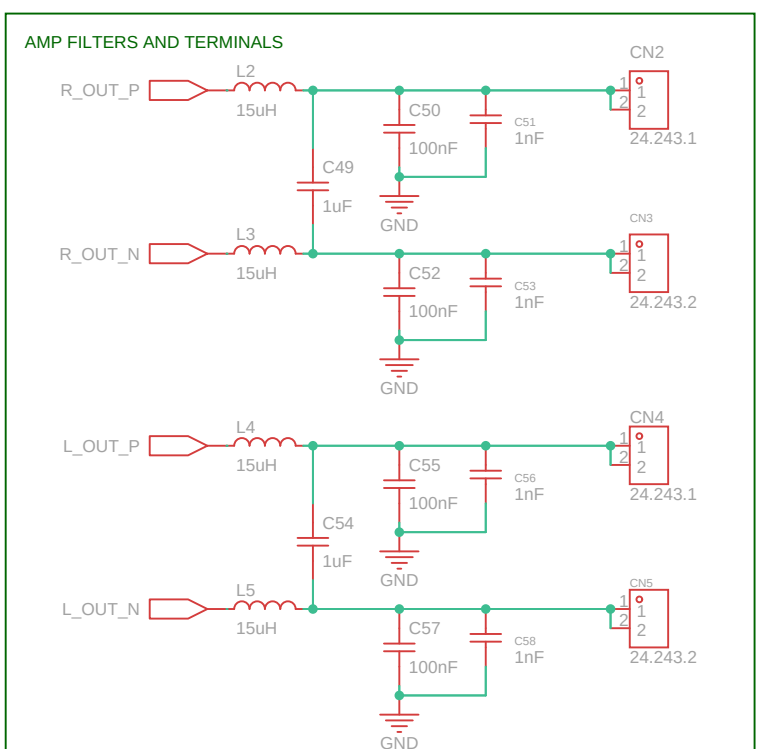


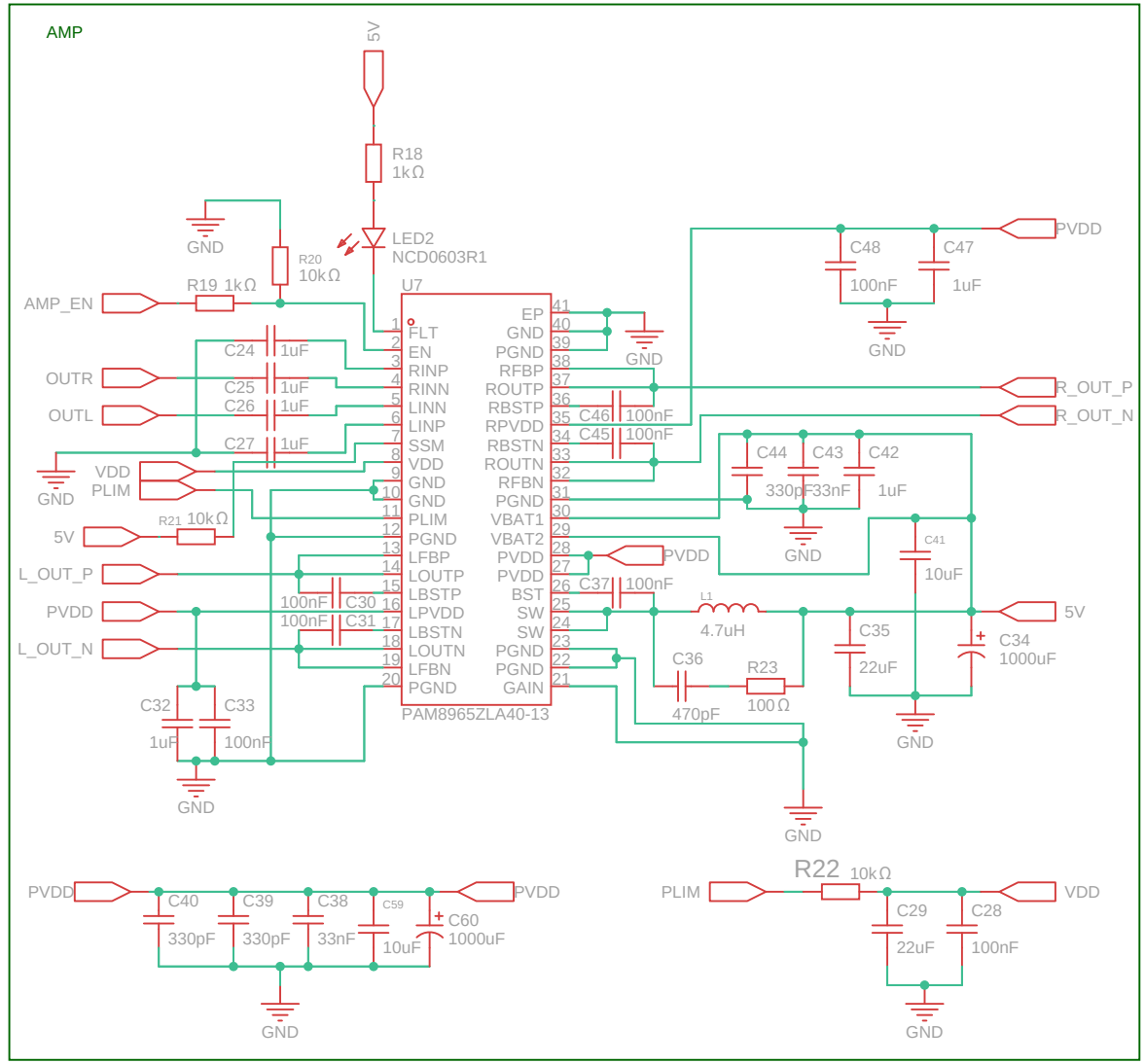


Schematic	DA15	Create at	2026-02-04
		Update at	2026-02-08
Board	DA15	Page	Controller
Drawn		dac-amp	
Reviewed			
		Version	Size	Page 2 Total 3
EasyEDA		V1.0	A4	EasyEDA.com

ï¿½ 2026 Elia Chiarucci
CERN-OHL-S v2 | https://ohwr.org/cern_ohl_s_v2.txt
Source: https://github.com/eliachiarucci/DA15







Schematic	DA15	Create at	2026-02-04
		Update at	2026-02-08
Board	DA15	Page	Audio
Drawn		dac-amp	
Reviewed			
		Version	Size
EasyEDA	V1.0	A4	EasyEDA.com

Page 3 Total 3

ï¿½ 2026 Elia Chiarucci
CERN-OHL-S v2 | https://ohwr.org/cern_ohl_s_v2.txt
Source: https://github.com/eliachiarucci/DA15