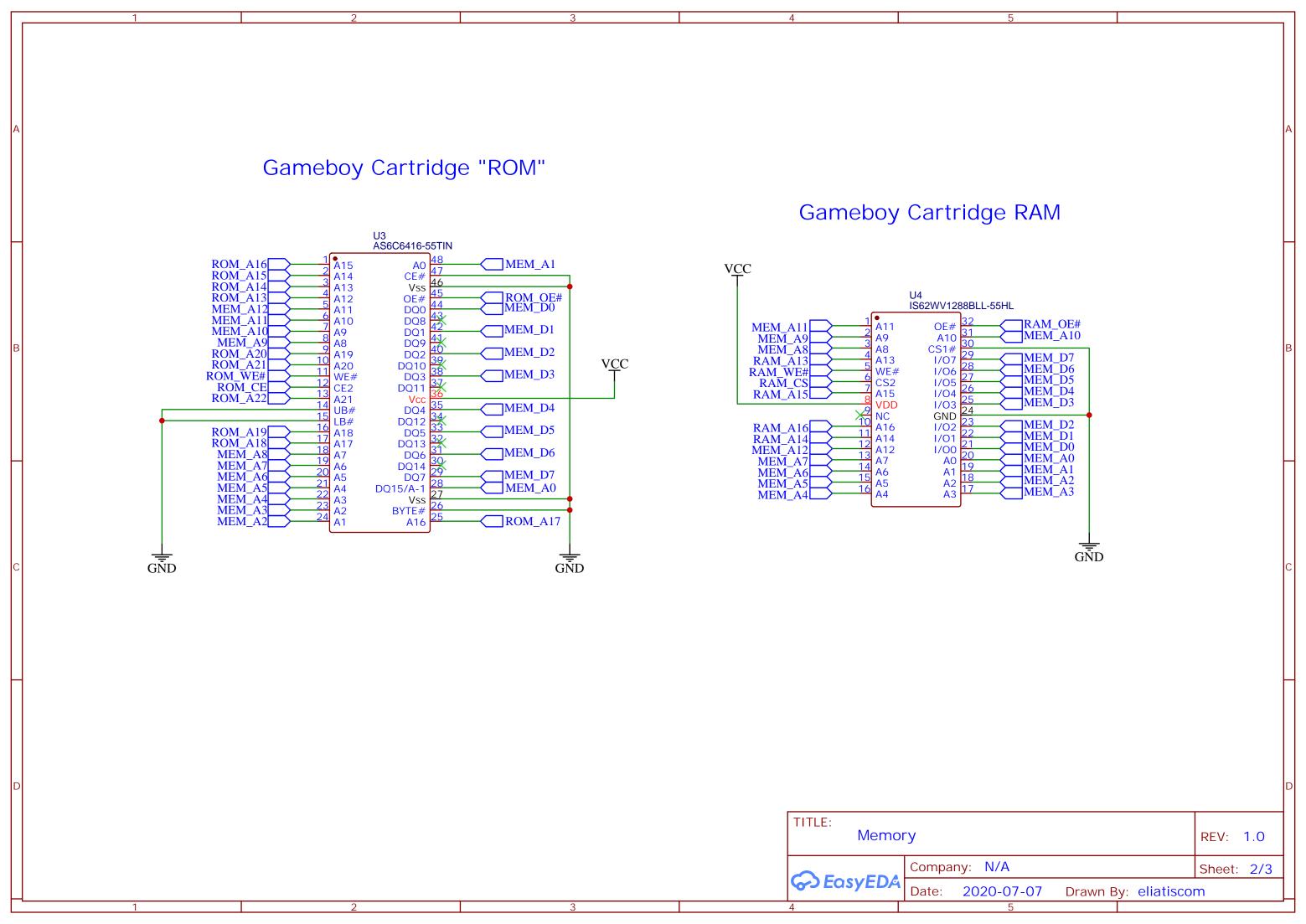
## **ROM Pins** CN3 A2005WV-16P Power regulator ROM\_OE# ROM CE VCC ROM\_WE# GB\_VCC GND U6 MIC53<u>65-3</u>.3YC5-TR GND GND RAM + Gameboy Control Pins GND VOUT ROM A14 GND ROM\_A15 NC X ΕN CN2 A2005WV-16P ROM\_A16 C10 1uF ROM\_A17 ROM\_A18 ROM\_A19 MC\_DIR ROM\_A20 RAM\_OE# RAM\_CS RAM\_WE# 를 GND ROM\_A21 ROM\_A22 12 GND RAM\_A13 RAM\_A14 RAM\_A15 RAM\_A16 JGND MC\_RST MC\_CS MC\_RD MC\_WR MC\_WR Gameboy Cartridge "ROM" Reset pin level shifter JP1 GAMEBOY-CARTRIGE-FULL-V2-5CM MODIFIED V<u>C</u>C GB\_GND U5 SN74LV1T34DCKR SND GB\_RST GB\_D7 !RST D7 GB<sup>-</sup>D6 D6 MC\_RST GB\_D5 D5 GB\_RST GND GB\_D4 GB\_D3 D4 D3 Down translated Gameboy Address bus GB\_D2 D2 GB\_D1 D1 GB\_D0 DO CN1 A2005WV-16P GND GB\_A15 GB\_A14 GB\_A13 A14 A13 MC\_A14 ROM\_A13 $GB_A12$ A12 $GB_A11$ A11 **GB\_A**10 MEM\_A12 A10 GB\_A9 GB\_A8 MEM\_A11 MEM\_A10 Α9 A8 GB\_GND GB\_A7 MEM\_A9 Α7 GB\_A6 MEM<sup>-</sup>A8 Α6 GND MEM\_A7 $GB_A5$ **A**5 GB\_A4 GB\_A3 MEM\_A6 MEM\_A5 А3 GB\_A2 MEM\_A4 A2 GB\_A1 GB\_A0 MEM\_A3 ₹ GND Α1 MEM A2 AO GB\_CS MEM\_A1 !CS GB\_RD GB\_WR MEM\_A0 !RD !WR GB\_CLK CLK GB\_VCC TITLE: Connectors & Power **REV:** 1.0 Company: N/A Sheet: 1/3 **EasyEDA** 2020-07-07 Drawn By: eliatiscom



## Data + Control transceiver U2 SN74LVCH16245ADGGR GND GB\_D7 VCC VCC MC\_DIR MEM\_D7 10E# 1B1 1A1 GB\_D6 MEM\_D6 1B2 1A2 GND **GND** GND C5 885012205012 C7 885012205012 GB D5 MEM\_D5 1B3 1A3 GB\_D4 MEM\_D4 1B4 1A4 VCC VCC GB\_D3 MEM\_D3 MEM\_D2 GND 1B5 1A5 10 nF GB\_D2 1B6 1A6 10 11 GND GND GND VCC MEM\_D0\_VCC <del>—</del> GND GB\_D1 1B7 1A7 ĠND GB\_D0 1B8 1A8 2B1 2A1 GND 14 15 16 GND GND 2B2 2A2 GND GND GND GND C6 885012205012 C8 885012205012 2B3 2B4 2A3 GND GND GND 2A4 VCC VCC 19 20 21 22 23 24 GB\_CS MC\_CS MC\_RD GND 2B5 2A5 10 nF GB\_RD 2B6 2A6 GND GND GND GB\_CLK GND ĠND MC WR 2B7 2A7 MC\_CLK 2B8 2A8 GND 2DIR 20E# **VCC** Address transceiver U1 SN7<u>4LVCH</u>16245ADGGR GND GB\_A15 VCC **VCC** 1B1 1A1 MC\_A15 GB\_A14 1A2 46 GND 44 MC\_A14 1B2 GND GND GB\_A13 ROM\_A13 MEM\_A12 1B3 1A3 885012205012 885012205012 GB\_A12 1B4 1A4 VCC VCC MEM\_A11 MEM\_A10 IGND GB\_A11 1B5 1A5 10 nF 10 nF GB\_A10 1B6 1A6 GND 1B7 GND — GND MEM\_A9 MEM\_A8 MEM\_A7 MEM\_A6 GB\_A9 1A7 GND GB\_A8 GB\_A7 1B8 1A8 13 14 VCC VCC 2B1 2A1 GB\_A6 2B2 2A2 15 16 GND GND GND C2 885012205012 C4 885012205012 GB\_A5 MEM\_A5 2B3 2A3 GB\_A4 2B4 2A4 ■MEM\_A4 VCC MEM\_A3 MEM\_A2 IGND 2B5 2A5 20 21 22 23 24 10 nF 10 nF 2A6 GND GB\_A2 2B6 GND **GND** GND 토 GND GB\_A1 MEM\_A1 2B7 2A7 2A8 2OE# MEM\_A0 GB\_A0 2B8 GND 2DIR TITLE: Bus transceivers **REV:** 1.0 Company: N/A Sheet: 3/3 **EasyEDA** Date: 2020-07-08 Drawn By: eliatiscom