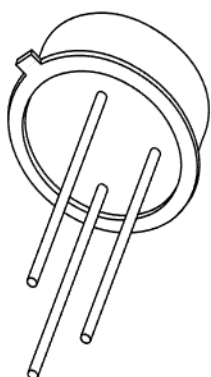


DATA SHEET



2N2222; 2N2222A NPN switching transistors

Product specification
Supersedes data of September 1994
File under Discrete Semiconductors, SC04

1997 May 29

NPN switching transistors

2N2222; 2N2222A

FEATURES

- High current (max. 800 mA)
- Low voltage (max. 40 V).

APPLICATIONS

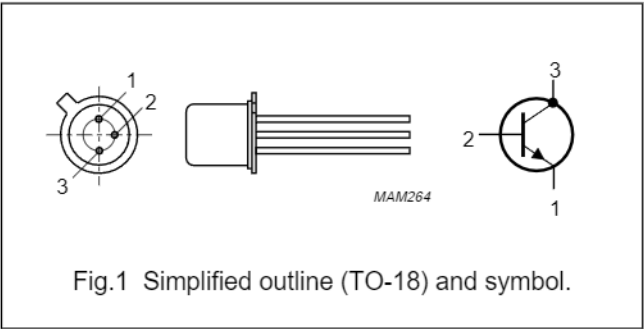
- Linear amplification and switching.

DESCRIPTION

NPN switching transistor in a TO-18 metal package.
PNP complement: 2N2907A.

PINNING

PIN	DESCRIPTION
1	emitter
2	base
3	collector, connected to case



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage 2N2222 2N2222A	open emitter	-	60	V
			-	75	V
V_{CEO}	collector-emitter voltage 2N2222 2N2222A	open base	-	30	V
			-	40	V
I_C	collector current (DC)		-	800	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ }^{\circ}\text{C}$	-	500	mW
h_{FE}	DC current gain	$I_C = 10\text{ mA}; V_{CE} = 10\text{ V}$	75	-	
f_T	transition frequency 2N2222 2N2222A	$I_C = 20\text{ mA}; V_{CE} = 20\text{ V}; f = 100\text{ MHz}$	250	-	MHz
			300	-	MHz
t_{off}	turn-off time	$I_{Con} = 150\text{ mA}; I_{Bon} = 15\text{ mA}; I_{Boff} = -15\text{ mA}$	-	250	ns

NPN switching transistors

2N2222; 2N2222A

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	–	60	V
	2N2222 2N2222A		–	75	V
V _{CEO}	collector-emitter voltage	open base	–	30	V
	2N2222 2N2222A		–	40	V
V _{EBO}	emitter-base voltage	open collector	–	5	V
	2N2222 2N2222A		–	6	V
I _C	collector current (DC)		–	800	mA
I _{CM}	peak collector current		–	800	mA
I _{BM}	peak base current		–	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	–	500	mW
		T _{case} ≤ 25 °C	–	1.2	W
T _{stg}	storage temperature		–65	+150	°C
T _j	junction temperature		–	200	°C
T _{amb}	operating ambient temperature		–65	+150	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	in free air	350	K/W
R _{th j-c}	thermal resistance from junction to case		146	K/W

NPN switching transistors

2N2222; 2N2222A

CHARACTERISTICST_j = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current 2N2222	I _E = 0; V _{CB} = 50 V	–	10	nA
		I _E = 0; V _{CB} = 50 V; T _{amb} = 150 °C	–	10	∞A
I _{CBO}	collector cut-off current 2N2222A	I _E = 0; V _{CB} = 60 V	–	10	nA
		I _E = 0; V _{CB} = 60 V; T _{amb} = 150 °C	–	10	∞A
I _{EBO}	emitter cut-off current	I _C = 0; V _{EB} = 3 V	–	10	nA
h _{FE}	DC current gain	I _C = 0.1 mA; V _{CE} = 10 V	35	–	
		I _C = 1 mA; V _{CE} = 10 V	50	–	
		I _C = 10 mA; V _{CE} = 10 V	75	–	
		I _C = 150 mA; V _{CE} = 1 V; note 1	50	–	
		I _C = 150 mA; V _{CE} = 10 V; note 1	100	300	
h _{FE}	DC current gain 2N2222A	I _C = 10 mA; V _{CE} = 10 V; T _{amb} = –55 °C	35	–	
h _{FE}	DC current gain 2N2222 2N2222A	I _C = 500 mA; V _{CE} = 10 V; note 1	30 40	– –	
V _{CEsat}	collector-emitter saturation voltage 2N2222	I _C = 150 mA; I _B = 15 mA; note 1	–	400	mV
		I _C = 500 mA; I _B = 50 mA; note 1	–	1.6	V
V _{CEsat}	collector-emitter saturation voltage 2N2222A	I _C = 150 mA; I _B = 15 mA; note 1	–	300	mV
		I _C = 500 mA; I _B = 50 mA; note 1	–	1	V
V _{BEsat}	base-emitter saturation voltage 2N2222	I _C = 150 mA; I _B = 15 mA; note 1	–	1.3	V
		I _C = 500 mA; I _B = 50 mA; note 1	–	2.6	V
V _{BEsat}	base-emitter saturation voltage 2N2222A	I _C = 150 mA; I _B = 15 mA; note 1	0.6	1.2	V
		I _C = 500 mA; I _B = 50 mA; note 1	–	2	V
C _c	collector capacitance	I _E = i _e = 0; V _{CB} = 10 V; f = 1 MHz	–	8	pF
C _e	emitter capacitance 2N2222A	I _C = i _c = 0; V _{EB} = 500 mV; f = 1 MHz	–	25	pF
f _T	transition frequency 2N2222 2N2222A	I _C = 20 mA; V _{CE} = 20 V; f = 100 MHz	250 300	– –	MHz MHz
F	noise figure 2N2222A	I _C = 200 ∞A; V _{CE} = 5 V; R _S = 2 kΩ; f = 1 kHz; B = 200 Hz	–	4	dB

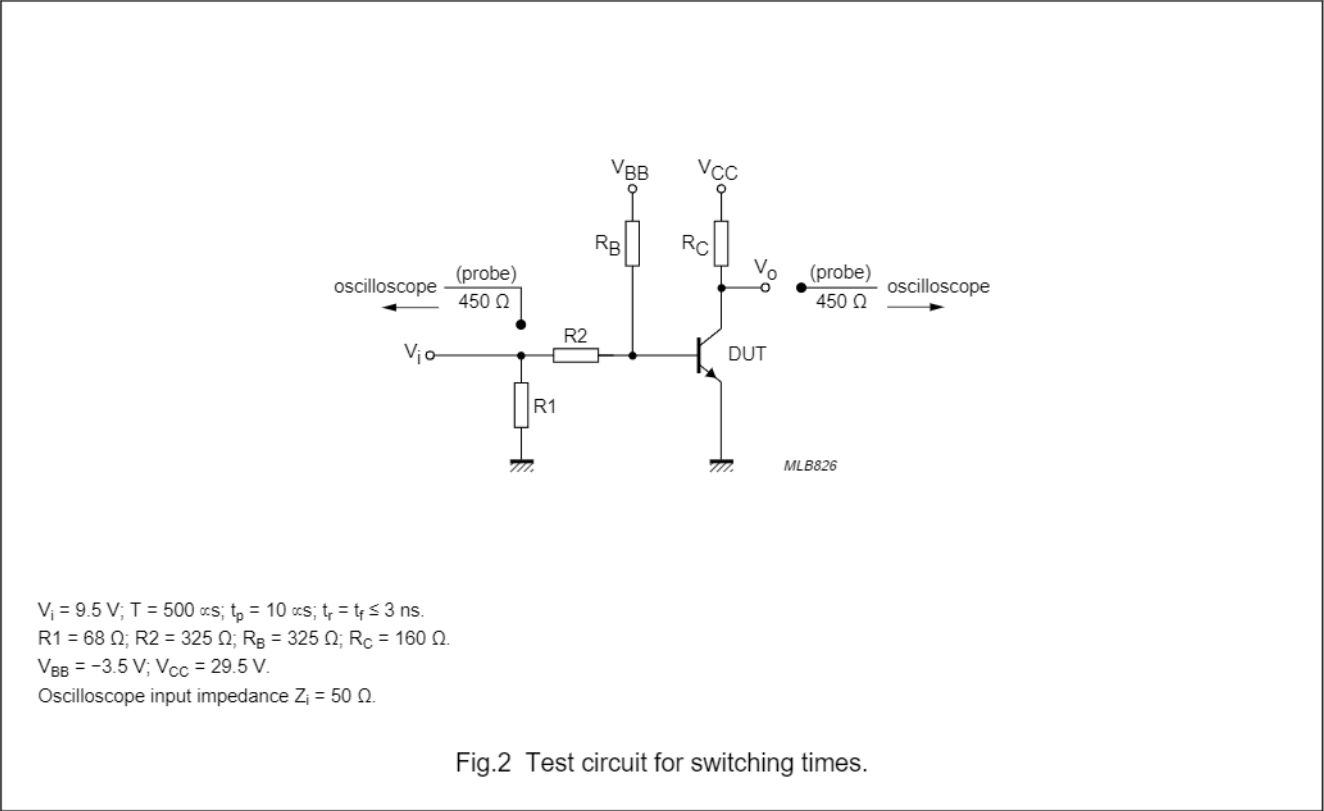
NPN switching transistors

2N2222; 2N2222A

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Switching times (between 10% and 90% levels); see Fig.2					
t _{on}	turn-on time	I _{Con} = 150 mA; I _{Bon} = 15 mA; I _{Boff} = -15 mA	-	35	ns
t _d	delay time		-	10	ns
t _r	rise time		-	25	ns
t _{off}	turn-off time		-	250	ns
t _s	storage time		-	200	ns
t _f	fall time		-	60	ns

Note

1. Pulse test: t_p ≤ 300 μs; δ ≤ 0.02.



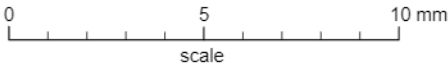
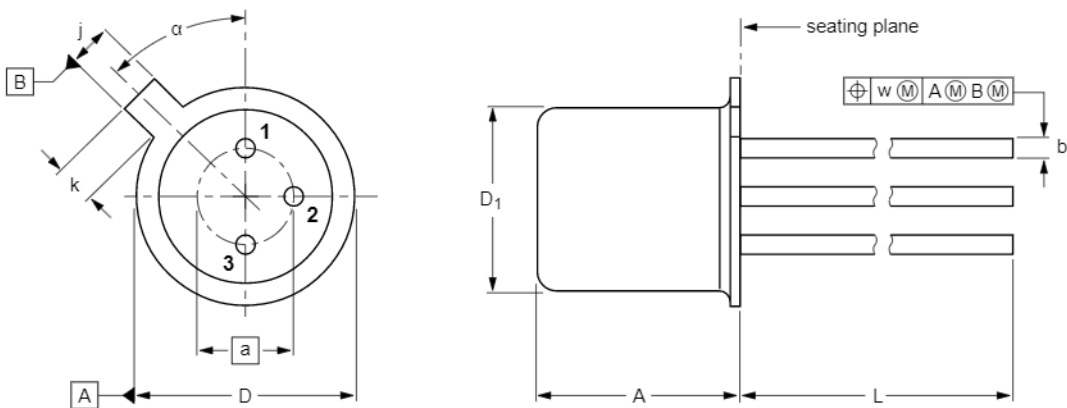
NPN switching transistors

2N2222; 2N2222A

PACKAGE OUTLINE


Metal-can cylindrical single-ended package; 3 leads

SOT18/13



DIMENSIONS (millimetre dimensions are derived from the original inch dimensions)

UNIT	A	a	b	D	D ₁	j	k	L	w	α
mm	5.31 4.74	2.54	0.47 0.41	5.45 5.30	4.70 4.55	1.03 0.94	1.1 0.9	15.0 12.7	0.40	45°

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT18/13	B11/C7 type 3	TO-18				97-04-18

NPN switching transistors

2N2222; 2N2222A

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

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