



Note:

- C12: when counter counts 12 clock cycles .
- C6 : when counter counts 6 clock cycles .
- C2: when counter counts 2 clock cycles .
- We are working with SB only for now to make it easier
NB will be later implemented .

outputs \rightarrow

$$\left\{ \begin{array}{l} TG = y_0 ; TY = y_1 ; TR = y_2 + y_3 + y_4 + y_5 + \dots + y_{10} \\ PC = y_3 + y_5 + y_7 + y_9 \\ PR = y_0 + y_1 + y_2 + y_{10} \end{array} \right.$$

next states \rightarrow

where
 $D_i = Y_i$
 (after simplification)

$$\left\{ \begin{array}{l} Y_0 = y_0 \cdot SB \cdot \overline{C12} + y_{10} \cdot C2 \\ Y_1 = y_0 \cdot SB \cdot C12 + y_1 \cdot \overline{C2} \\ Y_2 = y_1 \cdot C2 + y_2 \cdot \overline{C2} \\ Y_3 = y_2 \cdot C2 + y_3 \cdot \overline{C6} \\ Y_4 = y_3 \cdot C6 \\ Y_5 = y_4 ; Y_6 = \underline{y_5} ; Y_7 = y_6 ; Y_8 = y_7 ; Y_9 = y_8 \\ Y_{10} = y_9 + y_{10} \cdot \overline{C2} \end{array} \right.$$

Block Diagram Overview



