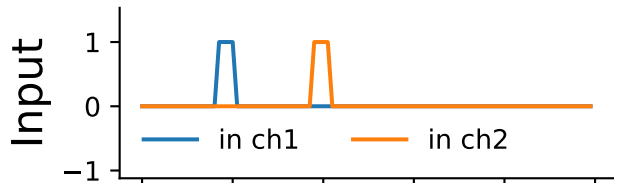
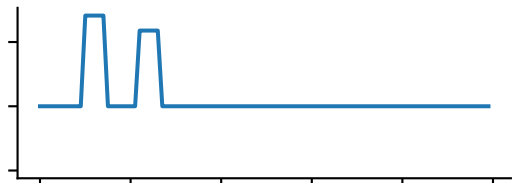


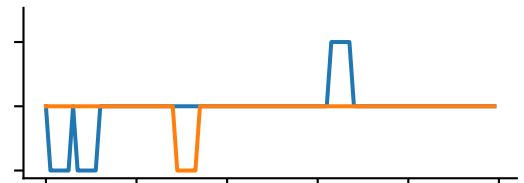
Ready-Set-Go



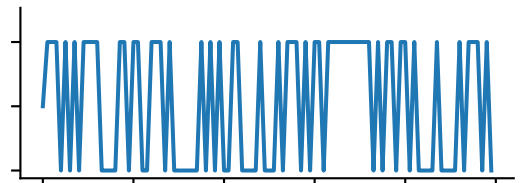
Delay-Discrimination



Flip-Flop



Integrator



Target

