Homework 4

Fetch Cycle Memory CPU Registers 300 3005 301 5940 302 7006 3005 IR	Execute Cycle Memory 300 3005 301 5940 302 7006 300 TR
990 0002 Devices Dev 5 0003 Dev 6	990 [0002] Devices Dev 5 [0003] Dev 6
Memory CPU Registers 300 3005 301 5940 302 7006 5940 IR	Memory (PU Registers 300 3005 301 5940 302 7006 3005 IR
990 0002 Devices Dev 5 0003 Dev 6	940 0002 3+2=5 Devices Dev 5 0003 Dev 6

CPU Registers Memory CPU Registers Memory [300] PC 300 3005 [300] PC 300 3005 0005 AC 301 5940 0005 AC 301 5940 3005 IR 302 7006 700 6 IR 302 7006 990 0002 990 0002 Devices Devices Dev 5 0003 Dev 5 0003 Dev 6 [0005 Dev 6