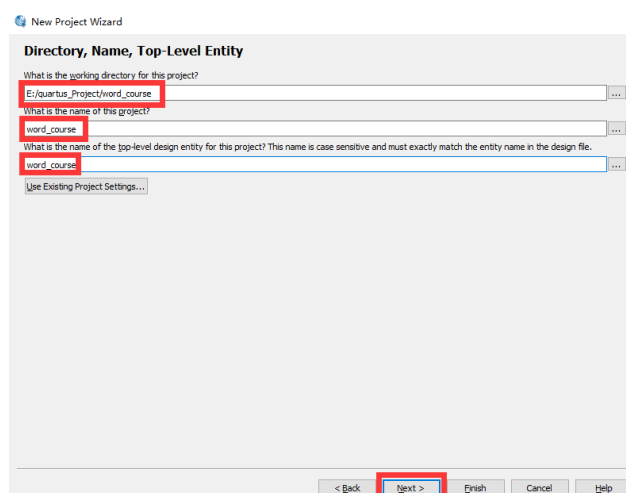
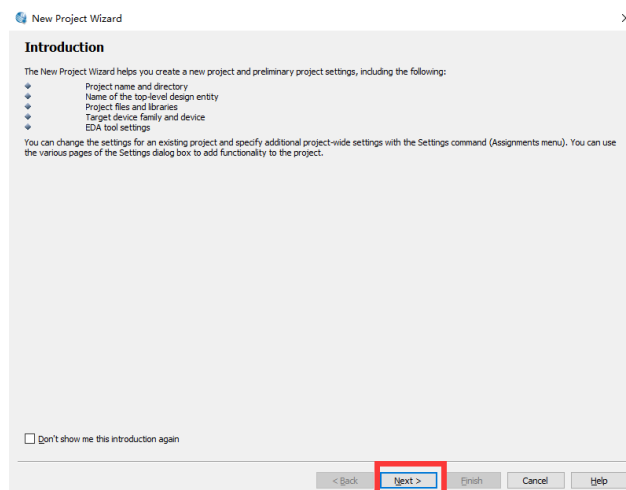
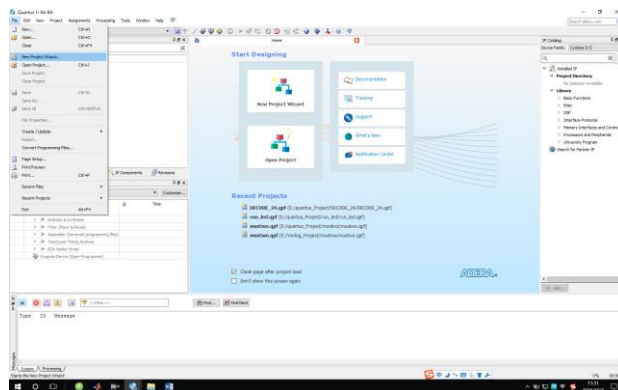


# Quartus II 的操作步骤

## 1、新建工程



New Project Wizard

Project Type

Select the type of project to create.

☒ Empty project

Create new project by specifying project files and libraries, target device family and device, and EDA tool settings.

☐ Project template

Create a project from an existing design template. You can choose from design templates installed with the Quartus II software, or download design templates from the [Design Store](#).

< Back

Next >

Finish

Cancel

Help

New Project Wizard

Add Files

Select the design files you want to include in the project. Click Add All to add all design files in the project directory to the project.  
Note: you can always add design files to the project later.

File name:

...

Add

File Name	Type	Library	Design Entry/Synthesis Tool	HDL Version
-----------	------	---------	-----------------------------	-------------

Add All  
Remove  
Up  
Down  
Properties

Specify the path names of any non-default libraries.

User Libraries...

< Back

Next >

Finish

Cancel

Help

New Project Wizard

Family & Device Settings

Select the family and device you want to target for compilation.  
You can install additional device support with the Install Devices command on the Tools menu.  
To determine the version of the Quartus II software in which your target device is supported, refer to the [Device Support List](#) webpage.

Device family

Family: **Cyclone IV E**

Devices: All

Target device

☐ Auto device selected by the Fitter

☒ Specific device selected in 'Available devices' list

☐ Other: n/a

Show in 'Available devices' list

Package: Any

Pin counts: Any

Core Speed grade: Any

Name filter:

☒ Show advanced devices

Available devices:

Name	Core Voltage	LEs	Total I/Os	GPIOs	Memory Bits	Embedded multiplier 9-bit elements
EP4KCE2227	1.2V	6272	92	92	276480	30
EP4KCE2238L	1.0V	6272	92	92	276480	30
EP4KCE9F17A7	1.2V	6272	180	180	276480	30
EP4KCE9F17C6	1.2V	6272	180	180	276480	30
EP4KCE9F17C7	1.2V	6272	180	180	276480	30
EP4KCE9F17C8	1.2V	6272	180	180	276480	30
EP4KCE9F17C8L	1.0V	6272	180	180	276480	30
EP4KCE9F17C9L	1.0V	6272	180	180	276480	30
EP4KCE9F17I7	1.2V	6272	180	180	276480	30

< Back

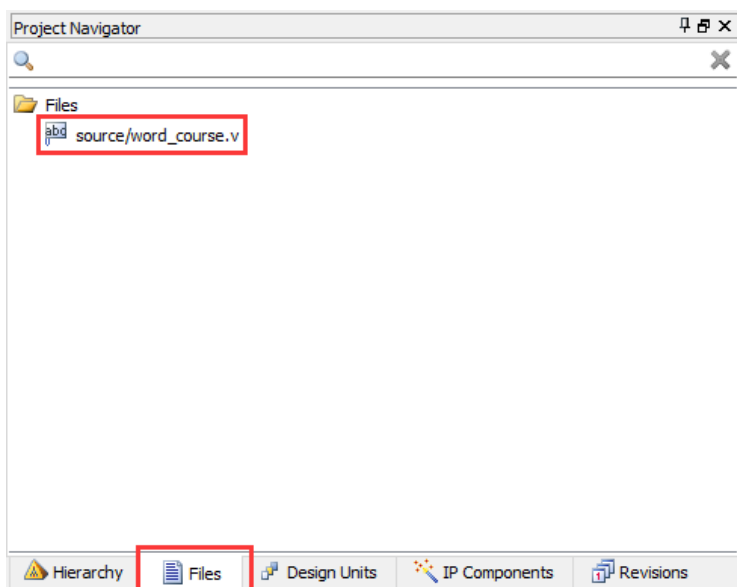
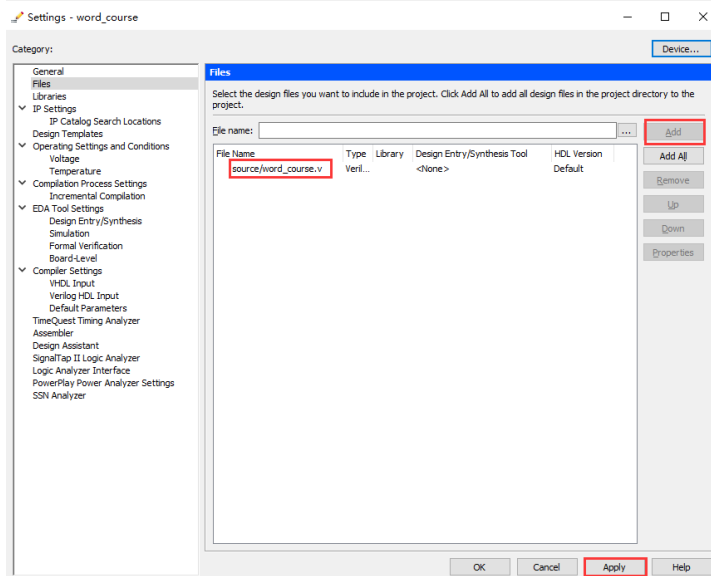
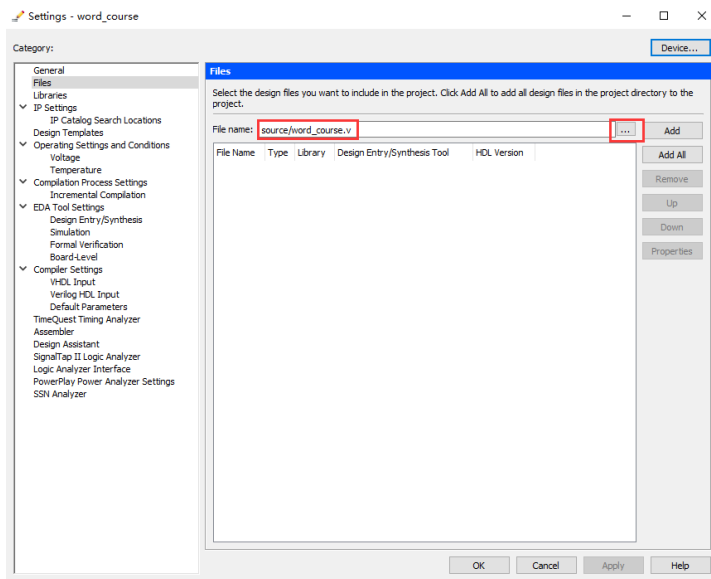
Next >

Finish

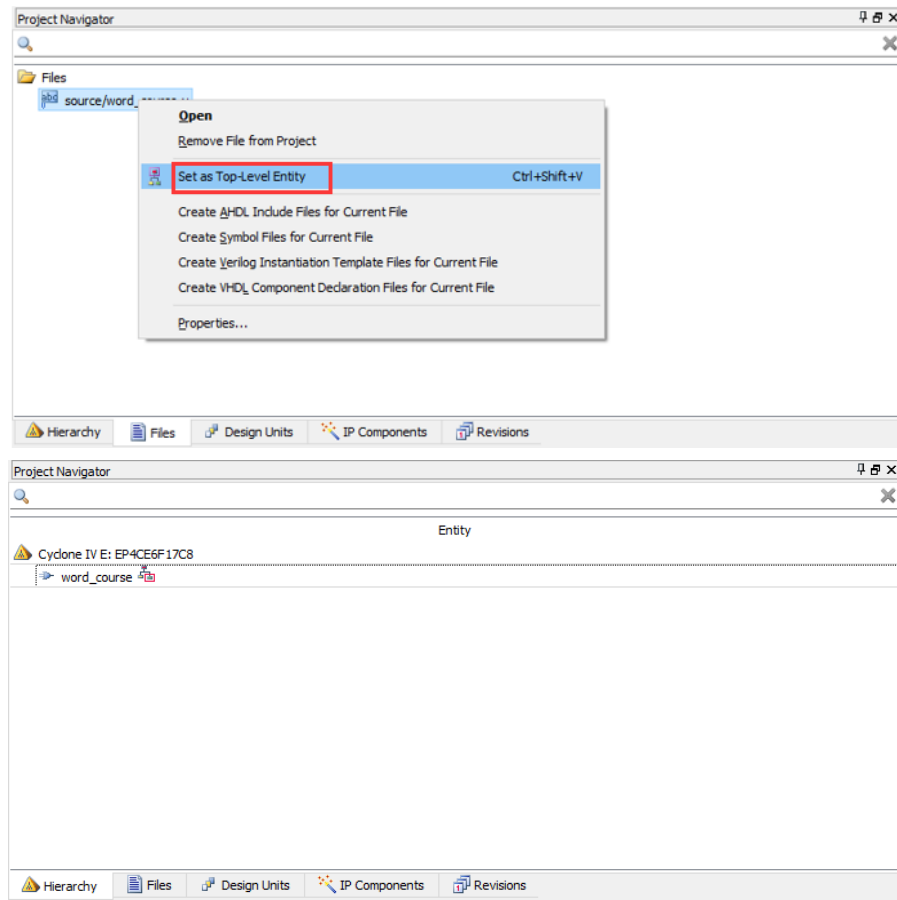
Cancel

Help

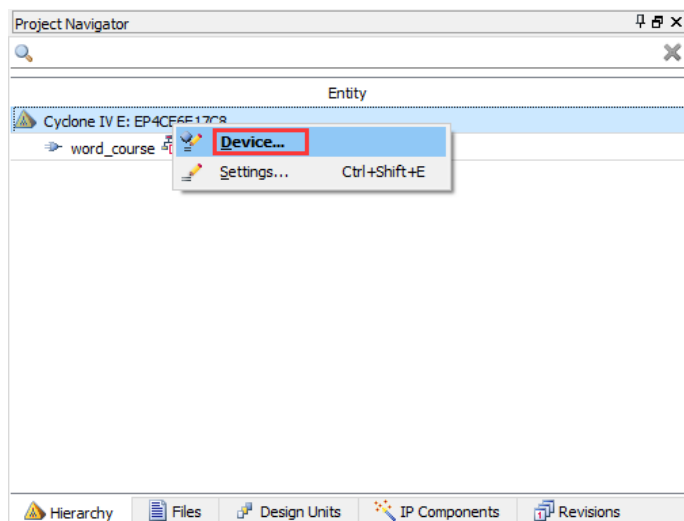




#### 4、设置顶层文件

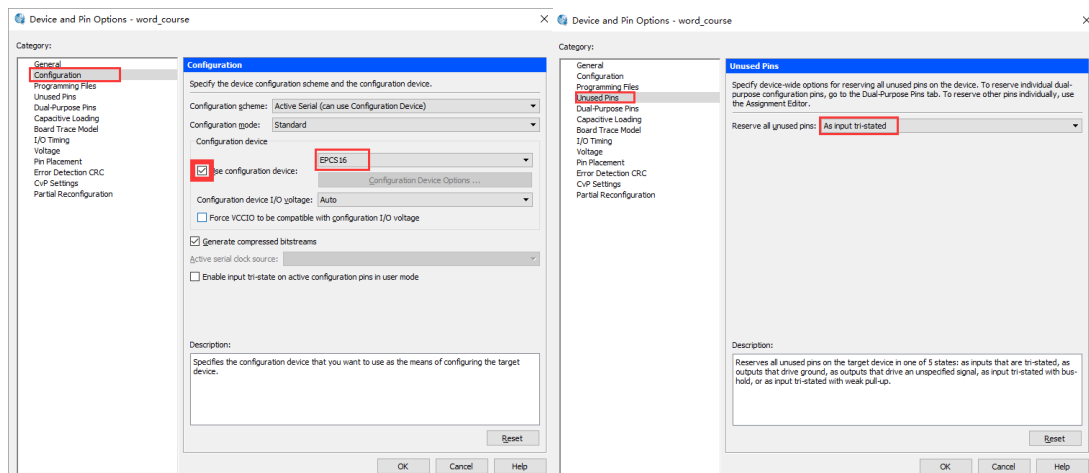
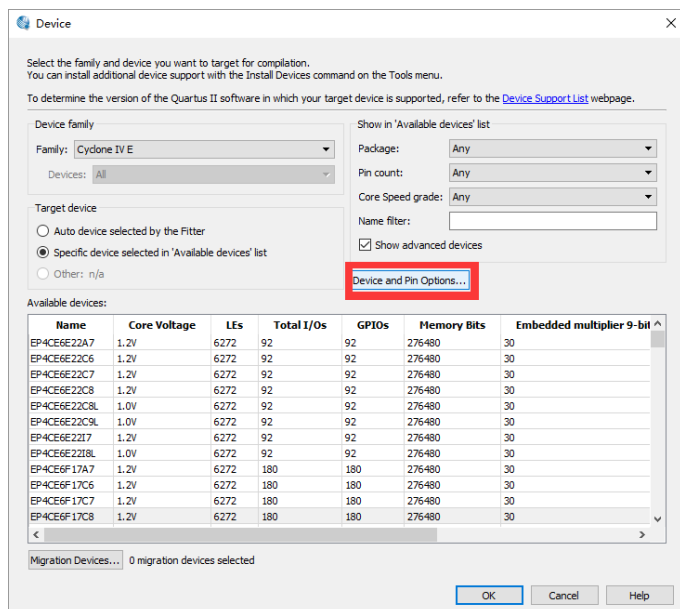
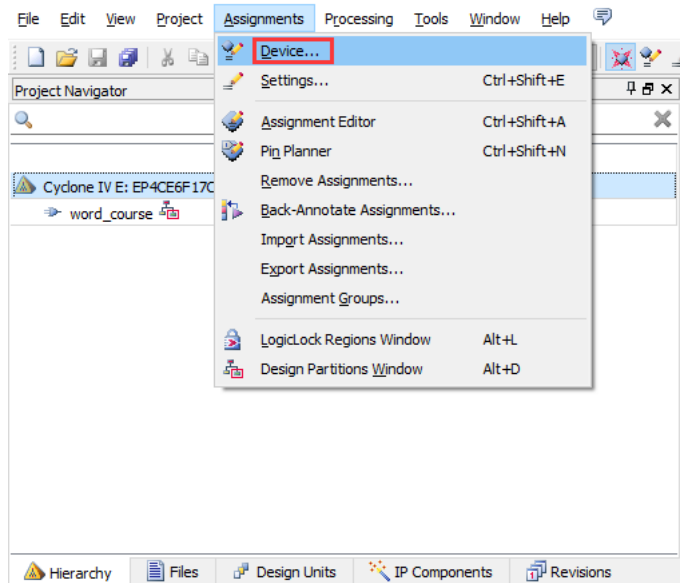


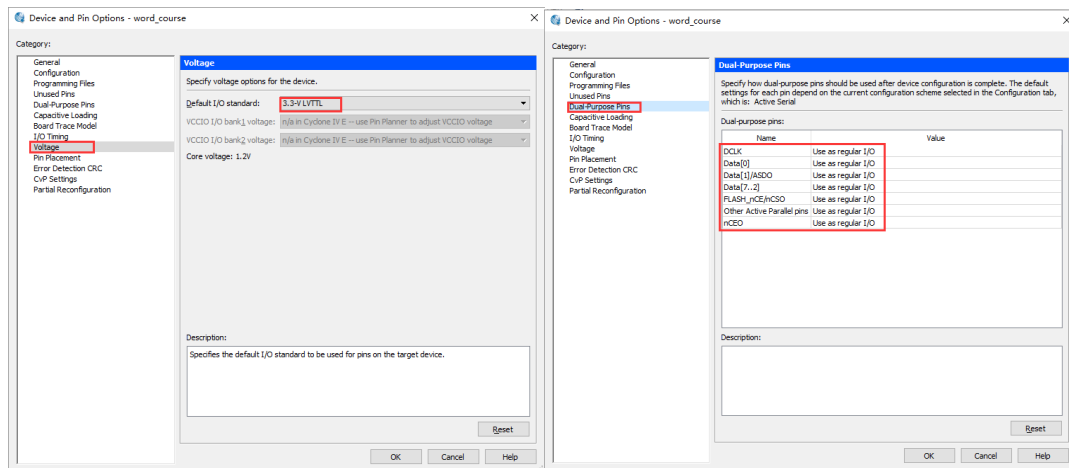
#### 5、工程设置



EP4CE6F17C8

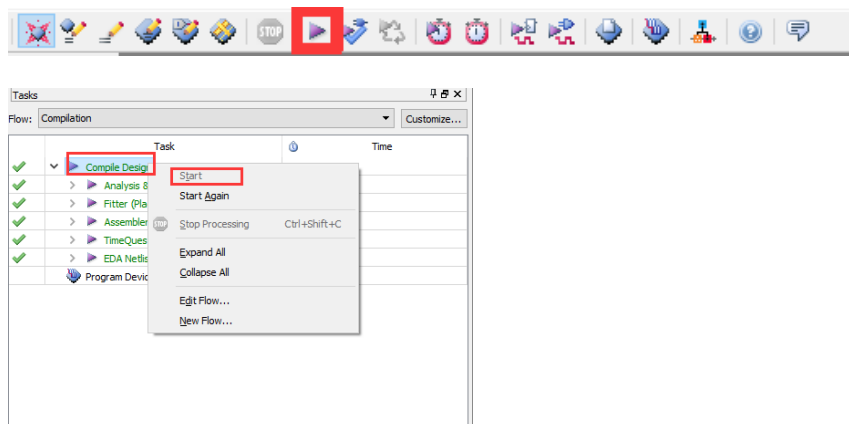
EPCS16





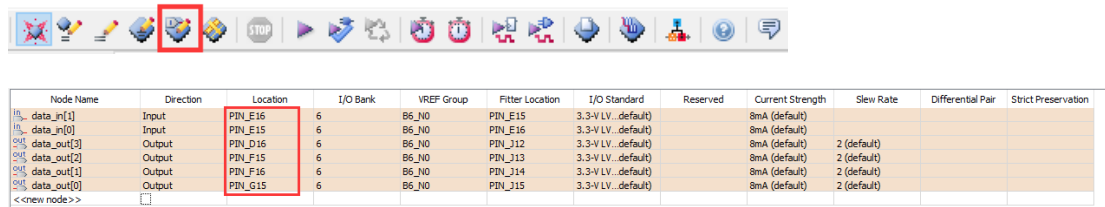
## 6、编译

### start Compilation



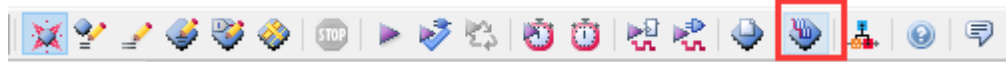
## 7、引脚分配

### Pin Planner



## 8、重新编译

## 9、下载到开发板



### Programmer

