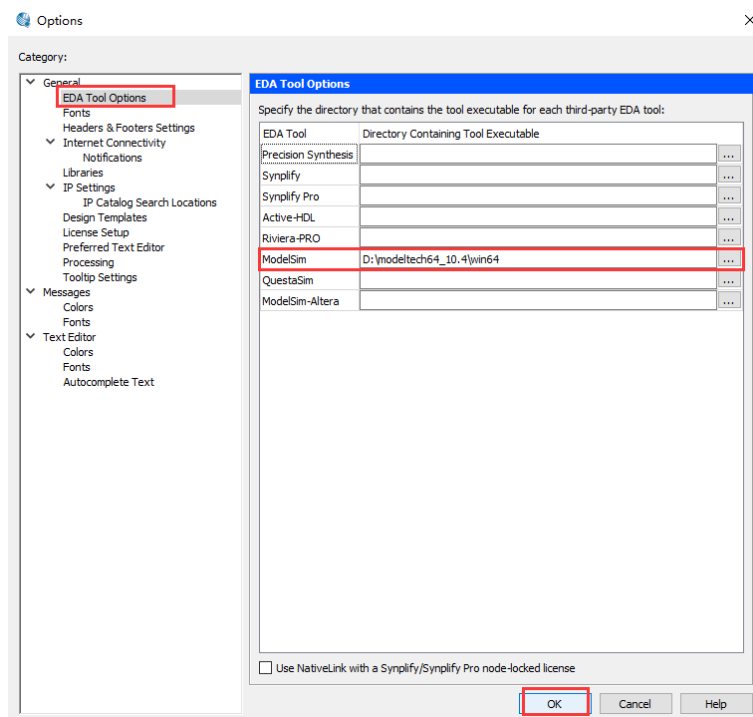
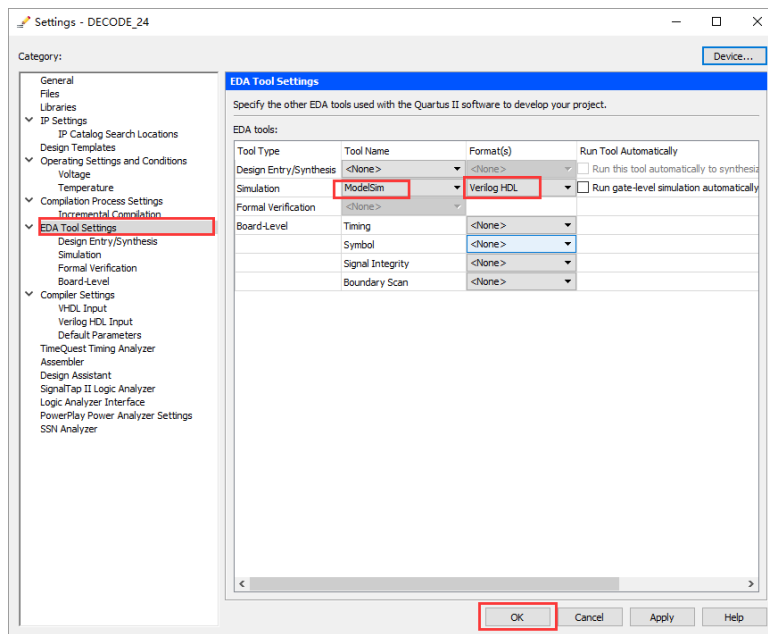


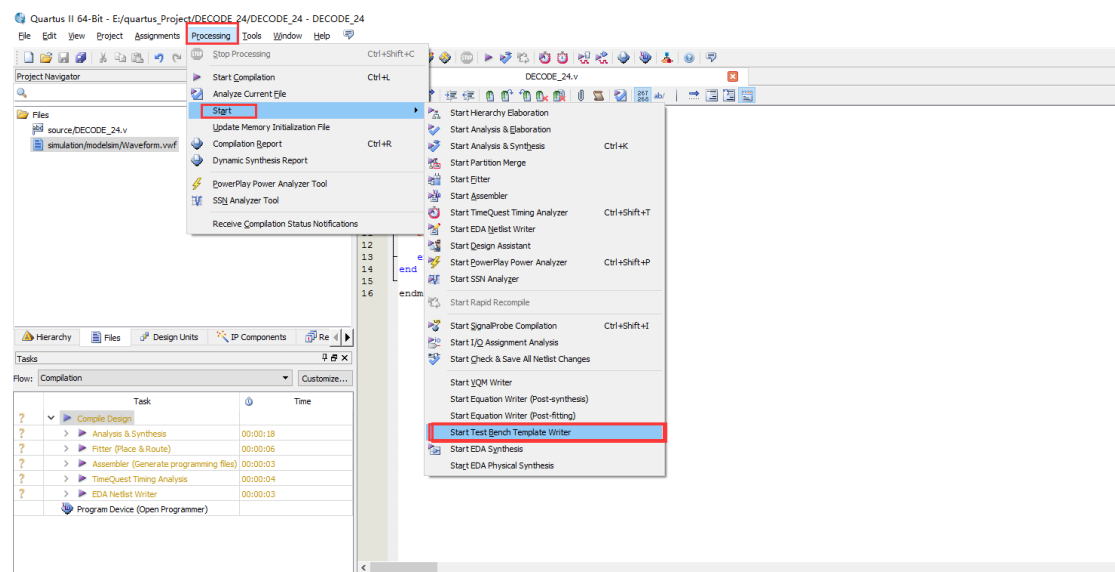
## 1、configuration



<http://www.cnblogs.com/emouse/archive/2012/07/08/2581223.html>

<http://www.cnblogs.com/Jezze/archive/2012/09/14/2684333.html>

## 2、生成 testbench writer 文件



Then we can see a file:

simulation->modelsim->\*\*\*.vt

## 3、编写 testbench 文件

## 4、仿真步骤

