컴파일러 최종과제

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1. 실험방법

- 한 학기동안 신택스분석과 시멘틱분석이 포함된 컴파일러를 만들어보았다. 이번 과제에서는 code generator를 추가하여 원시코드를 어셈블리코드로 작성하는 컴파일러를 완성할 것이다.
- 원시 프로그램은 신택스 분석과 시멘틱 분석 과정을 통하여 신택스 트리와 관련 테이블들로 번역되었다. 이런 신택스 트리를 중간 언어의 형태로 번역하고 프로그램의 실행 표율을 높이기 위해 중간 언어프로그램을 최적화하는 과정을 하는 것이 바람직하나, 편의상 생략하고 신택스 트리로부터 바로 기계어 코드를 생성하고자 하였다. 시멘틱 분석과정을 통하여 모든 선언문들의 성격을 분석하고 코드 생성에 필요한 주소나 크기들이 계산되었으며 명령문들이나수식이 올바르게 사용되고 있는지 분석하고 그 타입도 규칙에 따라 변환하여서 올바른 타입의수식이나 명령들로 변환되었다. 그러므로 코드 생성기는 신택스 트리의 루트노드에 연결되어 있는 선언문 목록을 차례로 탐사하면서 다음과 같은 일을 수행한다.
- * 함수 선언문인 경우에는 활성화 레코드를 할당하는 코드를 생성하고, 함수의 몸체 즉 복합문을 탐사하여 그 안에 나타나는 모든 명령문에 대한 코드를 생성한다.

```
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$ ls
code_generator.c lex.yy.c print_syn.c syntax
                                                print_syn.c syntax.c semantic.c test_code
code_generator.c
                                                                                      v.tab.c
kim.ī
                            main.c
                                                                     test_code
                                                                                      y.tab.h
kim.y print_sem.c simulator type.h
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$ gcc -o a.exe code_generator.c lex.yy.c y.tab.c m
kim.y
ain.c semantic.c print_sem.c print_sem.c syntax.c
code_generator.c: In function 'gen_program':
code_generator.c: In function 'gen_program':
code_generator.c:58:25: warning: passing argument 1 of 'gen_declaration_list' from incompatible
pointer type [-Wincompatible-pointer-types]
gen_declaration_list(node->clink);
code_generator.c:15:6: note: expected 'A_ID * {aka struct s_id *}' but argument is of type 'stru
ct s_node *'
void gen_declaration_list (A_ID *);
code_generator.c: In function 'gen_expression':
code_generator.c:72:6: warning: assignment from incompatible pointer type [-Wincompatible-pointe
   -types]
     id = node->clink;
code_generator.c:98:24: warning: passing argument 3 of 'gen_code_i' makes integer from pointer w
ithout a cast [-Wint-conversion]
   gen_code_i(LITI, 0, id->init);
code_generator.c:17:6: note: expected 'int' but argument is of type 'A_NODE * {aka struct s_node
 void gen_code_i(OPCODE,int,int);
```

(생략)

- code generator에서는 초기화구문, switch문, structure와 관련된 부분은 만들지 않았다. 따라서 예제에서는 switch나 structure가 포함되어 있지 않는 코드들을 가지고 실험을 해볼 것이다.
- 주어진 어셈블러/인터프리터 프로그램을 실행하기 위해 다음과 같은 준비를 하였다. yacc을 이용하여 y.tab.c와 y.tab.h 생성한 후에(\$ yacc -d interp.y) lex를 이용하여 lex.yy.c 생성한다(\$ lex interp.l). 그 후 실행파일인 interp.exe를 만들었다. (\$ gcc -o interp.exe y.tab.c lex.yy.c interp.c lib.c) 이 실행파일을 이용하여 완성한 컴파일러를 통해 생성한 어셈블리 코드를 실행시켜 본다.

```
minjeong@minjeong-VirtualBox:~/mjeong/hw_final/simulator$ interp.c interp.l interp.y lib.c type.h
minjeong@minjeong-VirtualBox:~/mjeong/hw_final/simulator$ yacc -d interp.y minjeong@minjeong-VirtualBox:~/mjeong/hw_final/simulator$ ls interp.c interp.l interp.y lib.c type.h y.tab.c y.tab.h minjeong@minjeong-VirtualBox:~/mjeong/hw_final/simulator$ lex interp.l
minjeong@minjeong-VirtualBox:~/mjeong/hw_final/simulator$ ls
interp.c interp.l interp.y lex.yy.c lib.c type.h y.tab.c y.tab.h
minjeong@minjeong-VirtualBox:~/mjeong/hw_final/simulator$ gcc -o interp.exe y.ta
b.c lex.yy.c interp.c lib.c
y.tab.c: In function 'yyparse':
y.tab.c:1191:16: warning: implicit declaration of function 'yylex' [-Wimplicit-f
unction-declaration]
          yychar = yylex ();
y.tab.c:1362:7: warning: implicit declaration of function 'yyerror' [-Wimplicit-
function-declaration]
yyerror (YY_("syntax error"));
interp.y: In function 'initialize':
interp.y:204:9: warning: assignment from incompatible pointer type [-Wincompatib
le-pointer-types]
   stack_f=stack;
interp.y:205:9: warning: assignment from incompatible pointer type [-Wincompatib
le-pointer-types]
   stack_c=stack;
```

(생략)

- 2. 실험 내용
- 1) test1.c
- 이름과 나이, 키를 출력하는 프로그램이다.

```
minjeong@minjeong-VirtualBox:~/mjeong/hw_final/simulator$ ./interp.exe a.asm
====== symbol ======
      printf
   1:
       malloc
                - 3
   3:
       scanf
   4:
      main
                19
   5:
       printAge 3
       printHeight
   6:
                         11
====== code =======
                0,72
   0:
       INT
                0,19
       SUP
   1:
       RET
   3:
       INT
                0,16
   4:
       INT
                0,12
                0,12
   5:
       LDA
       LOD
                1,12
   6:
       POP
                0,5
   7:
                 0,-1
   8:
       ADDR
                0,0
  9:
       CAL
  10:
       RET
                0,0
 11:
       INT
                0,16
  12:
       INT
                 0,12
 13:
       LDA
                0,28
  14:
       LOD
                1,12
                0,5
 15:
       POP
 16:
       ADDR
                 0,-1
                0,0
 17:
       CAL
  18:
       RET
                 0,0
 19:
       INT
                 0,24
 20:
       LDA
                 1,12
       LDA
                0,44
 21:
 22:
       STX
                0,0
```

(생략)

```
47:
       CAL
                 0,0
                 1,-4
0,0
0,0
  48:
       LDA
 49:
       LITI
  50:
       STO
 51:
      RET
start execution
name : abc
age : 20
height : 159.000000
end execution
minjeong@minjeong-VirtualBox:~/mjeong/hw_final/simulator$
```

test1.c를 gcc로 실행하면 다음과 같은 결과가 나온다. 두 개가 동일한 결과라는 것을 확인할 수 있다.

```
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$ gcc -o gcc.exe test1.c
test1.c: In function 'printAge':
test1.c:3:2: warning: implicit declaration of function 'printf' [-Wimplicit-function-declaration]
    printf("age: %d\n", age);

test1.c:3:2: warning: incompatible implicit declaration of built-in function 'printf'
test1.c:3:2: mote: include '<stdio.h>' or provide a declaration of 'printf'
test1.c: In function 'printHeight':
test1.c:8:2: warning: incompatible implicit declaration of built-in function 'printf'
printf("height: %f\n", height);

**
test1.c:8:2: note: include '<stdio.h>' or provide a declaration of 'printf'
test1.c: In function 'main':
test1.c:21:2: warning: incompatible implicit declaration of built-in function 'printf'
printf("name: %s\n", name);

**
test1.c:21:2: note: include '<stdio.h>' or provide a declaration of 'printf'
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$ ./gcc.exe
name: abc
age: 20
height: 159.000000
```

2) test2.c

● 컴파일러를 통하여 만들어진 어셈블러 프로그램은 다음과 같다.

(생략)

```
166 MULI 0, 0
167 SUBI 0, 0
168 STX 0, 0
169 POP 0, 1
170 LDA 1, -4
171 LITI 0, 0
172 STO 0, 0
173 RET 0, 0
174 .literal 12 "50000won => %d\n"
175 .literal 32 "10000won => %d\n"
176 .literal 52 "5000won => %d\n"
177 .literal 72 "1000won => %d\n"
178 .literal 92 "500won => %d\n"
179 .literal 112 "100won => %d\n"
179 .literal 12 "500won => %d\n"
179 .literal 12 "500won => %d\n"
179 .literal 12 "500won => %d\n"
180 .literal 12 "50won => %d\n"
181 .literal 148 "10won => %d\n"
```

```
minjeong@minjeong-VirtualBox:~/mjeong/hw_final/simulator$ ./interp.exe a.asm
====== symbol ======<u>==</u>
   1: printf
   2:
      malloc
                -2
                -3
   3: scanf
   4:
      main
                3
  ===== code =======
  0: INT
                0,164
      SUP
   1:
                0,3
      RET
                0,0
   2:
   3:
       INT
                0,48
      LDA
                1,12
   4:
                0,278970
   5:
      LITI
                0,0
   6:
      STX
   7:
       POP
                0,1
   8:
       LDA
                1,16
                1,12
   9:
       LOD
                0,50000
  10:
       LITI
  11:
       DIVI
                0,0
 12:
       STX
                0,0
 13:
       POP
                0,1
 14:
      INT
                0,12
```

(생략)

```
167:
       POP
                0,1
                1,-4
0,0
 168:
       LDA
       LITI
 169:
170:
      STO
                0,0
171:
      RET
                0,0
start execution
50000won => 5
10000won => 2
5000won => 1
1000won => 3
500won => 1
100won => 4
50won => 1
10won => 2
end execution
minjeong@minjeong-VirtualBox:~/mjeong/hw_final/simulator$
```

test2.c를 gcc로 실행하면 다음과 같은 결과가 나온다. 두 개가 동일한 결과라는 것을 확인할 수 있다.

3) test3.c

```
test3.c (-/mjeong/hw_final) - VIM

int main() {
    int sec, s, hour, minute, second;
    sec = 54321;
    s = sec;
    hour = sec / (60 * 60);
    sec = sec - hour * 60 * 60;

minute = sec / 60;
    sec = sec - minute * 60;

sec = sec - minute * 60;

return 0;

return 0;
```

● 컴파일러를 통하여 만들어진 어셈블러 프로그램은 다음과 같다.

(생략)

● 어셈블러 프로그램을 시뮬레이터를 이용하여 실행하면 다음과 같은 결과가 나온다.

```
minjeong@minjeong-VirtualBox:~/mjeong/hw_final/simulator$ ./interp.exe a.asm
====== symbol ======
  1: printf
      malloc
   2:
  3: scanf
                - 3
                3
   4: main
====== code =======
  0: INT
                0,44
   1: SUP
                0,3
   2: RET
                0,0
  3: INT
4: LDA
                0,32
                1,12
                0,54321
   5: LITI
   6:
     STX
                0,0
               0,1
1,16
   7:
      POP
      LDA
  8:
  9: LOD
                1,12
  10:
                0,0
      STX
                0,1
1,20
  11:
      POP
      LDA
  12:
  13:
      LOD
                1,12
  14:
      LITI
                0,60
                0,60
  15:
      LITI
      MULI
  16:
                0,0
      DIVI
  17:
                0,0
  18:
       STX
                0,0
                0,1
  19:
      POP
```

(생략)

```
58: LITI 0,0
59: STO 0,0
60: RET 0,0
start execution
54321sec is 15:5:21.
end execution
minicong@minicong-VirtualBox:~/micong/hw final/simulatorS
```

test3.c를 gcc로 실행하면 다음과 같은 결과가 나온다. 두 개가 동일한 결과라는 것을 확인할 수 있다.

4) test4.c

윤년인지 평년인지 판단하는 프로그램이다.

```
test4.c (~/mjeong/hw_final) - VIM
```

```
a.asm (~/mjeong/hw_final) - VIM
                                                    INT
SUP
                                                                                                                                                                                                                                                           80
81
82
83
84
85
86
87
88
90
91
92
93
94
95
96
97
98
99
                                                                                                                                                                                                                                                                                                      POP
LDA
INT
LOD
POP
ADDR
CAL
LOD
LOD
POP
ADDR
CAL
LOD
LOD
POP
ADDR
CAL
LITI
STO
RET
12
36
                                                                           0, 60
0, main
0, 0
                                                                                                                                       40
41 L5:
42
43
44
45
46 L6:
47 L4:
48 L2:
49
50
51
52
53 output:
54
55
66
61
62
63
64
65
66
67
77
78
88:
77
78
                                                    RET
                                                                                                                                                                                      LDA
LITI
4 yea 5 6 7 8 9 9 10 11 12 13 14 15 16 17 L1: 18 19 20 21 22 23 24 25 26 27 28 29 L3: 33 34 35 36 37 38
          yearCheck:
                                                                                                                                                                                          STX
POP
                                                                                                                                                                                                                                                                                                                                             yearCheck
                                               LOD
                                               MOD
LITI
NEQI
                                                                                                                                                                                                                                                                                                                                  0, 0
0, 1
0, 12
1, 12
1, 16
0, 5
0, output
0, 0
                                                                                                                                                                                          LDA
LOD
STO
RET
                                                  JPC
LDA
                                               LITI
STX
POP
JMP
                                                                                                                                                                                       INT
LOD
LITI
                                                                                                                                                                                      EQLI
JPC
INT
LDA
LOD
POP
ADDR
                                                                                                                                                                                                                                                                                                                                  0, 0
"%d is common year.\n"
"%d is leap year.\n"
                                               LOD
LITI
MOD
LITI
                                                                          1, 12
0, 100
0, 0
0, 0
0, L3
1, 16
0, 1
0, 0
0, L4
                                                                                                                                                                                                                                                                         .literal
                                                                                                                                                                                                                0, L7
0, 12
0, 12
1, 12
0, 5
0, printf
0, 0
0, L8
                                               NEQI
JPC
LDA
LITI
STX
POP
                                                                                                                                                                                         CAL
                                                                                                                                                                                       INT
LDA
LOD
POP
ADDR
                                                                           1, 12
0, 400
0, 0
0, 0
0, 0
0, L5
1, 16
0, 0
0, 0
                                                  LOD
                                               LITI
MOD
LITI
NEQI
JPC
LDA
                                                                                                                                                                                                                  0, printf
0. 0
                                                                                                                                                                                          CAL
                                                                                                                                                                                          INT
                                                                                                                                                                                       LDA
```

```
0,37
1,16
0,0
0,0
                                                                                                   0,0
0,0
0,0
         symbol
                                                                                 84.
                                                                                        LITI
                                               LDA
                                                                                        STO
      printf
                                          32:
                                                                                 85:
                                          33:
                                               LITI
      malloc
                                                                                 86:
                                                                                      RET
 2:
                   -2
                                          34:
      scanf
                   - 3
                                                                              start execution
                                               POP
JMP
                                          35:
                                                          0,1
                                                                               1749 is common year.
 4:
      main
                  64
                                                          0,41
                                          36:
      yearCheck
L1
                                                                              end execution
 5:
                             3
                                                          1,16
0,1
                                          37:
                                               LDA
                  15
 6:
                                          38:
                                               LITI
      L2
                  41
 7:
                                               STX
                                                          0,0
      L3
 8:
                  26
                                          40:
                                               POP
                                                          0,1
                                                          1,-4
1,16
 9:
      L4
                  41
                                         41:
42:
                                               LDA
                                               LOD
10:
      L5
                  37
                                          43:
                                                STO
                                                          0,0
11:
      L6
                  41
                                          44:
                                               RET
                                                          0,0
                  45
12:
      output
                                         45:
                                               INT
                                                          0,20
                  57
13:
      17
                                               LOD
                                                          1,16
                                          46:
14:
      L8
                  63
                                          47:
                                               LITI
                                               EQLI
JPC
INT
      ==
         code
                 ------
                                          48:
                                                          0,0
      INT
                                                          0,57
 0:
                  0,60
                                         49:
                                                          0,12
0,12
                  0,64
                                          50:
      SUP
                                               LDA
 2:
      RET
                                               LOD
                  0,20
1,12
      INT
 3:
                                               POP
                                                          0,5
 4:
      LOD
                                                          0,-1
0,0
                                          54:
                                               ADDR
                  0,4
 5:
      LITI
                                               CAL
 6:
      MOD
                                          56:
                                                JMP
                                                          0,63
      LITI
                  0,0
                                               INT
                                               LDA
LOD
 8:
      NEQI
                  0,0
                                          58:
                                                          0,36
                                                          1,12
0,5
                                          59:
 9:
      JPC
                  0,15
                                               POP
                                         60:
                  1,16
10:
      LDA
                                                          0,-1
                                               ADDR
      LITI
11:
                                               CAL
                  0,0
12:
      STX
                                         63:
                                               RET
                                                          0,0
      POP
13:
                  0,1
                                                          0,20
                                               INT
                                         64:
                  0,41
1,12
14:
      JMP
                                          65:
                                               LDA
                                                          0,1749
0,0
0,1
1,16
0,16
                                               LITI
STX
POP
15:
      LOD
                                          66:
16:
      LITI
                  0,100
                  0,0
17:
      MOD
                                         68:
                                               LDA
                                          69:
18:
      LITI
                                          70:
                                                INT
                  0,0
0,26
      NEQI
19:
                                               LOD
                                                          1,12
20:
      JPC
                                         72:
73:
74:
                                                          0,4
0,3
0,0
                                               POP
21:
      LDA
                  1,16
                                               ADDR
22:
      LITI
                  0,1
23:
      STX
                  0,0
                                                          0,0
                  0,1
0,41
                                               POP
24:
      POP
                                                          0,1
                                                          0,12
1,12
1,16
                                         77:
78:
                                               INT
25:
      JMP
                                               LOD
                  1,12
26:
      LOD
                                                LOD
                  0,400
      LITI
27:
                                          80:
                                               POP
                                                          0,5
                  0,0
28:
      MOD
                                               ADDR
                                         81:
                                                          0,45
      LITI
29:
                                                          0,0
                                               CAL
                                          82:
30:
      NEQI
                  0,0
                                          83:
                                               LDA
```

test4.c를 gcc로 실행하면 다음과 같은 결과가 나온다. 두 개가 동일한 결과라는 것을 확인할 수 있다.

```
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$ gcc -o gcc.exe test4.c
test4.c: In function 'output':
test4.c:25:3: warning: implicit declaration of function 'printf' [-Wimplicit-function-declaration]
    printf("%d is common year.\n", year);

at at a c:25:3: warning: incompatible implicit declaration of built-in function 'printf'
test4.c:25:3: note: include '<stdio.h>' or provide a declaration of 'printf'
test4.c:28:3: warning: incompatible implicit declaration of built-in function 'printf'
    printf("%d is leap year.\n", year);

at a common year. include '<stdio.h>' or provide a declaration of 'printf'
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$ ./gcc.exe
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$
```

5) test5.c

```
🔊 🗐 📵 a.asm (~/mjeong/hw_final) - VIM
                                                                                         0, 0
                                                             50
                                                                                STO
                             0, 32
0, main
                    INT
                                                              51
                                                                                RET
 2
                    SUP
                                                             52 .literal
53 .literal
54 .literal
                                                                                        "\n"
"\n"
                    RET
 4 main:
                    INT
                            1, 16
0, 6
0, 0
                    LDA
 6
7
8
                  LITI
                    STX
 9
                    POP
10 L2:
11
12
13
14
15
                             1, 12
1, 16
                    LOD
                    LOD
                  LEQI
                    JPC
                             0, L3
                    INT
16
                    LDA
17
18
                             0, 4
0, printf
                   POP
                  ADDR
19
20
21
22
23
24
25
26
27
28
                   CAL
                    LOD
                  LITI
                   MOD
                  LITI
                             0, 0
0, L4
                  EOLI
                    JPC
                             0, 12
0, 16
0, 4
                    INT
                    LDA
                    POP
29
                  ADDR
                             0, printf
30
31
                             0, 0
0, L5
                    CAL
                    JMP
32 L4:
33 L5:
34
                    LOD
35
36
37
38
                    LDA
                    LDX
                  INCI
                    STO
39
                    POP
40 L1:
                    JMP
                             0, L2
42 L3:
                             0, 12
0, 24
0, 4
43
44
45
46
                    INT
                    LDA
                    POP
                  ADDR
                             0, printf
47
                    CAL
48
                    LDA
                                  -4
```

```
symbol =======
                                  32:
                                        INCI
                                                   0,0
     printf
               -1
                                  33:
                                        STO
                                                   0,0
     malloc
               -2
 2:
                                  34:
                                        POP
                                                   0,1
               - 3
 3:
     scanf
                                  35:
                                        JMP
                                                   0,8
               3
 4:
     main
                                  36:
                                        INT
                                                   0,12
 5:
     12
               8
                                  37:
                                        LDA
                                                   0,24
 6:
     L3
               36
                                  38:
                                        POP
                                                   0,4
               29
 7:
     L4
                                  39:
                                        ADDR
                                                   0,-1
 8:
     L5
               29
                                  40:
                                        CAL
                                                   0,0
               35
 9:
     L1
        code
                                  41:
                                        LDA
                                                   1,-4
              _____
               0,32
0:
     INT
                                  42:
                                        LITI
                                                   0,0
     SUP
               0,3
                                  43:
                                        STO
                                                   0,0
 2:
     RET
               0,0
                                  44:
                                        RET
                                                   0,0
 3:
     INT
               0,20
                                start execution
 4:
     LDA
               1,16
 5:
     LITI
               0,6
                                ****
               0,0
 6:
     STX
 7:
     POP
               0,1
                                end execution
 8:
     LOD
               1,12
     LOD
9:
               1,16
               0,0
10:
     LEQI
11:
     JPC
               0,36
               0,12
12:
     INT
               0,12
13:
     IDA
14:
     POP
               0,4
     ADDR
15:
               0,-1
16:
     CAL
               0,0
     LOD
17:
               1,12
18:
     LITI
               0,5
19:
     MOD
               0,0
     LITI
20:
               0,0
21:
     EOLI
               0,0
22:
     JPC
               0,29
23:
     INT
               0,12
     LDA
24:
               0,16
25:
     POP
               0,4
     ADDR
26:
               0,-1
               0,0
     CAL
27:
     JMP
               0,29
28:
29:
     LOD
               1,12
     LDA
30:
               1,12
31:
     LDX
               0,0
```

test5.c를 gcc로 실행하면 다음과 같은 결과가 나온다. 예상과 달리 시뮬레이터를 이용하여 실행한 결과가 이상하게 출력되었다. 그 원인에 대하여 고찰이 필요할 것같다.

```
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$ gcc -o gcc.exe test5.c
test5.c: In function 'main':
test5.c:6:3: warning: implicit declaration of function 'printf' [-Wimplicit-function-declaration]
    printf("*");
    ^
test5.c:6:3: warning: incompatible implicit declaration of built-in function 'printf'
test5.c:6:3: note: include '<stdio.h>' or provide a declaration of 'printf'
test5.c:15:2: warning: incompatible implicit declaration of built-in function 'printf'
printf("\n");
    ^
test5.c:15:2: note: include '<stdio.h>' or provide a declaration of 'printf'
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$ ./gcc.exe
******
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$
```

6) test6.c

1~100까지의 숫자를 출력할 때 3의 배수는 '*'로, 5의 배수는 '#'으로 출력하고 3과 5의 공배수는 정상 숫자로 출력하는 프로그램이다.

```
a.asm (~/mjeong/hw_final) - VIM
                                                                                                                                                                                                                                                                                                                                                                  LITI
ADDI
STX
POP
JMP
                                                                                                                                                                                                                          LITI
EQLI
JPC
INT
LDA
POP
ADDR
                                                                                                                                                                       100
101
102
103
104 L3:
1 2 3 4 main: 5 6 6 7 8 9 9 10 L2: 11 12 13 14 15 16 17 18 L5: 20 21 22 23 24 25 26 27 28 29 30 31 32 24 25 26 27 28 29 30 31 32 44 45 46 L8: 47 48
                                                                                                                                                                                                                                                                  0
L10
                                                                                                 main
0
                                                              SUP
RET
                                                                                                                                                                                                                                                                  12
20
printf
                                                                                                                                                                                                                                                                                                                                                                LDA
LITI
STO
RET
12
20
28
36
44
                                                                                                                                                                                                                                                                                                                                                                                           1, -4
0, 0
0, 0
"%d\t"
"*\t"
"#\t"
"%d\t"
                                                          INT
LDA
LITI
STX
POP
                                                                                                                                                                                                                             CAL
                                                                                                                                                                                                                                                                                                                            .literal
.literal
.literal
.literal
.literal
                                                                                                                                                                                                                              LOD
                                                          LOD
LITI
LSSI
JPC
LDA
LOD
STX
POP
                                                                                                                                                                                                                          LITI
MOD
LITI
EQLI
JPC
INT
LDA
POP
ADDR
                                                                                                                                                                                                                                                      0, 12
0, 28
0, 4
0, printf
0, 0
0, L13
                                                                                      1, 16
1, 12
0, 9
0, 0
0, L6
1, 16
0, 0
0, 0
0, L7
1, 16
0, 0
0, 0
                                                         LOD
LOD
LITI
ADDI
LEQI
JPC
LITI
MOD
LITI
EQLI
MOD
LITI
MOD
LITI
MOD
LITI
EQLI
                                                                                                                                                                                                                             CAL
JMP
                                                                                                                                                                                                                          INT
LDA
LOD
POP
ADDR
                                                                                                                                                                                                                                                       0, 5
0, printf
0, 0
                                                                                                                                                                                                                          LOD
LDA
LDX
INCI
STO
POP
JMP
                                                          JPC
INT
LDA
LOD
POP
ADDR
                                                                                      1, 16
0, 5
0, printf
0, 0
0, L9
                                                                                                                                                                                                                          INT
LDA
POP
ADDR
                                                               CAL
                                                               JMP
                                                                                                                                                                                                                             CAL
                                                          LOD
LITI
                                                                                                                                                                                                                             LDA
LOD
```

```
printf
malloc
scanf
main
L2
L3
L5
L6
L7
L8
L9
L10
L11
L12
L13
 1:
2:
3:
4:
5:
6:
7:
8:
9:
10:
11:
12:
13:
14:
15:
16:
17:
                                                                                                               -1
-2
-3
8
90
16
78
33
41
71
53
71
65
71
71
83
                                                                                                                                                                                                                                                                                                                                                                                                                  0,5
0,-1
1,16
1,16
0,0
0,0
0,1
0,16
0,44
0,-1
1,12
0,0
0,0
0,1
1,12
0,0
0,0
0,1
                                                                                                                                                                                                                                                                                                         69:
70:
71:
72:
73:
74:
75:
76:
778:
80:
81:
82:
83:
84:
85:
86:
89:
91:
92:
                                                                                                                                                                                                                                                                                                                                       CAL
LOD
LDA
INCI
STO
POP
JMP
INT
LDA
POP
ADDR
CAL
LDA
LOD
LITI
ADDI
STX
POP
JMP
LDA
LOD
LITI
ADDI
STX
POP
JMP
LDA
LITI
ADDI
STX
POP
JMP
LDA
LITI
STO
RET
execution
                                        L4
L1
                                                                                                            0:
1:
2:
3:
4:
5:
6:
7:
8:
9:
10:
11:
12:
13:
14:
15:
16:
17:
18:
20:
21:
22:
23:
24:
25:
25:
26:
                                        INT
SUP
RET
INT
LDA
LITI
STX
POP
LOD
LITI
LSSI
JPC
LDA
LOD
STX
POP
LOD
                                                                                                                                                                                                                                                                                                start
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         #
30
#
60
#
90
                                                                                                                                                                                                                                                                                                                                                                                                                                                                               4
14
*
34
44
*
64
74
*
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              # 15
# # 45
# 75
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        16
26
*
                                                                                                                                                                                                                                                                                                                                                                                                                    13
23
*
43
53
*
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              19
29
*
49
59
*
                                                                                                                                                                                                                                                                                                                                                        22
32
*
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  28
38
*
58
68
*
                                        LOD
LITI
ADDI
LEQI
JPC
LOD
LITI
MOD
LITI
EQLI
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      37
47
*
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        46
56
*
                                                                                                                                                                                                                                    (생략)<sup>41</sup>
                                                                                                                                                                                                                                                                                                                                                        52
62
*
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   67
77
                                                                                                                                                                                                                                                                                              61
71
                                                                                                                                                                                                                                                                                                                                                                                                                     73
83
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         76
86
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               79
89
                                                                                                                                                                                                                                                                                                                                                        82
92
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  94
                                                                                                                                                                                                                                                                                             91
```

test6.c를 gcc로 실행하면 다음과 같은 결과가 나온다. 두 개가 동일한 결과라는 것을 확인할 수 있다.

```
minjeong@minjeong-VirtualBox:~/mjeong/hw_finalS gcc -o gcc.exe test6.c
test6.c: In function 'main':
test6.c:7:5: warning: implicit declaration of function 'printf' [-Wimplicit-function-declaration of function 'printf' [-Wimplicit-function of function 'printf' [-Wimplicit-function of function 'printf' [-Wimplicit-function of function of
                       printf("%d\t", j);
test6.c:7:5: warning: incompatible implicit declaration of built-in function 'printf'
test6.c:7:5: note: include '<stdio.h>' or provide a declaration of 'printf'
test6.c:10:5: warning: incompatible implicit declaration of built-in function 'printf'
    printf("*\t");
test6.c:10:5: note: include '<stdio.h>' or provide a declaration of 'printf'
test6.c:13:5: warning: incompatible implicit declaration of built-in function 'printf'
printf("#\t");
test6.c:13:5: note: include '<stdio.h>' or provide a declaration of 'printf'
test6.c:16:5: warning: incompatible implicit declaration of built-in function 'printf'
printf("%d\t", j);
test6.c:16:5: note: include '<stdio.h>' or provide a declaration of 'printf'
test6.c:19:3: warning: incompatible implicit declaration of built-in function 'printf'
printf("\n");
#
#
30
                                                                                                                                                  15
#
                                                                         13
23
*
                                                                                                              14
*
11
*
                                                                                                                                                                                                                              17
*
                                                                                                                                                                                                                                                                                                      19
                                    22
32
*
                                                                                                                                                                                        26
                                                                                                                                                                                                                                                                 28
                                                                                                                                                                                                                                                                                                      29
                                                                                                                                                                                                                                                                                                                                          #
#
60
 31
                                                                                                                                                                                                                                                                   38
                                                                                                              34
                                                                                                                                                                                                                              37
                                                                         43
53
*
41
*
                                                                                                              44
                                                                                                                                                                                                                             47
*
                                                                                                                                                                                        46
                                                                                                                                                                                                                                                                                                      49
                                    52
62
*
                                                                                                                                                  #
                                                                                                                                                                                        56
*
                                                                                                                                                                                                                                                                  58
                                                                                                                                                                                                                                                                                                      59
*
61
71
*
                                                                                                                                                                                                                                                                                                                                          #
#
90
                                                                                                                                                                                                                                                                  68
                                                                                                              64
                                                                                                                                                                                                                              67
                                                                         73
83
*
                                                                                                              74
*
                                                                                                                                                                                                                              77
*
                                                                                                                                                                                                                                                                                                       79
                                    82
92
                                                                                                                                                   #
                                                                                                                                                                                        86
                                                                                                                                                                                                                                                                 88
98
                                                                                                                                                                                                                                                                                                      89
 91
                            ong@minjeong-VirtualBox
                                                                                                                                           /mjeong/hw_final
```

7) test7.c

0부터 49까지의 숫자를 10개단위로 나눠서 출력하는 프로그램이다.

```
test7.c (~/mjeong/hw_final) - VIM

int main(){
   int i, j;
   for (i = 0; i < 10; i++){
      for (j = i; j < 50; j = j+10){
        printf("%d ", j);
      }
   printf("\n");
   }

return 0;
</pre>
```

● 컴파일러를 통하여 만들어진 어셈블러 프로그램은 다음과 같다.

```
🔊 🗐 📵 a.asm (~/mjeong/hw_final) - VIM
                                               45
                                                               LOD
                 INT
                                               46
                                                               LDA
                 SUP
                         0, main
                                               47
                                                               LDX
                 RET
                                               48
                                                              INCI
 4 main:
5
                                               49
                                                               STO
                 INT
                                               50
                                                                POP
                         1, 12
 6
7
                 LDA
                                                                       0, L2
                                               51
                                                                JMP
                LITI
                                                   L3:
                 STX
                                               53
                                                               LDA
 9
                 POP
                                               54
                                                              LITI
10
   L2:
                                               55
                                                                STO
                LOD
                                                                       0, 0
"%d "
                                               56
                                                                RET
12
               LITI
                                               57 .literal
13
                                                                       "\n"
                LSSI
                                               58 .literal
14
15
                 JPC
                         0, L3
                        1, 16
1, 12
0, 0
                 LDA
16
                 LOD
17
                 STX
18
                 POP
19 L5:
20
21
22
23
24
25
26
                        1, 16
0, 50
                 LOD
               LITI
                LSSI
                         0, L6
                 JPC
                        0, 12
                 INT
                        0, 12
                 LDA
                         1, 16
0, 5
                 LOD
27
28
                 POP
                ADDR
                         0, printf
29
                 CAL
30 L4:
31
                         1, 16
1, 16
                 LDA
32
                LOD
33
                LITI
34
                ADDI
35
36
                 STX
                         0, 1
0, L5
                 POP
37
                 JMP
38
39
                 INT
                        0, 20
0, 4
0, printf
40
                 LDA
41
                 POP
42
                ADDR
43
                 CAL
```

● 어셈블러 프로그램을 시뮬레이터를 이용하여 실행하면 다음과 같은 결과가 나온다.

```
====== symbol =======
1: printf -1
2: malloc -2
                                                                   0,1
                                                     JMP
                                                                  0,16
                                              32:
                                              33:
                                                     INT
                                                                  0,12
                 - 3
3
8
       scanf
                                              34:
                                                     LDA
                                                                  0,20
 4:
5:
      main
L2
L3
                                                     POP
                                              35:
                                                                  0,4
 6:
7:
                  45
16
                                              36:
                                                     ADDR
                                                                  0,-1
                                                                  0,0
                                             37:
                                                     CAL
       L6
                  33
                                                                  1,12
                                              38:
                                                     LOD
                  26
38
       L4
                                              39:
                                                     LDA
                                                                  1,12
10:
                                                     LDX
                                             40:
                                                                  0,0
      ==
INT
          code
 ---
                 _____
                  0,28
                                             41:
                                                     INCI
                                                                  0,0
 0:
                  0,3
 1:
2:
3:
4:
5:
6:
7:
       SUP
                                                                  0,0
                                             42:
                                                     ST0
       RET
                                             43:
                                                     POP
                                                                  0,1
                  0,20
1,12
       INT
                                              44:
                                                     JMP
                                                                   0,8
      LDA
                                                                  1,-4
                                             45:
                                                     LDA
                  0,0
0,0
0,1
1,12
      LITI
                                             46:
                                                     LITI
       POP
                                              47:
                                                     STO
                                                                  0,0
       LOD
                                             48:
                                                    RET
                                                                  0,0
      LITI
 9:
                  0,10
                                           start
                                                   execution
 10:
                  0,0
                                              10
                 0,45
1,16
1,12
0,0
                                                    20 30
                                                                 40
      JPC
                                          1
2
                                                     21
22
                                                                 41
                                               11
                                                           31
       LDA
 12:
       LOD
                                               12
                                                           32
                                                                 42
                                                     23
                                                                 43
                                          3
4
5
                                               13
                                                           33
                  0,1
1,16
0,50
       POP
                                                     24
                                                                 44
                                               14
                                                           34
 16:
       LOD
                                               15
                                                     25
                                                           35
                                                                 45
      LITI
17:
                                          6
7
                                                     26
                                               16
                                                           36
                                                                 46
                 0,0
0,33
18:
      JPC
INT
                                                     27
                                                                 47
 19:
                                               17
                                                           37
                  0,12
20:
                                          8
                                               18
                                                     28
                                                           38
                                                                 48
21:
22:
23:
24:
       LDA
                                          9
                                               19
                                                     29
                                                                 49
                                                           39
       LOD
                  1,16
                                          end execution
                  0,5
      POP
                  0,-1
0,0
      ADDR
25:
       CAL
26:
       LDA
       LOD
                 0,10
0,0
0,0
28:
       LITI
29:
      ADDI
30:
      STX
```

test7.c를 gcc로 실행하면 다음과 같은 결과가 나온다. 두 개가 동일한 결과라는 것을 확인할 수 있다.

```
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$ gcc -o gcc.exe test7.c
test7.c: In function 'main':
test7.c:5:4: warning: implicit declaration of function 'printf' [-Wimplicit-function-declar
ation]
       printf("%d ", j);
test7.c:5:4: warning: incompatible implicit declaration of built-in function 'printf'
test7.c:5:4: note: include '<stdio.h>' or provide a declaration of 'printf'
test7.c:7:3: warning: incompatible implicit declaration of built-in function 'printf'
    printf("\n");
test7.c:7:3: note: include '<stdio.h>' or provide a declaration of 'printf'
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$ ./gcc.exe
             20
                     30
                            40
             21
22
                     31
                             41
      12
                     32
                             42
      13
             23
                     33
                             43
             24
      14
                             44
                     34
             25
26
27
                             45
      15
                     35
      16
                     36
                              46
      17
                     37
                              47
      18
             28
                     38
                              48
      19
             29
                     39
                              49
```

8) test8.c

삼중 for문을 이용하여 구구단을 출력한 프로그램이다.

```
test8.c (~/mjeong/hw_final) - VIM

int main(){
   int i, j, k;
   for (i = 2; i < 9; i = i+4){
      for (j = 1; j <= 9; j++){
        for (k = i; k-i < 4; k++){
            printf("%d * %d = %d\t", k, j, k*j);
      }
      printf("\n");
      }
      printf("\n");
}
</pre>
```

● 컴파일러를 통하여 만들어진 어셈블러 프로그램은 다음과 같다.

```
🦫 🖨 a.asm (~/mjeong/hw_final) - VI
                                                                                     1, 20
1, 20
0, 0
0, 0
                                                         46
                                                                            LOD
                                                         47
48
                                                                            LDA
 2
                      SUP
                               0, main
                                                                            LDX
                                                         48
49
50
51
52
53 L9:
54
                      RET
                                                                           INCI
  4 main:
                                                                            STO
                     INT
                                                                            POP
                               1, 12
                                                                            JMP
                                                                                     0, L8
 6
                     LDA
  7
                    LITI
                                                                            INT
 8
                     STX
                                                                            LDA
 9
                     POP
                                                         56
57
58
59 L4:
                                                                            POP
10 L2:
                                                                           ADDR
                                                                                     0, printf
11
12
13
14
15
                     LOD
                                                                            CAL
                               0, 9
                    LITI
                                                                                     1, 16
1, 16
0, 0
0, 0
                                                         60
                                                                            LOD
                    LSSI
                                                         61
62
63
                                                                            LDA
                                0, L3
                     JPC
                                                                            LDX
                     LDA
                                                                           INCI
16
17
                               0, 1
                    LITI
                                                         64
65
                                                                            STO
                      STX
                                                                                     0, 1
0, L5
                                                                            POP
18
                      POP
                                                         66
67 L6:
                                                                             JMP
18
19 L5:
20
21
22
23
24
25
26
27
28 L8:
                               1, 16
0, 9
                     LOD
                                                         68
                                                                            INT
                                                        69
70
71
72
73 L1:
74
75
76
77
78
79
                                                                            LDA
                    LITI
                               0, 0
                                                                            POP
                    LEQI
                               0, L6
1, 20
1, 12
0, 0
                                                                           ADDR
                                                                                      0, printf
                      JPC
                     LDA
                     LOD
                                                                            LDA
                     STX
                                                                            LOD
                     POP
                                                                           ADDI
29
30
                               1, 20
1, 12
                     LOD
                                                                                     0, 1
0, L2
                                                                            POP
                     LOD
                                                         80
                                                                             JMP
31
32
33
                    SUBI
                                                         81 L3:
                    LITI
                                                                                     0, 0
"%d * %d = %d\t"
"\n"
"\n"
                                                         82
                    LSSI
                                                             .literal
.literal
.literal
                                                                               12
32
34
                     JPC
                                0, L9
                                                         84
                               0, 12
0, 12
1, 20
35
36
                      INT
                     LDA
37
38
39
                     LOD
                               1, 16
                     LOD
                      LOD
40
                     LOD
41
42
                    MULI
                     POP
43
                    ADDR
                                0, printf
                     CAL
```

● 어셈블러 프로그램을 시뮬레이터를 이용하여 실행하면 다음과 같은 결과가 나온다.

```
30:
                                                                                                        INT
                                                                                                                                          0,12
                                                                                                                                                                  60: LDA
61: POP
62: ADDR
63: CAL
64: LDA
65: LOD
66: LITI
67: ADDI
68: STX
69: POP
70: JMP
71: RET
22 * 2 = 4
2 * 3 = 6
2 * 4 = 8
2 * 5 = 10
2 * 6 = 12
2 * 7 = 14
2 * 8 = 16
2 * 9 = 18
                                                                                                       IDA
                                                                                                                                          0,12
                                                                                    31:
              malloc
scanf
2:
3:
4:
5:
6:
7:
8:
9:
10:
11:
12:
                                                                                                                                           1,20
                                                                                    32:
                                                                                                        LOD
                                          3
8
71
16
59
24
47
40
52
              main
L2
L3
L5
L6
L8
L9
L7
L4
L1
                                                                                    33:
                                                                                                       LOD
                                                                                                                                           1,16
                                                                                    34:
                                                                                                       LOD
                                                                                                                                           1,20
                                                                                                                                          1,16
0,0
                                                                                    35:
                                                                                                       LOD
                                                                                    36:
                                                                                                       MULI
                                                                                    37:
                                                                                                       POP
                                                                                                                                          0,7
                                                                                                                                          0,-1
0,0
                                                                                                       ADDR
                                                                                    38:
                                                                                                                                                                                                             3 * 1 = 3
3 * 2 = 6
3 * 3 = 9
3 * 4 = 12
3 * 5 = 15
3 * 6 = 18
3 * 7 = 21
3 * 8 = 24
3 * 9 = 27
                                                                                    39:
                                                                                                       CAL
                                                                                                                                                                                                                                                       4 * 1 = 4
4 * 2 = 8
4 * 3 = 12
4 * 4 = 16
4 * 5 = 20
4 * 6 = 24
4 * 7 = 28
4 * 8 = 32
4 * 9 = 36
                                                                                                                                                                                                                                                                                                  5 * 1 = 5

5 * 2 = 10

5 * 3 = 15

5 * 4 = 20

5 * 5 = 25

5 * 6 = 30

5 * 7 = 35

5 * 8 = 40

5 * 9 = 45
                                          64
                                                                                    40:
                                                                                                       LOD
                                                                                                                                           1,20
                       code
                                     ----
0,48
0,3
0,0
0,24
1,12
0,2
0,1
1,12
0,9
0,0
              INT
SUP
                                                                                                                                          1,20
                                                                                    41:
                                                                                                       IDA
0:
1:
2:
3:
4:
5:
8:
9:
10:
11:
12:
13:
14:
15:
16:
19:
20:
22:
24:
25:
26:
27:
29:
                                                                                                                                          0,0
                                                                                    42:
                                                                                                        LDX
                                                                                                                                          0,0
                                                                                    43:
                                                                                                        INCI
              INT
                                                                                                       STO
                                                                                    44:
                                                                                    45:
                                                                                                       POP
                                                                                                                                          0,1
              LITI
STX
POP
LOD
LITI
LSSI
JPC
LDA
LITI
STX
POP
                                                                                                                                                                   6 * 1 = 6
6 * 2 = 12
6 * 3 = 18
6 * 4 = 24
6 * 5 = 30
6 * 6 = 36
6 * 7 = 42
6 * 8 = 48
6 * 9 = 54
                                                                                                                                                                                                             7 * 1 = 7
7 * 2 = 14
7 * 3 = 21
7 * 4 = 28
7 * 5 = 35
7 * 6 = 42
7 * 7 = 49
7 * 8 = 56
7 * 9 = 63
                                                                                                                                                                                                                                                       8 * 1 = 8
8 * 2 = 16
8 * 3 = 24
8 * 4 = 32
8 * 5 = 40
8 * 6 = 48
8 * 7 = 56
8 * 8 = 64
8 * 9 = 72
                                                                                                                                                                                                                                                                                                  9 * 1 = 9
9 * 2 = 18
9 * 3 = 27
9 * 4 = 36
9 * 5 = 45
9 * 6 = 54
9 * 7 = 63
9 * 8 = 72
9 * 9 = 81
                                                                                    46:
                                                                                                         JMP
                                                                                                                                          0,24
                                                                                    47:
                                                                                                        INT
                                                                                                                                          0,12
                                                                                    48:
                                                                                                       LDA
                                                                                                                                          0,32
                                                                                    49:
                                                                                                       POP
                                                                                                                                          0,4
                                                                                                       ADDR
                                                                                    50:
                                                                                                                                          0,-1
                                         1,16
0,1
0,0
0,1
1,16
0,9
0,0
0,59
1,20
1,12
0,0
0,1
                                                                                                                                          0,0
                                                                                    51:
                                                                                                       CAL
                                                                                                                                          1,16
1,16
                                                                                    52:
                                                                                                       LOD
                                                                                    53:
                                                                                                       LDA
                                                                                                                                                                    end execution
minicong@minicong-VirtualBox:
              LOD
LITI
LEQI
JPC
LDA
                                                                                    54:
                                                                                                       LDX
                                                                                                                                          0,0
                                                                                    55:
                                                                                                                                          0,0
                                                                                                        INCI
                                                                                    56:
                                                                                                        STO
                                                                                                                                           0,0
                                                                                    57:
                                                                                                       POP
                                                                                                                                          0,1
              LOD
STX
                                                                                   58:
                                                                                                       JMP
                                                                                                                                          0,16
              POP
LOD
LOD
SUBI
LITI
LSSI
JPC
                                         1,20
1,12
0,0
0,4
0,0
```

test8.c를 gcc로 실행하면 다음과 같은 결과가 나온다. 두 개가 동일한 결과라는 것을 확인할 수 있다.

9) test9.c

배열 안에 있는 숫자의 개수를 세는 프로그램이다.

```
test9.c (~/mjeong/hw_final) - VIM
      int main(){
   2
           int i, j, k;
int ary[10],count[10];
   3
           ary[0] = 2;
ary[1] = 8;
ary[2] = 5;
   4 5
ary[2]
ary[3]
ary[4]
ary[5]
ary[6]
ary[7]
ary[8]
ary[9]
                (i = 0; i < 10; i++)
           for
                count[i] = 0;
           for (i = 0; i < 10; i++)
                 for (j = 0; j <= sizeof(ary) / sizeof(int); j++)</pre>
                      if (i + 1 == ary[j]) { count[i]++; }
           }
for (k = 0; k < 10; k++)
                printf("The number of %d is %d\n", k + 1, count[k]);
           return 0;
```

```
a.asm (~/mjeong/hw_final) 28
                                       29
                                                        POP
                                                                            58
                                                                                         OFFSET
                 INT
                                                                            59
                                       30
                                                        LDA
                 SUP
                                                                                            LITI
 2
                         0, main
                                                       LITI
                                                                            60
                                                                                             STX
                 RET
                                                       LITI
                                                                            61
                                                                                             POP
 4 main:
                                       33
                                                       MULI
                                                                                             LDA
                                                                            62
 5
                 INT
                         0, 104
                                       34
                                                    OFFSET
                         1, 24
                                                                            63
                                                                                            LITI
 6
7
8
                 LDA
                                                       LITI
                                                                            64
                                                                                            LITI
                LITI
                                       36
37
38
                                                        STX
                                                                            65
                                                                                            MULI
                LITI
                                                        POP
9
10
                                                                            66
                                                                                         OFFSET
                MULI
                                                        LDA
                                                                            67
                                                                                            LITI
             OFFSET
                                                                0, 4
                                       39
40
41
42
43
44
45
46
47
48
49
50
                                                       LITI
                                                                            68
                                                                                             STX
                LITI
                                                       LITI
                                                                            69
12
13
14
15
16
17
                                                                                             POP
                 STX
                                                       MULI
                                                                            70
71
72
                                                                                                         24
                                                                                             LDA
                 POP
                                                    OFFSET
                 LDA
                                                                                            LITI
                                                       LITI
                                                                                            LITI
                LITI
                                                        STX
                                                                            73
74
75
                                                                                            MULI
                LITI
                                                        POP
                                                                                         OFFSET
                MULI
                                                        LDA
18
             OFFSET
                                                                                            LITI
                                                       LITI
                                                                            76
77
19
20
                                                                                             STX
                LITI
                                                       LITI
                                                                                             POP
                 STX
                                                       MULI
                                                                            78
                                                                                             LDA
                                                                                                         24
21
                 POP
                                                    OFFSET
                                       51
52
53
                                                                           79
80
22
23
24
25
                                                                                            LITI
                 LDA
                                                       LITI
                                                                                            LITI
                LITI
                                                        STX
                                                                            81
                LITI
                                                        POP
                                                                                            MULI
                                       54
                                                        LDA
                                                                            82
                                                                                         OFFSET
                MULI
                                        55
                                                                            83
                                                       LITI
                                                                                            LITI
26
             OFFSET
                                       56
                                                       LITI
                                                                            84
                                                                                             STX
                                                                                                     0,
                LITI
```

```
186
187
188
189
190
191
192
193
194
195
001
196
197
200
L11:
201
202
203
204
205
206
207
208
L13:
209
210
212
213
. ttteral
                                       LDA
                                                                                                                                                                                                                           INT
LDA
LOD
LITI
ADDI
LDA
LOD
LITI
MULI
OFFSET
LDI
POP
ADDR
CAL
                                                                                                      135
136
137
                                                                                                                                          LDA
LOD
                                                                                                                                                           87
88
89
90
                                    LITI
                                                                                                                                   MULI
OFFSET
                                       POP
                                                                                                      138
139
140
141
142
143
144
145
146
147
148
149
150
151
152
153
154
155
156
157
158
L10:
                                    LOD
LITI
LSSI
JPC
                                                        1, 12
0, 10
0, 0
0, L3
1, 64
1, 12
0, 4
0, 0
0, 0
0, 0
0, 0
 91
92
93
94
95
96
97
98
99
100
101
                                                                                                                                          LDI
                                                                                                                                        EQLI
JPC
LDA
                                                                                                                                        LOD
LITI
                                       LDA
                                                                                                                                                                                                                              LOD
LDA
LDX
INCI
STO
POP
JMP
                                    LOD
                                                                                                                                        MULI
                                                                                                                                   OFFSET
                                    MULI
                                                                                                                                          LDI
LDA
LOD
                               OFFSET
                                    STX
POP
                                                                                                                                                                                                                                          1, -4
0, 0
0, 0
0, 0
0, 0
"The number of %d is %d\n"
                                                                                                                                   MULI
 103 L1:
104
105
106
                                       LOD
LDA
                                                                                                                                           LDX
                                                                                                                                         INCI
                                       LDX
                                                                                                                                           STO
 107
                                     INCI
                                                                                                                                           POP
108
109
                                      STO
POP
                                                                                                                                                           1, 16
1, 16
0, 0
0, 0
0, 0
0, 1
0, L8
                                                                                                      160
161
162
                                                                                                                                           LOD
LDA
LDX
 111 L3:
112
113
114
                                       LDA
                                    LITI
                                                                                                      163
164
165
                                                                                                                                         INCI
STO
POP
                                       POP
116 L5:
                                                                                                      166
167 L9:
168 L4:
                                                                                                                                            JMP
                                                        1, 12
0, 10
0, 0
0, L6
1, 16
0, 0
0, 0
                                       LOD
 117
 118
                                                                                                     168 L4:
169
170
171
172
173
174
175
176 L6:
                                                                                                                                           LOD
LDA
LDX
 119
                                      JPC
LDA
 120
121
122
                                                                                                                                         INCI
                                                                                                                                           STO
                                                                                                                                            POP
                                       POP
124
                                                                                                                                            JMP
 125 L8:
                                                        1, 16
0, 40
0, 4
0, 0
0, 0
0, L9
1, 12
0, 1
126
                                       LOD
                                                                                                      177
178
179
                                                                                                                                           LDA
                                    LITI
LITI
DIVI
                                                                                                                                        LITI
128
129
                                                                                                      180
                                                                                                                                           POP
 130
                                     LEQI
                                                                                                      181 L12:
                                        JPC
                                                                                                                                                                   20
10
0
                                                                                                                                           LOD
 132
                                       LOD
                                                                                                      183
184
                                                                                                                                         LITI
133
```

```
0,-1
0,0
                                                                   ADDR
                                                         185:
    printf
malloc
scanf
main
L2
L3
L1
L5
L6
L8
L9
L10
L7
L4
L12
L13
L11
1:
2:
3:
4:
5:
6:
7:
8:
9:
10:
11:
12:
13:
14:
15:
16:
                 -1
-2
-3
3
88
107
100
111
165
119
158
151
151
158
169
194
187
                                                         186:
                                                                   CAL
                                                        187:
                                                                   LOD
                                                                                    1,20
                                                                   LDA
                                                                                    1,20
                                                         188:
                                                        189:
                                                                   LDX
                                                                                    0,0
                                                        190:
                                                                   INCI
                                                                                    0,0
                                                        191:
                                                                   STO
                                                                                    0,0
                                                         192:
                                                                                    0,1
                                                                   POP
                                                         193:
                                                                    JMP
                                                                                    0,169
                                                        194:
                                                                   LDA
                                                                                    1,-4
                                                                                    0,0
                                                        195:
                                                                   LITI
                                                        196:
                                                                   STO
                                                                                    0,0
                                                        197:
                                                                   RET
                                                                                    0,0
9:
1:
2:
3:
4:
5:
6:
7:
8:
9:
10:
11:
12:
13:
14:
15:
16:
17:
     0,40
0,3
0,104
1,24
0,0
0,4
0,0
0,2
0,0
0,1
10,1
0,4
0,0
0,0
                                                       start execution
                                                       The number of
                                                                               1
2
3
4
5
                                                                                    is
                                                                                         3 3
                                                                                    is
is
                                                       The number of
                                                       The
                                                              number
                                                                          of
                                                                                    is
                                                                                          0
                                                       The number of
                                                                                    is
                                                             number of
                                                       The
                                                             number of
                                                                                    is
                                                                                         0
                                                       The
                                                                                7 8
                                                       The
                                                              number
                                                                          of
                                                                                    is
                                                                                          0
                                                             number of
                                            (생략)
                                                      The
                                                                                    is
                                                                                          2
                                                              number of 9
                                                       The
                                                             number of
                                                                                10 is 0
                                                       The
                                                             execution
                                                       end
                                                      minjeong@minjeong-VirtualBox:~/m
```

test9.c를 gcc로 실행하면 다음과 같은 결과가 나온다. 두 개가 동일한 결과라는 것을 확인할 수 있다.

10) test10.c

배열안의 값을 오름차순으로 정렬하는 프로그램이다.

```
a.asm (~/mjeong/hw_final) - VIA
                                                                                                                                                                                                                                      CAL
LDA
LITI
STX
POP
                                                                                                                                                                                                                                                                              0
40
0
1
                                                     INT
SUP
RET
                                                                              0, 52
0, main
0, 0
                                                                                                                                                                                187
188
189
190
191
192 L12:
193
194
195
196
197
2 3 4 sor' 8 9 10 L2: 13 14 15 16 17 18 19 20 21 L5: 22 3 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42
                                                 INT
LDA
LITI
STX
POP
                                                                                                                                                                                                                              LOD
LITI
LSSI
JPC
INT
LDA
LDA
LOD
LITI
MULI
OFFSET
LDI
POP
ADDR
                                                  LOD
LOD
LSSI
JPC
LDA
LOD
LITI
ADDI
STX
POP
                                                                                                                                                                                 202
203
204
205
206
207
208
                                                                                                                                                                                                                                                                   0, 5
0, printf
0, 0
                                         LOD
LOST
JPC
LOD
LOT
LOD
LITI
MULI
OFFSET
LDI
MULI
OFFSET
LOD
LOD
LITI
MULI
OFFSET
LDI
GTRI
JPC
LDA
LOD
LOD
                                                                                                                                                                                                                                         LOD
LDA
LDX
                                                                                                                                                                                                                                      INCI
STO
POP
JMP
                                                                                                                                                                                 216 L13:
                                                                                                                                                                                                                                      INT
LDA
POP
ADDR
                                                                                                                                                                                                                                                                   0, 4
0, printf
                                                                                                                                           (생략)
                                                                                                                                                                                                                                                                   ", o
"%d "
"\n sort()\n"
"%d "
"\n"
                                                                                                                                                                                                                                          CAL
                                                                                                                                                                                                 .literal
.literal
.literal
.literal
                                                                                                                                                                                                                                                    20
36
44
```

```
== symbol
                ------
      printf
 1:
                -1
      malloc
 2:
 3:
      scanf
                -3
 4:
      main
                81
 5:
      sort
                3
 6:
     L2
                8
      L3
                80
 7:
      L5
 8:
                18
      L6
 9:
                73
                                                      1,40
                                     179:
                                            LOD
10:
      L7
                66
                                     180:
                                            LITI
                                                      0,7
      L4
                66
11:
                                     181:
                                            LSSI
                                                      0,0
12:
      L1
                73
                                     182:
                                            JPC
                                                      0,201
13:
      L9
                142
                                     183:
                                            TNT
                                                      0,12
14:
      L10
                164
                                     184:
                                            LDA
                                                      0,36
15:
      18
                157
                                     185:
                                            LDA
                                                      1,12
16:
      L12
                179
                                                      1,40
                                     186:
                                            LOD
     L13
                201
17:
                                                      0,4
                                     187:
                                            LITI
18:
     L11
                194
                                     188:
                                            MULI
                                                      0,0
====== code =======
                                     189:
                                            OFFSET
                                                      0,0
                0,52
     INT
 0:
                                     190:
                                            LDI
                                                      0,0
 1:
      SUP
                0,81
                                     191:
                                            POP
                                                      0,5
 2:
      RET
                0,0
                                     192:
                                            ADDR
                                                      0,-1
 3:
      TNT
                0,32
                                     193:
                                            CAL
                                                      0,0
 4:
     IDA
                1,20
                                     194:
                                            LOD
                                                      1,40
      LITI
                0,0
                                     195:
                                            LDA
                                                      1,40
      STX
                0,0
 6:
                                     196:
                                            LDX
                                                      0,0
     POP
 7:
                0,1
                                     197:
                                            INCI
                                                      0,0
                1,20
 8:
      LOD
                                     198:
                                            STO
                                                      0,0
                1,16
      LOD
 9:
                                     199:
                                            POP
                                                      0,1
10:
      LSST
                0,0
                                     200:
                                            JMP
                                                      0,179
11:
      JPC
                0,80
                                                      0,12
                                     201:
                                            TNT
                1,24
      LDA
12:
                                     202:
                                            IDA
                                                      0,44
                1,20
13:
      LOD
                                     203:
                                            POP
                                                      0,4
                0,1
                                                      0,-1
0,0
14:
      LITI
                                     204:
                                            ADDR
15:
      ADDI
                                     205:
                                            CAL
                                    206: RET
start execution
16:
      STX
                0,0
                                                      0,0
                             (생략)
      POP
                0,1
17:
                1,24
                                    4 6 2 7 9 11 1
18:
      LOD
                                     sort()
19:
      LOD
                1,16
                                      2 4 6 7 9 11
20:
      LSSI
                0,0
      JPC
                0,73
                                    end execution
21:
     LOD
22:
                1,12
23:
      LOD
                1,20
```

test10.c를 gcc로 실행하면 다음과 같은 결과가 나온다. 두 개가 동일한 결과라는 것을 확인할 수 있다.

```
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$ gcc -o gcc.exe test10.c
test10.c: In function 'main':
test10.c:23:3: warning: implicit declaration of function 'printf' [-Wimplicit-function-declaration]
    printf("%d ",arr[i]);
    ^

test10.c:23:3: warning: incompatible implicit declaration of built-in function 'printf'
test10.c:23:3: note: include '<stdio.h>' or provide a declaration of 'printf'
test10.c:25:2: warning: incompatible implicit declaration of built-in function 'printf'
    printf("\n sort()\n");
    ^

test10.c:25:2: note: include '<stdio.h>' or provide a declaration of 'printf'
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$ ./gcc.exe
4 6 2 7 9 11 1
sort()
1 2 4 6 7 9 11
minjeong@minjeong-VirtualBox:~/mjeong/hw final$
```

11) test11.c

```
test11.c(-/mjeong/hw_final) - VIM

1 void func2()
2 {
3     printf("func2() is running...\n");
4     return;
5 }
6 void func3()
7 {
8     printf("func3() is running...\n");
9     return;
10 }
11 void func4()
12 {
13     printf("func4() is running...\n");
14     return;
15 }
16 void func1()
17 {
18     printf("func1() is running...\n");
19     func3();
20     return;
21 }
22     3 int main()
24 {
25     func2();
26     func1();
27     return 0;
28 }
29
30
```

● 컴파일러를 통하여 만들어진 어셈블러 프로그램은 다음과 같다.

```
🔊 🖱 🗇 a.asm (~/mjeong/hw_final) - VIM
                      INT
  2
                      SUP
                                0, main
                      RET
  4 5
     func2:
                      INT
  6
7
8
                      INT
                                                                    31
32
33
34
35
36
37
38
                      LDA
                                                                                      POP
                      POP
                                                                                    ADDR
                                                                                                  printf
  9
                                                                                      CAL
                     ADDR
                                 0, printf
10
                                                                                      INT
                      CAL
                                                                                     POP
11
                      RET
                                                                                                  func3
                                                                                    ADDR
12
13
14
15
16
17
     func3:
                                                                                     CAL
                      INT
                                                                                      RET
                                                                   40 main:
41
42
43
44
                      INT
                                                                                      INT
                      LDA
                                                                                              0,
                                                                                      INT
                      POP
                                                                                      POP
                                                                                              0, func2
                     ADDR
                                 0, printf
                                                                                    ADDR
18
                                                                                              0,
                      CAL
                                                                    46
47
48
                                                                                      INT
19
20
21
22
23
24
25
26
                      RET
                                                                                    POP
ADDR
     func4:
                                                                                              0, func1
                                                                    49
50
                      INT
                                                                                     CAL
                      INT
                                                                                      LDA
                                                                                    LITI
STO
                                                                    51
                      LDA
                                                                    52
                      POP
                                                                                              0, 0
"func2() is running...\n"
"func3() is running...\n"
"func4() is running...\n"
"func1() is running...\n"
                                                                    53
54
                                                                                      RET
                     ADDR
                                 0, printf
                                                                        .literal
                                                                    55
56
                                                                       .literal
                      CAL
27
28 func1:
                      RET
                                                                        .literal
29
                      INT
30
                      INT
```

● 어셈블러 프로그램을 시뮬레이터를 이용하여 실행하면 다음과 같은 결과가 나온다.

```
printf
                 -2
-3
35
      malloc
 3:
4:
     scanf
main
                 3
10
      func2
                                  28:
                                          ADDR
                                                       0,-
      func3
                                  29:
                                         CAL
      func4
 7:
                 17
      func1
                                                       0,12
                                  30:
                                          TNT
        code
                                  31:
                                          POP
                                                       0,3
                 0,124
0,35
0,0
 0:
     INT
                                  32:
                                         ADDR
                                                       0,10
     SUP
                                  33:
                                         CAL
     RET
                                                       0,0
 3:
4:
5:
     INT
                 0,12
                                  34:
                                         RET
                                                       0,0
                 0,12
0,12
     INT
                                  35:
                                         INT
                                                       0,12
      LDA
                                         TNT
                                  36:
                                                       0,12
 6:
7:
8:
     POP
                 0,4
     ADDR
CAL
                 0,-1
                                  37:
                                         POP
                                                       0,3
                                                       0,3
                                  38:
                                         ADDR
     RET
                 0,0
                                  39:
                                         CAL
                                                       0,0
                 0,12
0,12
0,40
10:
     INT
INT
                                  40:
                                         TNT
                                                       0,12
11:
12:
                                  41:
                                         POP
                                                       0,3
      LDA
                                                       0,24
0,0
                                  42:
                                         ADDR
     POP
ADDR
                 0,4
14:
15:
                 0,-1
0,0
                                  43:
                                         CAL
      CAL
                                                       1,-4
                                  44:
                                         LDA
     RET
                 0,0
                                                       0,0
                                  45:
                                         LITI
                 0,12
0,12
0,68
17:
     INT
                                         STO
                                 46:
     INT
19:
     LDA
                                 47: RET
                                                       0,0
                 0,4
0,-1
0,0
20:
     POP
                               start execution
21:
     ADDR
                               func2() is running...
func1() is running...
func3() is running...
      CAL
     RET
INT
INT
                 0,0
                 0,12
0,12
0,96
24:
                               end execution
      LDA
                               minieona@minieona-Virtu
                 0.4
```

test11.c를 gcc로 실행하면 다음과 같은 결과가 나온다. 두 개가 동일한 결과라는 것을 확인할 수 있다.

12) test12.c

소수를 구하는 프로그램이다.

```
a.asm (~/mjeong/hw_final) - VIM
                                                                                   L6:
                        INT
SUP
                                                                             42
43
44
                                                                                                      LDA
                                                                                                                      0, main
0, 0
                                                                                                      LOD
                        RET
                                                                                                    LITI
 4 main:
                                                                                                    ADDI
                                   0, 1236
1, 36
0, 1
0, 4
0, 0
0, 0
0, 2
0, 0
0, 12
0, 12
0, 2
0, 5
0, printf
0, 0
1, 12
0, 100
0, 0
1, 16
0, 1
1, 16
0, 1
1, 28
0, 1
1, 28
0, 0
0, 1
1, 20
0, 0
0, 1
                        INT
                                                                             46
47
48
49
50
51
55
55
55
55
56
66
66
66
67
67
77
77
77
77
77
77
78
78
                                                                                                      STX
                      LDA
LITI
 6789
                                                                                                      POP
                                                                                                      LOD
                      LITI
                                                                                                      LDA
                      MULI
                                                                                                      LDX
OFFSET
                                                                                                     INCI
                                                                                                      ST0
                       STX
POP
INT
LDA
                                                                                                      POP
                                                                                                      LDA
                                                                                                      LOD
                                                                                                    LITI
                      LITI
POP
                                                                                                    MULI
                                                                                                OFFSET
                      ADDR
                                                                                                      LDI
                        CAL
                                                                                                      LDA
                        LDA
                                                                                                      LOD
                      LITI
                                                                                                    LITI
                                                                                                    MULI
                        POP
                                                                                                OFFSET
                        LDA
                                                                                                     LDI
                      STX
POP
                                                                                                    MULI
                                                                                                     LOD
                      LDA
LITI
                                                                                                    LEQI
                                                                                                      JPC
                                                                                                      LOD
                                                                                                                       28
0
0
0
                                                                                                      LDA
                        POP
                        LDA
                                                                                                     LDX
                                                                                                    INCI
                      LITI
                        STX
POP
                                                                                                      STO
                                                                                                      POP
                                                                                  L7:
                                   1, 20
1, 12
0, 0
0, L3
                                                                                                     LDA
                        LOD
                        LOD
                                                                                                    LITI
                                                                                                      STX
                      LEGI
                                                                                                      POP
                        JPC
```

```
82
83
84
85
                   LITI
                                              121
122
123
124
                                                                   MULI
                                                               OFFSET
                    POP
                                                                    LOD
     L9:
                             1, 32
0, L10
1, 24
1, 28
0, 0
                                                                    STX
 86
                    LOD
                                              125
126
                                                                    POP
87
88
89
                   JPCR
                                                                    INT
                    LOD
                                              127
                                                                    LDA
                    LOD
                                              128
                                                                    LOD
                   LSSI
                                              129
130
131
132
133
                                                                    POP
     L10:
                                                                   ADDR
                                                                              0, printf
92
93
94
                    JPC
                             1, 32
1, 16
                                                                   CAL
                    LDA
                                                                    LOD
                    LOD
                                                                   LITI
                    LDA
                                              134
                                                                    MOD
96
97
                              1, 24
0, 4
0, 0
                    LOD
                                              135
136
137
138
                                                                   LITI
                   LITI
                   MULI
                                                                   EQLI
98
99
                OFFSET
                                                                    JPC
                    LDI
                             0, 0
0, 0
0, 0
1, 24
1, 24
0, 0
0, 0
                                                                    INT
                                                                              0, 12
                                              139
140
141
142
                                                                    LDA
101
                    MOD
                                                                    POP
102
103
                    STX
                                                                   ADDR
                    POP
                                                                              0, printf
                    LOD
                                                                    CAL
105
                    LDA
                                              143 L12:
106
107
108
                                              144 L1:
                    LDX
                                             145
146
147
148
                   INCI
                                                                    LOD
                    STO
                                                                    LDA
                                                                    LDX
110 L8:
111
                                                                   INCI
                    JMP
                                              149
112 L11:
                                              150
                                                                    POP
 13 L5:
                                              151
152 L3:
                                                                     TMP
                                                                                  L2
114
                    LOD
115
                                              153
154
                    NOT
                                                                    RET
                                                                              0, 6
"%d
116
117
                                                                             "%d "
"%d "
                     JPT
                                                    .literal
    L4:
                                                    .literal
                    LDA
119
                    LOD
120
                   LITI
```

```
======= symbol =======
                                                      1,16
                                     117:
                                            LOD
                -1
-2
                                            POP
                                                      0,5
       printf
                                     118:
                                                      0,-1
0,0
       malloc
                                     119:
                                            ADDR
  3:
       scanf
                                     120:
                                            CAL
  4:
                                     121:
                                            LOD
      main
                                                      1,20
  5:
      L2
                34
                                     122:
                                            LITI
                                                      0,10
  6:
7:
      L3
                139
                                                      0,0
                                     123:
                                            MOD
      L6
                38
                                     124:
                                            LITI
                                                      0,0
       L7
  8:
                                     125:
                                            EQLI
                                                      0,0
                                            JPC
  9:
      L9
                80
                                     126:
                                                      0,132
 10:
      L10
                85
                                     127:
                                            INT
                                                      0,12
                104
 11:
      111
                                     128:
                                            LDA
                                                      0,28
      L8
                103
 12:
                                     129:
                                            POP
                                                      0,4
 13:
                104
                                     130:
                                            ADDR
                                                      0,-1
 14:
       L4
                107
                                     131:
                                            CAL
                                                      0,0
 15:
      L12
                132
                                     132:
                                            LOD
                                                      1,20
 16:
      L1
                132
                                     133:
                                            LDA
                                                      1,20
 ===== code
               _____
                                     134:
                                            LDX
                                                      0,0
                0,36
  0:
       TNT
                                     135:
                                            INCI
                                                      0,0
       SUP
                0,3
                                            ST0
                                                      0,0
                                     136:
  2:
       RET
                0,0
                                     137:
                                            POP
                                                      0,1
  3:
       INT
                0,1236
                                     138:
                                            JMP
                                                      0,34
  4:
                1,36
      LDA
                                     139:
                                            RET
                                                      0,0
                0,1
      LITI
                                    start execution
       LITI
                0,4
                                    2 3 5 7 11 13 17 19 23 29
  7:
      MULI
                0,0
                                    31 37 41 43 47 53 59 61 67 71
  8:
                0,0
       OFFSET
                                       79 83 89 97 101 103 107 109 113
                                    73
  9:
      LITI
                0,2
                                    127 131 137 139 149 151 157 163 167
 10:
                0,0
       STX
                                        181 191 193 197 199
                                                               211 223
                                    179
                                                                       227
                             (생략)
 11:
      POP
                0,1
                                    233 239 241 251 257 263 269 271
                                                                       277
 12:
       INT
                0,12
                                    283
                                        293
                                             307
                                                 311 313 317
                                                               331
                                                                   337
 13:
       LDA
                0,12
                                        359
                                            367
                                                 373 379 383 389 397
                                    353
                                                                       401
 14:
       LITI
                0,2
                                    419 421 431 433 439 443 449 457
                                                                       461 463
                0,5
 15:
       POP
                                    467 479 487 491 499 503 509
                                                                   521 523 541
      ADDR
 16:
                0,-1
                                    end execution
```

173

229

281

409

347 349 test12.c를 gcc로 실행하면 다음과 같은 결과가 나온다. 두 개가 동일한 결과라는 것을 확인할 수 있다.

13) test13.c

```
test13.c (~/mjeong/hw_final) - VIM
    Int strcmp(char *s, char *t){
    for( ;*s==*t;s++){
        if(*s==0)
                      return 0;
          t++;}
return *s-*t;
 6
7 }
8
    void result(char *s, char *t, int ret){
          if(ret == 0)
               printf("ret : %d\n %s and %s are the same\n",ret, s, t);
12
13
14 }
15
          else
                printf("ret : %d\n %s and %s are not the same\n",ret, s, t);
    void main(){
    char *a, *b, *c, *d;
    int ret;
16
18
          a="computer";
b="computer";
19
20
21
22
23
24
25
          ret = strcmp(a,b);
result(a,b,ret);
          c="os";
d="compiler";
26
27
28
29
          ret = strcmp(c,d);
result(c,d,ret);
30 }
```

```
🧝 🗎 📵 a.asm (~/mjeong/hw_final) - V
                                               0, 140
0, main
                                 INT
                                 SUP
                                 RET
  4 strcmp:
  5
6 L2:
                                 INT
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21 L4:
22
23
24
25
26
27
28
29 L1:
30
31
31
32
33
34
35
                                 LOD
                              LDIB
                                LOD
                              LDIB
                                                                                                                                      0, 120
0, 0
0, 1
1, 24
0, 128
0, 0
0, 1
1, 28
0, 16
1, 20
1, 20
1, 24
0, 5
0, strcmp
                                                                                                                         STX
POP
LDA
LDA
STX
POP
LDA
INT
LOD
LOD
POP
ADDR
                               EQLI
                                 JPC
                                                0, L3
                                LOD
                                               0, 0
                              LDIB
                              LITI
                                                0, 0
0, L4
                               EQLI
                                 JPC
                                LDA
                              LITI
                                 ST0
                                                                                                                          CAL
STX
POP
INT
LOD
LOD
LOD
POP
ADDR
CAL
                                 LOD
                                LDA
                                LDX
                              LITI
                                                                                                                                       0, 6
0, result
0, 0
                               ADDI
                                STO
                                                                                                                                      "ret : %d\n %s and %s are the same\n"
"ret : %d\n %s and %s are not the same\n"
"computer"
"computer"
"os"
"compiler"
                                 POP
                                                                                                                             12
52
96
108
120
128
                                                                                                         .literal
                                                                                 (생략)
                                LOD
                                                                                                         .literal
.literal
.literal
                                LDA
                                LDX
                              LITI
                               ADDI
                                STO
                                                 0,
```

```
======= symbol =======
       printf
  1:
                  -1
  2:
       malloc
                  -2
       scanf
  3:
                  - 3
  4:
       main
                  64
  5:
       strcmp
                  3
       L2
  6:
                  4
       L3
  7:
                  33
  8:
       14
                  18
                                                      0,5
  9:
       L1
                  25
                                       102:
                                             ADDR
       result
                  41
 10:
                                                      0,0
                                       103:
                                             CAL
 11:
       L5
                  55
                                       104:
                                             STX
                                             POP
 12:
       L6
                  63
                                       105:
                                                      0,1
                                       106:
                                                      0,12
                                             TNT
======= code ========
                                             LOD
                                       107:
  0:
       INT
                  0,140
                                       108:
                                             LOD
                                                      1,24
       SUP
                  0,64
  1:
                                       109:
                                             LOD
                                                      1,28
       RET
                  0,0
  2:
                                       110:
                                             POP
                                                      0,6
                  0,20
  3:
       INT
                                       111:
                                             ADDR
                                                      0,41
                  1,12
  4:
       LOD
                                       112:
                                             CAL
                                                      0,0
                                       113:
                                             RET
                                                      0,0
  5:
       LDIB
                  0,0
                                       start execution
  6:
       LOD
                  1,16
                                       ret : -12
       LDIB
  7:
                  0,0
                                       computer and computer are not the same
                  0,0
       EOLI
  8:
                               (생략)
                                      ret : 12
  9:
       JPC
                  0,33
                                       os and compiler are not the same
       LOD
                                      end execution
 10:
                  1,12
                                      minieong@minieong-VirtualBox:~/mieong/hw fina
 11:
       LDIB
                  0,0
 12:
       LITI
                  0,0
```

test13.c를 gcc로 실행하면 다음과 같은 결과가 나온다. 예상과 달리 시뮬레이터를 이용하여 실행한 결과가 틀린 결과가 나왔다. 포인터와 관련된 부분에서 뭔가 잘못 구현된 부분이 있으 리라 추정된다.

```
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$ gcc -o gcc.exe test13.c
test13.c: In function 'result':
test13.c:11:3: warning: implicit declaration of function 'printf' [-Wimplicit-function-declaration]
    printf("ret : %d\n %s and %s are the same\n",ret, s, t);
    ^
test13.c:11:3: warning: incompatible implicit declaration of built-in function 'printf'
test13.c:11:3: note: include '<stdio.h>' or provide a declaration of 'printf'
test13.c:13:3: warning: incompatible implicit declaration of built-in function 'printf'
    printf("ret : %d\n %s and %s are not the same\n",ret, s, t);
    ^
test13.c:13:3: note: include '<stdio.h>' or provide a declaration of 'printf'
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$ ./gcc.exe
ret : 0
    computer and computer are the same
ret : 12
    os and compiler are not the same
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$
```

14) test14.c

```
test14.c (~/mjeong/hw_final) - VIM

int gcd (int x, int y){
   int a,b;
   a=x;
   b=y;
   while(a != b){
      if(a < b)
      b=b-a;
      if(a > b)
      a=a-b;}

return (a);

int i;
   i=gcd(84,36);
   printf("result=%d\n",i);

// **

**Comparison of the print of the p
```

```
🔊 🖨 🗊 a.asm (~/mjeong/hw_final)
                    INT
                    SUP
                             0, main
 3
                    RET
3
4 gcd:
5
6
7
8
                    INT
                    LDA
                    LOD
                    STX
                    POP
10
                    LDA
11
                    LOD
12
                    STX
13
                    POP
14 L2:
15
                    LOD
16
17
18
                   LOD
                             0, 0
                  NEQI
                    JPC
                             0, L3
                                            43 L3:
19
                             1, 20
1, 24
                    LOD
                                                                     1, -4
1, 20
0, 0
                                            44
                                                             LDA
20
21
22
23
24
25
26
                   LOD
                                                             LOD
                  LSSI
                                            46
                                                             STO
                             0, L4
                    JPC
                                                             RET
                             1, 24
1, 24
                                            48 main:
                    LDA
                                                                     0, 16
1, 12
0, 16
0, 84
0, 36
                                                             INT
                    LOD
                                            50
                                                             LDA
                    LOD
                                                             INT
                  SUBI
                                            52
53
                                                            LITI
27
28
                    STX
                                                            LITI
                                                                     0, 36
0, 5
0, gcd
0, 0
0, 1
0, 12
0, 12
1, 12
0, 5
                    POP
                                                             POP
29
30
                                             55
                                                            ADDR
    L4:
                                            56
                                                             CAL
                             1, 20
1, 24
                   LOD
                                            57
                                                             STX
31
                   LOD
                                            58
                                                             POP
32
33
34
                  GTRI
                                            59
                                                             INT
                    JPC
                             0, L5
                                            60
                                                             LDA
                                            61
                             1, 20
                                                             LOD
                    LDA
                                                                     0, 5
0, printf
                                                             POP
                                            62
35
                    LOD
                                                            ADDR
                                            63
36
                    LOD
                                            64
                                                             CAL
37
                  SUBI
                                                                     0, 0
"result=%d\n"
                                            65
                                                             RET
38
                    STX
                                            66 .literal
39
                    POP
40 L5:
41 L1:
42
                    JMP
                             0, L2
```

```
======= symbol =======
       printf
   1:
                  -1
   2:
       malloc
                  -2
   3:
       scanf
                  -3
   4:
       main
                  41
                                  27:
                                        LOD
                                                  1,24
   5:
       gcd
                  3
                                  28:
                                        GTRI
                                                  0,0
   6:
       L2
                  12
                                  29:
                                        JPC
                                                  0,36
   7:
       L3
                  37
                                  30:
                                        LDA
                                                  1,20
       L4
   8:
                  26
                                  31:
                                        LOD
                                                  1,20
   9:
       L5
                  36
                                  32:
                                        LOD
                                                  1,24
  10:
       L1
                  36
                                  33:
                                        SUBI
                                                  0,0
                                  34:
                                        STX
                                                  0,0
======= code =======
                                  35:
                                        POP
   0:
       INT
                 0,28
                                                  0,1
                                  36:
                                        JMP
                                                  0,12
   1:
       SUP
                 0,41
                                  37:
                                        LDA
                                                  1,-4
   2:
       RET
                 0,0
       INT
                                  38:
                                        LOD
                                                  1,20
   3:
                 0,28
   4:
                                  39:
                                        ST0
                                                  0,0
       LDA
                 1,20
                                  40:
                                        RET
                                                  0,0
       LOD
   5:
                  1,12
                                  41:
                                        INT
                                                  0,16
       STX
   6:
                 0,0
                                  42:
                                        LDA
                                                  1,12
       POP
   7:
                 0,1
                                  43:
                                        INT
                                                  0,16
       LDA
   8:
                  1,24
                                  44:
                                       LITI
                                                  0,84
   9:
       LOD
                  1,16
                                  45:
                                        LITI
                                                  0,36
  10:
       STX
                 0,0
                                  46:
                                        POP
                                                  0,5
  11:
       POP
                 0,1
                                  47:
                                        ADDR
                                                  0,3
       LOD
  12:
                  1,20
                                        CAL
                                                  0,0
                                  48:
  13:
       LOD
                  1,24
                                                  0,0
                                  49:
                                        STX
  14:
       NEOI
                 0,0
                                  50:
                                        POP
                                                  0,1
  15:
       JPC
                 0,37
                                  51:
                                        INT
                                                  0,12
  16:
       LOD
                  1,20
                                  52:
                                        LDA
                                                  0,12
  17:
       LOD
                  1,24
                                  53:
                                        LOD
                                                  1,12
       LSSI
  18:
                 0,0
                                  54:
                                        POP
                                                  0,5
  19:
       JPC
                  0,26
                                  55:
                                        ADDR
                                                  0,-1
                 1,24
  20:
       LDA
                                  56:
                                        CAL
                                                  0,0
                 1,24
  21:
       LOD
                                  57:
                                       RET
                                                  0,0
                 1,20
       LOD
  22:
                                start execution
  23:
       SUBI
                 0,0
                                result=12
  24:
       STX
                  0,0
                                end execution
  25:
       POP
                 0,1
                                minjeong@minjeong-VirtualBox:~/
  26:
       LOD
                  1,20
```

test14.c를 gcc로 실행하면 다음과 같은 결과가 나온다. 두 개가 동일한 결과라는 것을 확인할 수 있다.

15) test15.c

```
lest15.c + (~/mjeong/hw_final) - VIM

int mul(int a, int b){
   int result;
   result=0;
   while(a){
        if(a%2)
        result=result+b;
        a=a/2;
   b=b*2;
   }
   return result;

   il }

   void main(){
        int i;
        i=mul(120,3);
        printf("result=%d\n",i);
   }
}
```

```
🗑 🗐 🌼 a.asm (~/mjeong/hw_final)
 1
                INT
 2
                SUP
                       0, main
 3
                RET
                                                 LITI
                                    32
 4
   mul:
                                    33
                                                 MULI
 5
                       0, 24
                INT
                                    34
                                                  STX
 6
                LDA
                       1, 20
                                    35
                                                  POP
 7
              LITI
                                    36 L1:
 8
                STX
                                    37
                                                  JMP
                                                         0, L2
 9
                POP
                                    38 L3:
                       0,
                                    39
                                                  LDA
10
   L2:
                                    40
                                                  LOD
                                                         1, 20
11
                LOD
                       1, 12
                                    41
                                                  ST0
                                                         0, 0
12
                JPC
                       0, L3
                                    42
                                                  RET
                                                         0, 0
13
                LOD
                       1, 12
                                    43
                                       main:
14
              LITI
                       0, 2
                                    44
                                                  INT
                                                         0, 16
15
                MOD
                       0,0
                                    45
                                                  LDA
16
                JPC
                       0, L4
                                    46
                                                  INT
                       1, 20
                                    47
17
                LDA
                                                         0, 120
                                                 LITI
                                    48
18
                                                 LITI
                LOD
                       1, 20
                                    49
                                                  POP
                       1,
19
                LOD
                           16
                                    50
                                                 ADDR
                                                         0, mul
20
               ADDI
                                    51
                                                  CAL
21
                STX
                       0, 0
                                    52
                                                  STX
                POP
22
                                    53
                                                  POP
23
   L4:
                                    54
                                                  INT
24
                LDA
                       1, 12
                                    55
                                                  LDA
                                                         0, 12
25
                LOD
                       1, 12
                                    56
                                                  LOD
                                                         1, 12
                       0,
26
              LITI
                                    57
                                                  POP
                                    58
                                                 ADDR
27
                                                         0, printf
                           0
              DIVI
                                    59
                                                  CAL
28
                STX
                                    60
                                                  RET
                                                         0, 0
29
                POP
                                                         "result=%d\n"
                                    61
                                       .literal
                                                     12
30
                LDA
                       1, 16
                       1,
31
                LOD
                           16
```

```
======= symbol =======
        printf
   1:
                  -1
   2:
        malloc
                   -2
   3:
                  -3
        scanf
   4:
        main
                  37
   5:
                   3
        mul
   6:
        L2
                  8
   7:
        L3
                  33
        L4
                  20
   8:
                                  28:
                                       LITI
                                                  0,2
   9:
        11
                  32
                                 29:
                                       MULI
                                                  0,0
           code =======
=======
                                                  0,0
                                 30:
                                       STX
   0:
        INT
                  0,28
                                 31:
                                       POP
                                                  0.1
        SUP
                  0,37
   1:
                                 32:
                                       JMP
                                                  0,8
   2:
        RET
                  0.0
                                 33:
                                       LDA
                                                  1,-4
   3:
        INT
                  0,24
                                 34:
                                       LOD
                                                  1,20
   4:
                  1,20
        LDA
                                                  0,0
                                 35:
                                       ST<sub>0</sub>
   5:
        LITI
                  0,0
                                       RET
                                                  0,0
                                 36:
                  0,0
   6:
        STX
                                 37:
                                       INT
                                                  0.16
   7:
        POP
                  0,1
                                                  1,12
                                 38:
                                       LDA
   8:
        LOD
                  1,12
                                                  0,16
                                 39:
                                       INT
   9:
        JPC
                  0,33
                                 40:
                                       LITI
                                                  0,120
                  1,12
  10:
        LOD
                                 41:
                                       LITI
                                                  0,3
  11:
        LITI
                  0,2
                                 42:
                                       POP
                                                  0,5
  12:
        MOD
                  0,0
                                 43:
                                       ADDR
                                                  0,3
  13:
        JPC
                  0,20
                                 44:
                                       CAL
                                                  0,0
                  1,20
  14:
        LDA
                                 45:
                                       STX
                                                  0,0
                  1,20
  15:
        LOD
                                 46:
                                       POP
                                                  0,1
  16:
        LOD
                  1,16
                                 47:
                                       INT
                                                  0,12
  17:
        ADDI
                  0,0
                                                  0,12
                                 48:
                                       LDA
  18:
        STX
                  0,0
                                 49:
                                       LOD
                                                  1,12
  19:
        POP
                  0,1
                                                  0,5
                                 50:
                                       POP
                  1,12
  20:
        LDA
                                 51:
                                       ADDR
                                                  0,-1
  21:
        LOD
                  1,12
                                 52:
                                       CAL
                                                  0,0
                  0,2
  22:
        LITI
                                 53:
                                       RET
                                                  0,0
        DIVI
                  0,0
  23:
                               start execution
                  0,0
  24:
        STX
                               result=360
  25:
        POP
                  0,1
                               end execution
  26:
        LDA
                  1,16
                               minicong@minicong-VirtualBox
                  1,16
  27:
        LOD
```

test15.c를 gcc로 실행하면 다음과 같은 결과가 나온다. 두 개가 동일한 결과라는 것을 확인할 수 있다.

16) test16.c

```
test16.c (~/mjeong/hw_final) - VIM
      int a[10];
void qsort(int l, int r){
   int i,j,x,w,k;
   i=l;
1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
               j=r;
x=a[(l+r)/2];
do{
                        while(a[i]<x) i++;
                         while(x<a[j])
                        wntle(x<a[]])
j--;
if(i<=j){
    w=a[i];
    a[i]=a[j];
    a[j]=w;
    i++;
    j--;
                while(i<=j);
if(l<j)
                        qsort(l,j);
23
24
25
                if(i<r)
                        qsort(i,r);
      }
      }
void main(){
   int k;
   a[0]=0;a[1]=1;a[2]=3;a[3]=5;a[4]=7;
   a[5]=9;a[6]=2;a[7]=4;a[8]=6;a[9]=8;
   for(k=0;k<10;k++) printf("%d ",a[k]);printf("\n");
   asort(0,0);</pre>
26
27
28
29
30
31
32
33 }
                qsort(0,9);
for(k=0;k<10;k++) printf("%d ",a[k]);printf("\n");</pre>
```

● 컴파일러를 통하여 만들어진 어셈블러 프로그램은 다음과 같다.

```
MULI
OFFSET
LDI
LSSI
JPC
LOD
LDA
LDX
DECI
STO
POP
                                                                                                                                                                                                      INT
SUP
RET
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     INT
LDA
POP
ADDR
CAL
INT
LITI
POP
ADDR
CAL
LDA
LITI
STX
POP
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        0, 12
0, 60
0, 4
0, print:
0, 0
0, 12
0, 0
0, 9
0, 5
0, qsort
0, 0
1, 12
0, 0
0, 0
255
256
257
258
269
260
261
262
263
264
265
266
267
268
270
271
272
273
274
275
276
277
278
279
281
282
283
284
284
284
284
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         printf
                                                                    INT
LDA
LOD
STX
POP
LDA
LOD
STX
POP
LDA
LOD
LDA
LOD
LOD
LITI
DIVI
LITI
MULI
OFFSET
LDI
STX
POP
                                                                                                                                                                                                                                                                              ЭМР
                                                                                                                                                                                                                                                              LOD
LITI
LSSI
JPC
INT
LDA
LOD
LITI
MULI
OFFSET
LDI
POP
ADDR
CAL
                                                                    LDA
LOD
LITI
MULI
OFFSET
LDD
LSSI
JPC
LOD
LDA
LDA
LDX
INCI
STO
POP
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     LOD
LDA
LDX
INCI
STO
POP
JMP
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          0, 12
0, 76
0, 4
0, printf
0, 0
0, 0
"%d "
"\n"
"%d "
"\n"
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    INT
LDA
POP
ADDR
CAL
RET
52
60
68
76
                                                                                                                                                                                                                                                                                                                                                                        (생략)
                                                                                                                   0. L5
                                                                                LOD
LDA
LOD
LITI
```

● 어셈블러 프로그램을 시뮬레이터를 이용하여 실행하면 다음과 같은 결과가 나온다.

```
printf
                  -1
   1:
       malloc
                  -2
   2:
   3:
       scanf
                  -3
   4:
       main
                  128
   5:
                  3
       qsort
   6:
       L<sub>3</sub>
                  25
   7:
       L5
                  25
   8:
       L6
                  41
                                               LOD
                                        257:
                                                         1,12
       L4
   9:
                  40
                                        258:
                                               LITI
                                                         0,4
  10:
       L8
                  41
                                        259:
                                               MULI
                                                         0,0
  11:
       L9
                  57
                                        260:
                                               OFFSET
                                                         0,0
  12:
       L7
                  56
                                        261:
                                               LDI
                                                         0,0
  13:
       L10
                  103
                                        262:
                                               POP
                                                         0,5
  14:
       L2
                  103
                                        263:
                                               ADDR
                                                         0,-1
  15:
                                        264:
                                               CAL
                                                         0,0
       L1
                  107
                                        265:
                                               LOD
                                                         1,12
  16:
       L11
                  117
                                                         1,12
                                        266:
                                               LDA
  17:
       L12
                  127
                                        267:
                                               LDX
                                                         0,0
  18:
       L14
                  213
                                        268:
                                               INCI
                                                         0,0
  19:
       L15
                  235
                                        269:
                                               STO
                                                         0,0
  20:
       L13
                  228
                                        270:
                                               POP
                                                         0,1
  21:
       L17
                  250
                                        271:
                                               JMP
                                                         0,250
  22:
       L18
                  272
                                        272:
                                               INT
                                                         0,12
       L16
  23:
                  265
                                        273:
                                               LDA
                                                         0,76
          code =======
======
                                                         0,4
                                        274:
                                               POP
   0:
       INT
                  0,84
                                        275:
                                                         0,-1
                                               ADDR
       SUP
   1:
                  0,128
                                        276:
                                              CAL
                                                         0,0
                  0,0
   2:
       RET
                                                         0,0
                                        277:
                                              RET
                                  (생략) start execution
                  0,40
   3:
       INT
   4:
       LDA
                  1,20
                                       0 1 3 5 7 9 2 4 6 8
                                       0 1 2 3 4 5 6 7 8 9
   5:
       LOD
                  1,12
   6:
       STX
                  0,0
                                       end execution
                                       minjeong@minjeong-VirtualBox
   7:
       POP
                  0,1
   8:
       LDA
                  1,24
```

test16.c를 gcc로 실행하면 다음과 같은 결과가 나온다. 두 개가 동일한 결과라는 것을 확인할 수 있다.

```
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$ gcc -o gcc.exe test16.c
test16.c: In function 'main':
test16.c:30:20: warning: implicit declaration of function 'printf' [-Wimplicit-function-declaration]
    for(k=0;k<10;k++) printf("%d ",a[k]);printf("\n");

test16.c:30:20: warning: incompatible implicit declaration of built-in function 'printf'
test16.c:30:20: note: include '<stdio.h>' or provide a declaration of 'printf'
test16.c:30:39: warning: incompatible implicit declaration of built-in function 'printf'
for(k=0;k<10;k++) printf("%d ",a[k]);printf("\n");

test16.c:30:39: note: include '<stdio.h>' or provide a declaration of 'printf'
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$ ./gcc.exe
0 1 3 5 7 9 2 4 6 8
0 1 2 3 4 5 6 7 8 9
minjeong@minjeong-VirtualBox:~/mjeong/hw_final$
```

```
3. 작성한 코드
-code_generator.c
#include <stdio.h>
#include <string.h>
```

#include "type.h"

typedef enum op {OP_NULL, LOD, LDX, LDXB, LDA, LITI, STO, STOB, STX, STXB, SUBI, SUBF, DIVI, DIVF, ADDI, ADDF, OFFSET, MULI, MULF, MOD, LSSI, LSSF, GTRI, GTRF, LEQI, LEQF, GEQI, GEQF, NEQI, NEQF, EQLI, EQLF, NOT, OR, AND, CVTI, CVTF, JPC, JPCR, JMP, JPT, JPTR, INT, INCI, INCF, DECI, DECF, SUP, CAL, ADDR, RET, MINUSI, MINUSF, CHK, LDI, LDIB, POP, POPB} OPCODE;

char *opcode_name[] = {"OP_NULL", "LOD", "LDX", "LDXB", "LDA", "LITI", "STO", "STOB", "STX", "STXB", "SUBI", "SUBF", "DIVI", "DIVF", "ADDI", "ADDF", "OFFSET", "MULI", "MULF", "MOD", "LSSI", "LSSF", "GTRI", "GTRF", "LEQI", "LEQF", "GEQI", "GEQF", "NEQI", "NEQF", "EQLI", "EQLF", "NOT", "OR", "AND", "CVTI", "CVTF", "JPC", "JPCR", "JMP", "JPT", "JPT", "INT", "INCI", "INCF", "DECI", "DECF", "SUP", "CAL", "ADDR", "RET", "MINUSI", "MINUSF", "CHK", "LDI", "LDIB", "POP", "POPB"};

```
void code_generation(A_NODE *);
void gen_literal_table();
void gen_program (A_NODE *);
void gen_expression (A_NODE *);
void gen_expression_left (A_NODE *);
int gen_arg_expression (A_NODE *);
void gen_statement (A_NODE *,int, int);
void gen_statement_list (A_NODE *,int, int);
void gen_declaration_list (A_ID *);
void gen_declaration (A_ID *);
```

```
void gen_code_i(OPCODE,int,int);
void gen_code_f(OPCODE,int,float);
void gen_code_s(OPCODE,int,char*);
void gen_code_l(OPCODE,int,int);
void gen_label_number(int);
void gen_label_name(char*);
void gen_error();
int get_label();
int label_no = 0;
int gen_err = 0;
extern FILE *fout;
extern A_TYPE *int_type, *char_type, *void_type, *float_type, *string_type;
extern A_LITERAL literal_table[];
extern int literal_no;
void code_generation(A_NODE *node) {
        gen_program(node);
        gen_literal_table();
}
```

```
void gen_literal_table() {
        int i;
        for (i=1;i<=literal_no; i++) {
                 fprintf(fout,".literal %5d ",literal_table[i].addr);
                 if(literal_table[i].type == int_type)
                          fprintf(fout,"%d\n",literal_table[i].value.i);
                 else if(literal_table[i].type == float_type)
                          fprintf(fout,"%f\n",literal_table[i].value.f);
                 else if(literal_table[i].type == char_type)
                          fprintf(fout,"%d\n",literal_table[i].value.c);
                 else if(literal_table[i].type == string_type)
                          fprintf(fout,"%s\n",literal_table[i].value.s);
        }
}
void gen_program(A_NODE *node) {
        switch(node->name) {
                 case N_PROGRAM:
                          gen_code_i(INT,0,node->value);
                          gen_code_s(SUP,0,"main");
                          gen_code_i(RET,0,0);
```

```
gen_declaration_list(node->clink);
                        break;
                default:
                        gen_error(100,node->line);
                        break;
}
void gen_expression (A_NODE *node){
        A_ID *id;
        A_TYPE *t;
        int i, ll;
        switch (node->name) {
        case N_EXP_IDENT:
                id = node->clink;
                t = id->type;
                switch (id->kind) {
                case ID_VAR:
                case ID_PARM:
                        switch (t->kind)
```

```
case T_ENUM:
       case T_POINTER:
               gen_code_i(LOD, id->level, id->address);
               break;
       case T_ARRAY:
               if (id->kind == ID_VAR)
                       gen_code_i(LDA, id->level, id->address);
               else
                       gen_code_i(LOD, id->level, id->address);
               break;
       case T_STRUCT:
       case T_UNION:
               break;
       default:
               gen_error(11, id->line);
               break;
       break;
case ID_ENUM_LITERAL:
       gen_code_i(LITI, 0, id->init);
```

{

```
break;
       default:
               gen_error(11, node->line);
               break;
       }
       break;
case N_EXP_INT_CONST:
       gen_code_i(LITI, 0, node->clink);
       break;
case N_EXP_FLOAT_CONST:
       i = node->clink;
       gen_code_i(LOD, 0, literal_table[i].addr);
       break;
case N_EXP_CHAR_CONST:
       gen_code_i(LITI, 0, node->clink);
       break;
case N_EXP_STRING_LITERAL:
       i = node->clink;
       gen_code_i(LDA, 0, literal_table[i].addr);
       break;
```

```
case N_EXP_ARRAY:
        gen_expression(node->llink);
        gen_expression(node->rlink);
        if (node->type->size > 1) {
                gen_code_i(LITI, 0, node->type->size);
                gen_code_i(MULI, 0, 0);
       }
        gen_code_i(OFFSET, 0, 0);
       if (!isArrayType(node->type)) {
               i = node->type->size;
               if (i == 1)
                        gen_code_i(LDIB, 0, 0);
                else
                        gen_code_i(LDI, 0, 0);
       }
        break;
case N_EXP_FUNCTION_CALL:
       t = node->llink->type;
       i = t->element_type->element_type->size;
       if (i % 4) i = i / 4 * 4 + 4;
```

if (node->rlink) {

```
gen\_code\_i(INT, 0, 12 + i);
               gen_arg_expression(node->rlink);
               gen_code_i(POP, 0, node->rlink->value / 4 + 3);
       }
        else
               gen_code_i(INT, 0, i);
        gen_expression(node->llink);
        gen_code_i(CAL, 0, 0);
        break;
case N_EXP_STRUCT:
        break;
case N_EXP_ARROW:
        break;
case N_EXP_POST_INC:
        gen_expression(node->clink);
        gen_expression_left(node->clink);
        t = node->type;
        if (node->type->size == 1)
               gen_code_i(LDXB, 0, 0);
        else
```

```
gen_code_i(LDX, 0, 0);
        if (isPointerOrArrayType(node->type)) {
                gen_code_i(LITI, 0, node->type->element_type->size);
                gen_code_i(ADDI, 0, 0);
        }
        else if (isFloatType(node->type))
                gen_code_i(INCF, 0, 0);
        else
                gen_code_i(INCI, 0, 0);
        if (node->type->size == 1)
                gen_code_i(STOB, 0, 0);
        else
                gen_code_i(STO, 0, 0);
        break;
case N_EXP_POST_DEC:
        gen_expression(node->clink);
        gen_expression_left(node->clink);
        t = node->type;
        if (node->type->size == 1)
                gen_code_i(LDXB, 0, 0);
        else
```

```
gen_code_i(LDX, 0, 0);
        if (isPointerOrArrayType(node->type)) {
                gen_code_i(LITI, 0, node->type->element_type->size);
                gen_code_i(SUBI, 0, 0);
        }
        else if (isFloatType(node->type))
                gen_code_i(DECF, 0, 0);
        else
                gen_code_i(DECI, 0, 0);
        if (node->type->size == 1)
                gen_code_i(STOB, 0, 0);
        else
                gen_code_i(STO, 0, 0);
        break;
case N_EXP_PRE_INC:
        gen_expression_left(node->clink);
        t = node->type;
        if (node->type->size == 1)
                gen_code_i(LDXB, 0, 0);
        else
```

```
gen_code_i(LDX, 0, 0);
        if (isPointerOrArrayType(node->type)) {
                gen_code_i(LITI, 0, node->type->element_type->size);
                gen_code_i(ADDI, 0, 0);
        }
        else if (isFloatType(node->type))
                gen_code_i(INCF, 0, 0);
        else
                gen_code_i(INCI, 0, 0);
        if (node->type->size == 1)
                gen_code_i(STXB, 0, 0);
        else
                gen_code_i(STX, 0, 0);
        break;
case N_EXP_PRE_DEC:
        gen_expression_left(node->clink);
        t = node->type;
        if (node->type->size == 1)
                gen_code_i(LDXB, 0, 0);
        else
                gen_code_i(LDX, 0, 0);
```

```
gen_code_i(LITI, 0, node->type->element_type->size);
                gen_code_i(SUBI, 0, 0);
       }
        else if (isFloatType(node->type))
                gen_code_i(DECF, 0, 0);
        else
                gen_code_i(DECI, 0, 0);
        if (node->type->size == 1)
                gen_code_i(STXB, 0, 0);
        else
                gen_code_i(STX, 0, 0);
        break;
case N_EXP_NOT:
        gen_expression(node->clink);
        gen_code_i(NOT, 0, 0);
        break;
case N_EXP_PLUS:
        gen_expression(node->clink);
        break;
```

if (isPointerOrArrayType(node->type)) {

```
case N_EXP_MINUS:
        gen_expression(node->clink);
        if (isFloatType(node->type))
               gen_code_i(MINUSF, 0, 0);
        else
               gen_code_i(MINUSI, 0, 0);
        break;
case N_EXP_AMP:
        gen_expression_left(node->clink);
        break;
case N_EXP_STAR:
       gen_expression(node->clink);
       i = node->type->size;
       if (i == 1)
               gen_code_i(LDIB, 0, 0);
        else
               gen_code_i(LDI, 0, 0);
        break;
case N_EXP_SIZE_TYPE:
        gen_code_i(LITI, 0, node->clink);
        break;
```

```
case N_EXP_SIZE_EXP:
        gen_code_i(LITI, 0, node->clink);
        break;
case N_EXP_CAST:
        gen_expression(node->rlink);
        if (node->type != node->rlink->type)
               if (isFloatType(node->type))
                       gen_code_i(CVTF, 0, 0);
               else if (isFloatType(node->rlink->type))
                       gen_code_i(CVTI, 0, 0);
        break;
case N_EXP_MUL:
        gen_expression(node->llink);
        gen_expression(node->rlink);
        if (isFloatType(node->type))
               gen_code_i(MULF, 0, 0);
        else
               gen_code_i(MULI, 0, 0);
        break;
case N_EXP_DIV:
```

```
gen_expression(node->llink);
        gen_expression(node->rlink);
        if (isFloatType(node->type))
                gen_code_i(DIVF, 0, 0);
        else
                gen_code_i(DIVI, 0, 0);
        break;
case N_EXP_MOD:
        gen_expression(node->llink);
        gen_expression(node->rlink);
        gen_code_i(MOD, 0, 0);
        break;
case N_EXP_ADD:
        gen_expression(node->llink);
        if (isPointerOrArrayType(node->rlink->type)) {
                gen_code_i(LITI, 0, node->rlink->type->element_type->size);
                gen_code_i(MULI, 0, 0);
        }
        gen_expression(node->rlink);
        if (isPointerOrArrayType(node->llink->type)) {
                gen_code_i(LITI, 0, node->llink->type->element_type->size);
```

```
gen_code_i(MULI, 0, 0);
               }
                if (isFloatType(node->type))
                        gen_code_i(ADDF, 0, 0);
                else
                        gen_code_i(ADDI, 0, 0);
                break;
       case N_EXP_SUB:
                gen_expression(node->llink);
                gen_expression(node->rlink);
                if
                             (isPointerOrArrayType(node->llink->type)
                                                                                  &&
!isPointerOrArrayType(node->rlink->type)) {
                        gen_code_i(LITI, 0, node->llink->type->element_type->size);
                        gen_code_i(MULI, 0, 0);
               }
               if (isFloatType(node->type))
                        gen_code_i(SUBF, 0, 0);
                else
                        gen_code_i(SUBI, 0, 0);
                break;
       case N_EXP_LSS:
```

```
gen_expression(node->rlink);
        if (isFloatType(node->llink->type))
                gen_code_i(LSSF, 0, 0);
        else
                gen_code_i(LSSI, 0, 0);
        break;
case N_EXP_GTR:
        gen_expression(node->llink);
        gen_expression(node->rlink);
        if (isFloatType(node->llink->type))
                gen_code_i(GTRF, 0, 0);
        else
                gen_code_i(GTRI, 0, 0);
        break;
case N_EXP_LEQ:
        gen_expression(node->llink);
        gen_expression(node->rlink);
        if (isFloatType(node->llink->type))
                gen_code_i(LEQF, 0, 0);
```

gen_expression(node->llink);

```
gen_code_i(LEQI, 0, 0);
        break;
case N_EXP_GEQ:
        gen_expression(node->llink);
        gen_expression(node->rlink);
        if (isFloatType(node->llink->type))
                gen_code_i(GEQF, 0, 0);
        else
                gen_code_i(GEQI, 0, 0);
        break;
case N_EXP_NEQ:
        gen_expression(node->llink);
        gen_expression(node->rlink);
        if (isFloatType(node->llink->type))
                gen_code_i(NEQF, 0, 0);
        else
                gen_code_i(NEQI, 0, 0);
        break;
case N_EXP_EQL:
        gen_expression(node->llink);
```

else

```
gen_expression(node->rlink);
        if (isFloatType(node->llink->type))
                gen_code_i(EQLF, 0, 0);
        else
                gen_code_i(EQLI, 0, 0);
        break;
case N_EXP_AND:
        gen_expression(node->llink);
        gen_code_l(JPCR, 0, i = get_label());
        gen_expression(node->rlink);
        gen_label_number(i);
        break;
case N_EXP_OR:
        gen_expression(node->llink);
        gen_code_l(JPTR, 0, i = get_label());
        gen_expression(node->rlink);
        gen_label_number(i);
        break;
case N_EXP_ASSIGN:
        gen_expression_left(node->llink);
```

```
i = node->type->size;
                if (i == 1)
                        gen_code_i(STXB, 0, 0);
                else
                        gen_code_i(STX, 0, 0);
                break;
        default:
                gen_error(100, node->line);
                break;
       }
}
void gen_expression_left (A_NODE *node){
        A_ID *id;
        A_TYPE *t;
        int result;
        switch (node->name) {
        case N_EXP_IDENT:
                id = node->clink;
                t = id->type;
                switch (id->kind) {
```

gen_expression(node->rlink);

```
case ID_VAR:
case ID_PARM:
       switch (t->kind){
       case T_ENUM:
       case T_POINTER:
               gen_code_i(LDA, id->level, id->address);
               break;
       case T_ARRAY:
               if (id->kind == ID_VAR)
                       gen_code_i(LDA, id->level, id->address);
               else
                       gen_code_i(LOD, id->level, id->address);
               break;
       case T_STRUCT:
       case T_UNION:
               break;
       default:
               gen_error(13, node->line,id->name);
               break;
       }
```

```
case ID_FUNC:
               gen_code_s(ADDR, 0, id->name);
               break;
       default:
               gen_error(13, node->line,id->name);
               break;
       }
       break;
case N_EXP_ARRAY:
       gen_expression(node->llink);
       gen_expression(node->rlink);
       if (node->type->size > 1) {
               gen_code_i(LITI, 0, node->type->size);
               gen_code_i(MULI, 0, 0);
       }
       gen_code_i(OFFSET, 0, 0);
       break;
case N_EXP_STRUCT:
       break;
case N_EXP_ARROW:
```

break;

```
break;
case N_EXP_STAR:
       gen_expression(node->clink);
       break;
case N_EXP_INT_CONST:
case N_EXP_FLOAT_CONST:
case N_EXP_CHAR_CONST:
case N_EXP_STRING_LITERAL:
case N_EXP_FUNCTION_CALL:
case N_EXP_POST_INC:
case N_EXP_POST_DEC:
case N_EXP_PRE_INC:
case N_EXP_PRE_DEC:
case N_EXP_NOT:
case N_EXP_MINUS:
case N_EXP_CAST:
case N_EXP_SIZE_TYPE:
case N_EXP_SIZE_EXP:
case N_EXP_MUL:
case N_EXP_DIV:
```

```
case N_EXP_MOD:
case N_EXP_ADD:
case N_EXP_SUB:
case N_EXP_LSS:
case N_EXP_GTR:
case N_EXP_LEQ:
case N_EXP_GEQ:
case N_EXP_NEQ:
case N_EXP_EQL:
case N_EXP_AMP:
case N_EXP_AND:
case N_EXP_OR:
case N_EXP_ASSIGN:
       gen_error(12, node->line);
       break;
default:
       gen_error(100, node->line);
       break;
}
```

int gen_arg_expression (A_NODE *node){

}

```
A_NODE *n;
        switch(node->name){
                case N_ARG_LIST:
                        gen_expression(node->llink);
                        gen_arg_expression(node->rlink);
                        break;
                case N_ARG_LIST_NIL:
                        break;
                default:
                        gen_error(100,node->line);
                        break;
      }
}
int get_label(){
        label_no++;
        return(label_no);
}
void gen_statement (A_NODE *node, int cnt_label, int brk_label)
```

```
{
       A_NODE *n;
       int i,l1,l2,l3;
       switch(node->name) {
               case N_STMT_LABEL_CASE:
               case N_STMT_LABEL_DEFAULT:
                       break;
               case N_STMT_COMPOUND:
                       if(node->llink) gen_declaration_list(node->llink);
                       gen_statement_list(node->rlink ,cnt_label, brk_label);
                       break;
               case N_STMT_EMPTY:
                       break;
               case N_STMT_EXPRESSION:
                       n=node->clink;
                       gen_expression(n);
                       i=n->type->size;
                       if (i) gen_code_i(POP,0,i%4?i/4+1:i/4);
                       break;
               case N_STMT_IF:
```

gen_expression(node->llink);

```
gen_code_l(JPC, 0, 11=get_label());
        gen_statement(node->rlink,cnt_label,brk_label);
        gen_label_number(l1);
        break;
case N_STMT_IF_ELSE:
        gen_expression(node->llink);
        gen_code_l(JPC, 0, l1=get_label());
        gen_statement(node->clink,cnt_label,brk_label);
        gen_code_l(JMP, 0, l2=get_label());
        gen_label_number(l1);
        gen_statement(node->rlink,cnt_label,brk_label);
        gen_label_number(l2);
        break;
case N_STMT_SWITCH:
        break;
case N_STMT_WHILE:
        13=get_label();
        gen_label_number(l1=get_label());
        gen_expression(node->llink);
        gen_code_l(JPC, 0, l2=get_label());
```

```
gen_statement(node->rlink,l3,l2);
        gen_label_number(l3);
        gen_code_l(JMP, 0, 11);
        gen_label_number(l2);
        break;
case N_STMT_DO:
        13=get_label();
        l2=get_label();
        gen_label_number(l1=get_label());
        gen_statement(node->llink,l2,l3);
        gen_label_number(l2);
        gen_expression(node->rlink);
        gen_code_l(JPT, 0, 11);
        gen_label_number(l3);
        break;
case N_STMT_FOR:
        n=node->llink;
        l3=get_label();
        if (n->llink){}
                gen_expression(n->llink);
                i=n->llink->type->size;
```

```
if(i)
                gen_code_i(POP,0,i%4?i/4+1:i/4);
}
gen_label_number(l1=get_label());
l2=get_label();
if (n->clink) {
        gen_expression(n->clink);
        gen_code_l(JPC, 0, 12);
}
gen_statement(node->rlink,l3,l2);
gen_label_number(l3);
if (n->rlink){}
        gen_expression(n->rlink);
        i=n->rlink->type->size;
        if(i)
                gen_code_i(POP,0,i%4?i/4+1:i/4);
}
gen_code_l(JMP, 0, 11);
gen_label_number(l2);
break;
```

```
case N_STMT_CONTINUE:
       if (cnt_label)
               gen_code_l(JMP,0,cnt_label);
       else
               gen_error(22,node->line);
       break;
case N_STMT_BREAK:
       if (brk_label)
               gen_code_l(JMP,0,brk_label);
       else
               gen_error(23,node->line);
       break;
case N_STMT_RETURN:
       n=node->clink;
       if(n) { i=n->type->size;
               if (i\%4) i=i/4*4+4;
               gen_code_i(LDA, 1, -i);
               gen_expression(n);
               gen_code_i(STO, 0, 0);}
       //gen_code_i(RET,0,0);
       break;
```

```
default:
                        gen_error(100,node->line);
                        break;
}
void gen_statement_list (A_NODE *node,int cnt_label, int brk_label){
        switch (node->name) {
        case N_STMT_LIST:
                gen_statement(node->llink, cnt_label, brk_label);
                gen_statement_list(node->rlink, cnt_label, brk_label);
                break;
        case N_STMT_LIST_NIL:
                break;
        default:
                gen_error(100, node->line);
                break;
       }
}
```

```
void gen_declaration_list (A_ID *id){
        while(id){
                gen_declaration(id);
                id=id->link;
        }
}
void gen_declaration(A_ID *id) {
        int i;
        A_NODE *node;
        switch (id->kind) {
                case ID_VAR:
                        break;
                case ID_FUNC:
                        if (id->type->expr) {
                                gen_label_name(id->name);
                                gen_code_i(INT, 0, id->type->local_var_size);
                                gen_statement(id->type->expr, 0, 0);
                                gen_code_i(RET,0,0); }
                        break;
                case ID_PARM:
```

```
case ID_ENUM:
                case ID_STRUCT:
                case ID_FIELD:
                case ID_ENUM_LITERAL:
                case ID_NULL:
                         break;
                default:
                         gen_error(100,id->line);
                         break;
}
void gen_error(int i, int ll, char*s){
        gen_err++;
        printf("*** error at line %d: ",ll);
        switch(i){
                case 11: printf("illegal identifier in expression\n");
                         break;
                case 12: printf("illegal l-value expression\n");
```

case ID_TYPE:

```
case 13: printf("identifier %s not l-value expression\n",s);
                        break;
                case 22: printf("no destination for continue statement\n");
                        break;
                case 23: printf("no destination for break statement\n");
                        break;
                case 100: printf("fatal compiler error during code generation\n");
                        break;
                default:printf("unknown \n");
                        break;
       }
}
void gen_code_i(OPCODE op,int l,int a){
        fprintf(fout,"\t%9s
                               %d, %d\n",opcode_name[op],l,a);
}
void gen_code_f(OPCODE op,int l,float a){
        fprintf(fout,"\t%9s
                                %d, %f\n",opcode_name[op],l,a);
}
void gen_code_s(OPCODE op,int l,char* a){
```

break;

}

```
-main.c
#include <stdio.h>
#include <stdlib.h>
#include "type.h"
extern int syntax_err;
extern int semantic_err;
extern A_NODE *root;
FILE *fout;
extern void initialize();
extern void print_ast();
extern void print_sem_ast();
extern void semantic_analysis();
extern void code_generation();
void main(int argc, char *argv[])
{
    if ((fout = fopen("a.asm", "w")) == NULL)
    {
        printf("can not open output file: a.asm\n");
```

```
exit(1);
}
initialize();
yyparse();
if (syntax_err)
    exit(1);
else
    print_ast(root);
semantic_analysis(root);
if(semantic_err)
    exit(1);
else
    print_sem_ast(root);
code_generation(root);
exit(0);
```

}