

A NOT gate (or inverter) is a logic gate where the output is the opposite of the input. So you can say that the output is NOT the same as the input. It's often called an *inverter* since it inverts the input.

The schematic symbol for an inverter is like a buffer, just with a circle at the output to indicate that it's an inverted version of the input.



A	Q
0	1
1	0

The logic or Boolean expression for a NOT gate is $\overline{A} = Q$ which means that:

Q is the opposite of A