
STM32
CubeMX

1. Description

1.1. Project

Project Name	BLE_Custom
Board Name	P-NUCLEO-WB55-NUCLEO
Generated with:	STM32CubeMX 6.16.0
Date	11/29/2025

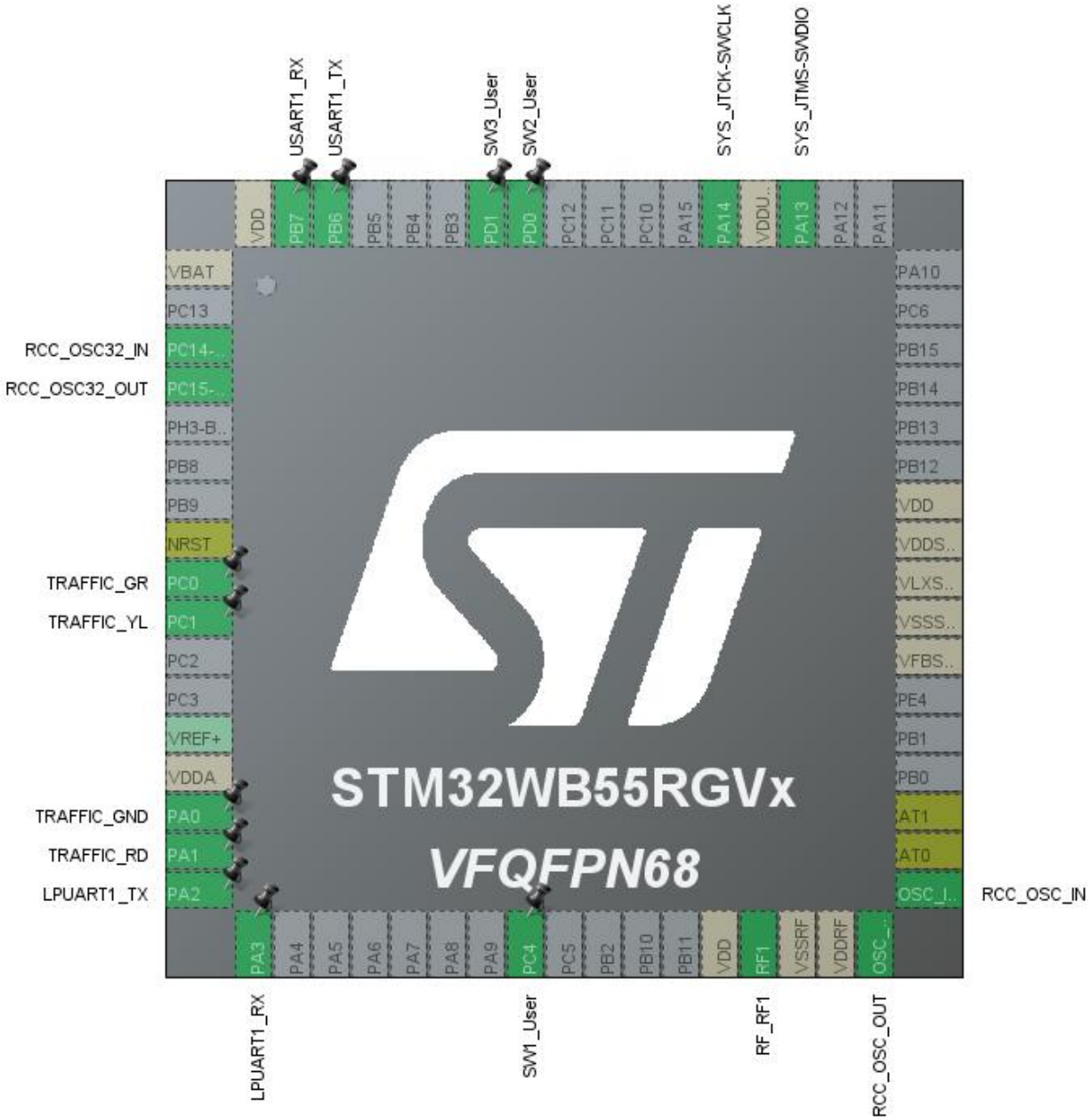
1.2. MCU

MCU Series	STM32WB
MCU Line	STM32WBx5
MCU name	STM32WB55RGVx
MCU Package	VFQFPN68
MCU Pin number	68

1.3. Core(s) information

Core(s)	ARM Cortex-M4

2. Pinout Configuration

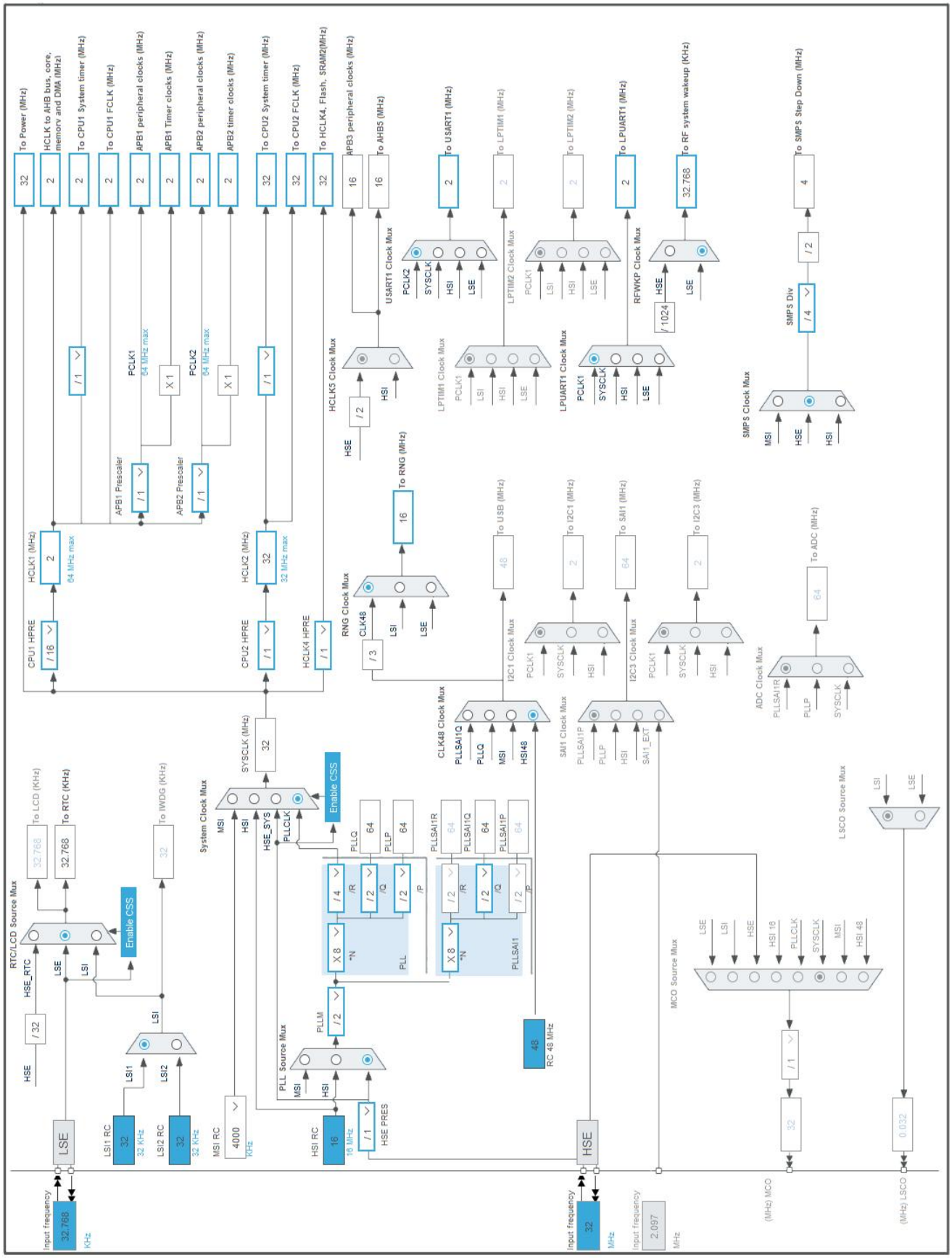


3. Pins Configuration

Pin Number VFQFPN68	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
3	PC14-OSC32_IN	I/O	RCC_OSC32_IN	
4	PC15-OSC32_OUT	I/O	RCC_OSC32_OUT	
8	NRST	Reset		
9	PC0 *	I/O	GPIO_Output	TRAFFIC_GR
10	PC1 *	I/O	GPIO_Output	TRAFFIC_YL
14	VDDA	Power		
15	PA0 *	I/O	GPIO_Output	TRAFFIC_GND
16	PA1 *	I/O	GPIO_Output	TRAFFIC_RD
17	PA2	I/O	LPUART1_TX	
18	PA3	I/O	LPUART1_RX	
25	PC4	I/O	GPIO_EXTI4	SW1_User
30	VDD	Power		
31	RF1	MonoIO	RF_RF1	
32	VSSRF	Power		
33	VDDRF	Power		
34	OSC_OUT	MonoIO	RCC_OSC_OUT	
35	OSC_IN	MonoIO	RCC_OSC_IN	
36	AT0	NC		
37	AT1	NC		
41	VFBSMPS	Power		
42	VSSSMPS	Power		
43	VLXSMPS	Power		
44	VDDSMPS	Power		
45	VDD	Power		
54	PA13	I/O	SYS_JTMS-SWDIO	
55	VDDUSB	Power		
56	PA14	I/O	SYS_JTCK-SWCLK	
61	PD0	I/O	GPIO_EXTI0	SW2_User
62	PD1	I/O	GPIO_EXTI1	SW3_User
66	PB6	I/O	USART1_TX	
67	PB7	I/O	USART1_RX	
68	VDD	Power		

* The pin is affected with an I/O function

4. Clock Tree Configuration



1. Power Consumption Calculator report

1.1. Microcontroller Selection

Series	STM32WB
Line	STM32WBx5
MCU	STM32WB55RGVx
Datasheet	DS11929_Rev3

1.2. Parameter Selection

Temperature	25
Vdd	3.0

1.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

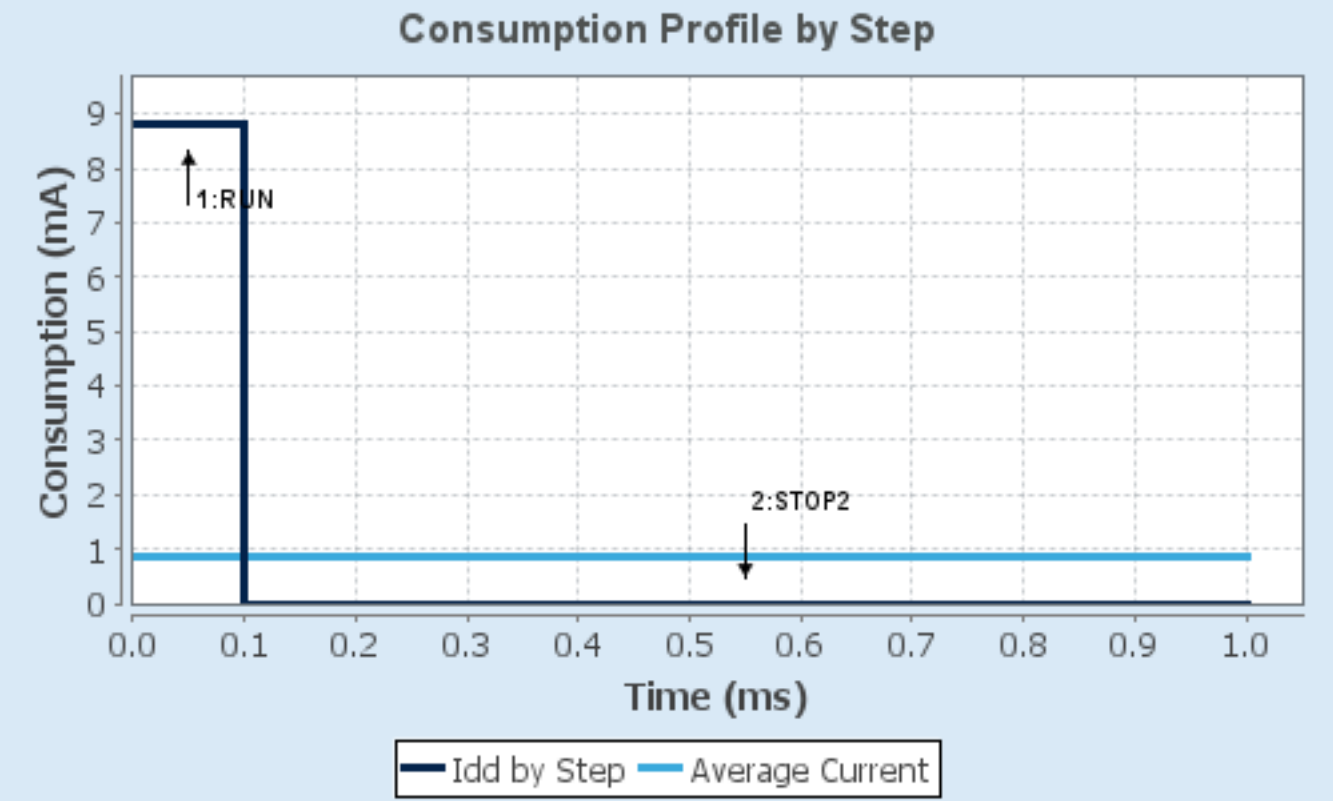
1.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP2
Vdd	3.0	3.0
Voltage Source	Battery	Battery
Range	Range1-High	NoRange
Fetch Type	SRAM1/Flash-PowerDown	FLASH/ART/CACHE
CPU Frequency	64 MHz	0 Hz
Clock Configuration	HSI PLL Regulator_ON	ALL CLOCKS OFF Regulator_ON
Clock Source Frequency	16 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	8.8 mA	1.85 µA
Duration	0.1 ms	0.9 ms
DMIPS	80.0	0.0
Ta Max	103.76	105
Category	In DS Table	In DS Table

1.5. Results

Sequence Time	1 ms	Average Current	881.66 µA
Battery Life	5 months, 7 days, 21 hours	Average DMIPS	8.0 DMIPS

1.6. Chart


Consumption Profile by Step
Consumption (mA)
9
8
7
6
5
4
3
2
1
0
1:RUN
2:STOP2
0.0
0.1
0.2
0.3
0.4
0.5
0.6
0.7
0.8
0.9
1.0
Time (ms)
Idd by Step
Average Current

2. Software Project

2.1. Project Settings

Name	Value
Project Name	BLE_Custom
Project Folder	D:\projects\2025-speech-last-mile\BLE_Custom
Toolchain / IDE	CMake
Firmware Package Name and Version	STM32Cube FW_WB V1.24.0
Application Structure	Advanced
Generate Under Root	No
Do not generate the main()	No
Minimum Heap Size	0x400
Minimum Stack Size	0x1000

2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	MX_GPIO_Init	GPIO
2	MX_DMA_Init	DMA
3	SystemClock_Config	RCC
4	MX_LPUART1_UART_Init	LPUART1
5	MX_USART1_UART_Init	USART1
6	MX_RTC_Init	RTC
7	APPE_Init	STM32_WPAN
8	MX_IPCC_Init	IPCC
9	MX_RNG_Init	RNG
10	MX_RF_Init	RF

3. Peripherals and Middlewares Configuration

3.1. HSEM

mode: Activated

3.2. IPCC

mode: Activated

3.3. LPUART1

Mode: Asynchronous

3.3.1. Parameter Settings:

Basic Parameters:

Baud Rate	115200
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	Receive and Transmit
Single Sample	Disable
ClockPrescaler	1
Fifo Mode	FIFO mode disable
Txfifo Threshold	1 eighth full configuration
Rxfifo Threshold	1 eighth full configuration

Advanced Features:

TX Pin Active Level Inversion	Disable
RX Pin Active Level Inversion	Disable
Data Inversion	Disable
TX and RX pins Swapping	Disable
Overrun	Enable
DMA on RX Error	Enable
MSB First	Disable

3.4. MEMORYMAP

mode: Activated

3.5. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator
Low Speed Clock (LSE) : Crystal/Ceramic Resonator

3.5.1. Parameter Settings:

System Parameters:	
VDD voltage (V)	3.3
Instruction Cache	Enabled
Prefetch Buffer	Enabled
Data Cache	Enabled
Flash Latency(WS)	1 WS (2 CPU cycle)
RCC Parameters:	
HSI Calibration Value	16
MSI Calibration Value	0
MSI Auto Calibration	Disabled
MSI State	Enabled
HSI State	Enabled
HSE Startup Timout Value (ms)	100
LSE Startup Timout Value (ms)	5000
LSE Drive Capability	**LSE oscillator low drive capability ***
Power Parameters:	
Power Regulator Voltage Scale	Power Regulator Voltage Scale 1
Peripherals Clock Configuration:	
Generate the peripherals clock configuration	TRUE

3.6. RF

mode: Activate RF1

3.7. RNG

mode: Activated

3.7.1. Parameter Settings:

Clock Error Detection	Enable

3.8. RTC

mode: Activate Clock Source

WakeUp: Internal WakeUp

3.8.1. Parameter Settings:

General:

Hour Format	Hourformat 24
Asynchronous Predivider value	CFG_RTC_ASYNCH_PRESCALER
Synchronous Predivider value	CFG_RTC_SYNCH_PRESCALER

Wake UP:

Wake Up Clock	RTCCLK / 16
Wake Up Counter	0

3.9. SEQUENCER

mode: Enabled

3.10. SYS

Debug: Serial Wire

Timebase Source: SysTick

3.11. TINY_LPM

mode: Enabled

3.12. USART1

Mode: Asynchronous

3.12.1. Parameter Settings:

Basic Parameters:

Baud Rate	115200
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	8 Samples
Single Sample	Disable
ClockPrescaler	1
Fifo Mode	Disable
Txfifo Threshold	1 eighth full configuration

Rxfifo Threshold	1 eighth full configuration

Advanced Features:

Auto Baudrate	Disable
TX Pin Active Level Inversion	Disable
RX Pin Active Level Inversion	Disable
Data Inversion	Disable
TX and RX Pins Swapping	Disable
Overrun	Enable
DMA on RX Error	Enable
MSB First	Disable

3.13. STM32_WPAN

mode: BLE

3.13.1. BLE Applications and Services:

BLE Wireless Stack:

BLE Wireless Stack	Full

BLE Application Type:

BLE Application Type	Server profile

Server Mode:

BT SIG Beacon	Disabled
BT SIG Blood Pressure Sensor	Disabled
BT SIG Health Thermometer Sensor	Disabled
BT SIG Heart Rate Sensor	Disabled
Custom P2P Server	**Disabled ***
Custom Template	**Enabled ***

BLE Services Configuration:

The device needs to support the Peripheral Role	1
The device needs to support the Central Role	0
BLE_CFG_SVC_MAX_NBR_CB	7
BLE_CFG_CLT_MAX_NBR_CB	0

3.13.2. Configuration:

HW Timer Server:

CFG_HW_TS_MAX_NBR_CONCURRENT_TIMER	6
CFG_HW_TS_NVIC_RTC_WAKEUP_IT_PREEMPTPRIO	3
CFG_HW_TS_NVIC_RTC_WAKEUP_IT_SUBPRIO	0
CFG_HW_TS_USE_PRIMASK_AS_CRITICAL_SECTION	1

CFG_HW_TS_RTC_HANDLER_MAX_DELAY	(10 * (LSI_VALUE/1000))
CFG_HW_TS_RTC_WAKEUP_HANDLER_ID	RTC_WKUP_IRQn

HW UART:

CFG_HW_LPUART1_ENABLED	**Enabled ***
CFG_HW_LPUART1_DMA_TX_SUPPORTED	Enabled
CFG_HW_USART1_ENABLED	**Enabled ***
CFG_HW_USART1_DMA_TX_SUPPORTED	Enabled

Generic parameters:

CFG_HW_RESET_BY_FW	Disabled
CFG_USE_SMPS	Disabled
CFG_LPM_SUPPORTED	Disabled
CFG_DEBUGGER_SUPPORTED	Enabled
CFG_DEBUG_BLE_TRACE	**Enabled ***
CFG_DEBUG_APP_TRACE	**Enabled ***
CFG_DEBUG_TRACE_LIGHT	**Enabled ***
CFG_DEBUG_TRACE_FULL	Disabled
DBG_TRACE_USE_CIRCULAR_QUEUE	Enabled
DBG_TRACE_MSG_QUEUE_SIZE	4096
MAX_DBG_TRACE_MSG_SIZE	1024

Application parameters:

CFG_TX_POWER	**0dBm (0x19) ***
CFG_DEBUG_TRACE_UART	**hw_uart1 ***
CFG_CONSOLE_MENU	**hw_lpuart1 ***
CFG_ADV_BD_ADDRESS	**0x7257acd87a6c ***
CFG_FAST_CONN_ADV_INTERVAL_MIN	80
CFG_FAST_CONN_ADV_INTERVAL_MAX	100
CFG_LP_CONN_ADV_INTERVAL_MIN	1000
CFG_LP_CONN_ADV_INTERVAL_MAX	2500
CFG_IO_CAPABILITY	Display Yes No (0x01)
CFG_MITM_PROTECTION	MITM protection required (0x01)
L2CAP_REQUEST_NEW_CONN_PARAM	**1 ***
CFG_RTCCLK_DIVIDER_CONF	0
CFG_RTCCLK_DIV	16
CFG_RTC_WUCKSEL_DIVIDER	0
CFG_RTC_ASYNCH_PRESCALER	**0x0F ***
CFG_RTC_SYNCH_PRESCALER	**0x7FFF ***
CFG_BLE_NUM_LINK	2
CFG_BLE_NUM_GATT_SERVICES	8
CFG_BLE_NUM_GATT_ATTRIBUTES	68
CFG_BLE_MAX_ATT_MTU	**350 ***

CFG_BLE_ATT_VALUE_ARRAY_SIZE	1344
CFG_BLE_DATA_LENGTH_EXTENSION	Enabled
CFG_BLE_PERIPHERAL_SCA	500
CFG_BLE_CENTRAL_SCA	0
CFG_BLE_HSE_STARTUP_TIME	**0x148 ***
CFG_BLE_MAX_CONN_EVENT_LENGTH	**0xFFFFFFFF ***
CFG_BLE_VITERBI_MODE	Enabled
CFG_BLE_OPTIONS	BLE stack Options flags:
- CFG_BLE_OPTIONS_LL	SHCI_C2_BLE_INIT_OPTIONS_LL_HOST
- CFG_BLE_OPTIONS_SVC	SHCI_C2_BLE_INIT_OPTIONS_WITH_SVC_CHANGE_DESC
- CFG_BLE_OPTIONS_DEVICE_NAME	SHCI_C2_BLE_INIT_OPTIONS_DEVICE_NAME_RW
- CFG_BLE_OPTIONS_EXT_ADV	SHCI_C2_BLE_INIT_OPTIONS_NO_EXT_ADV
- CFG_BLE_OPTIONS_CS_ALGO	SHCI_C2_BLE_INIT_OPTIONS_NO_CS_ALGO2
- CFG_BLE_OPTIONS_GATTDB_NVM	SHCI_C2_BLE_INIT_OPTIONS_FULL_GATTDB_NVM
- CFG_BLE_OPTIONS_GATT_CACHING	SHCI_C2_BLE_INIT_OPTIONS_GATT_CACHING_NOTUSED
- CFG_BLE_OPTIONS_POWER_CLASS	SHCI_C2_BLE_INIT_OPTIONS_POWER_CLASS_2_3
- CFG_BLE_OPTIONS_APPEARANCE	SHCI_C2_BLE_INIT_OPTIONS_APPEARANCE_READONLY
- CFG_BLE_OPTIONS_ENHANCED_ATT	SHCI_C2_BLE_INIT_OPTIONS_ENHANCED_ATT_NOTSUPPORTED
CFG_BLE_MAX_COC_INITIATOR_NBR	32
CFG_BLE_MIN_TX_POWER	**-40 ***
CFG_BLE_MAX_TX_POWER	**6 ***
CFG_BLE_MAX_ADD_EATT_BEARERS	4
CFG_BLE_RX_MODEL_CONFIG	SHCI_C2_BLE_INIT_RX_MODEL_AGC_RSSI_LEGACY
CFG_BLE_MAX_ADV_SET_NBR	3
CFG_BLE_MAX_ADV_DATA_LEN	1650
CFG_BLE_TX_PATH_COMPENS	0
CFG_BLE_RX_PATH_COMPENS	0
CFG_BLE_CORE_VERSION	SHCI_C2_BLE_INIT_BLE_CORE_5_4
CFG_TLBLE_EVT_QUEUE_LENGTH	5
CFG_TLBLE_MOST_EVENT_PAYLOAD_SIZE	255

Debug options:

BLE_DBG_APP_EN	Disabled

3.13.3. BLE Advertising:

Advertising configuration:

Advertising Type	Undirected scannable and connectable(0x00)
CFG_IDENTITY_ADDRESS	GAP_PUBLIC_ADDR
CFG_PRIVACY	Disabled
Advertising Filter	No white list(0x00)
Peripheral: Advertise and connectable	**Yes (0x01) ***
Broadcaster: Advertise and non-connectable	No (0x00)
Central: Scan and connect	No (0x00)
Observer: Scan	No (0x00)
CFG_GAP_DEVICE_NAME	**ELM_LED ***
CFG_GAP_DEVICE_NAME_LENGTH	**7 ***

Advertising elements:

ad_data[] length	21
Include AD_TYPE_TX_POWER_LEVEL element	**Yes ***
AD_TYPE_TX_POWER_LEVEL_LENGTH	2
AD_TYPE_TX_POWER_LEVEL	(0x19) /* 0dBm */
Include AD_TYPE_COMPLETE_LOCAL_NAME element	**Yes ***
AD_TYPE_COMPLETE_LOCAL_NAME_LENGTH	**8 ***
AD_TYPE_COMPLETE_LOCAL_NAME	**ELM LED ***
Include AD_TYPE_SHORTENED_LOCAL_NAME element	No
Include AD_TYPE_APPEARANCE element	No
Include AD_TYPE_ADVERTISING_INTERVAL element	No
Include AD_TYPE_LE_ROLE element	No
Include AD_TYPE_16_BIT_SERV_UUID_CMPLT_LIST element	**Yes ***
AD_TYPE_16_BIT_SERV_UUID_CMPLT_LIST_LENGTH	3
ServiceClass UUIDs number	1
UUID 1	**18 15 ***
Include AD_TYPE_128_BIT_SERV_UUID_CMPLT_LIST element	No
Include AD_TYPE_SLAVE_CONN_INTERVAL element	No
Include AD_TYPE_URI element	No
Include AD_TYPE_MANUFACTURER_SPECIFIC_DATA element	**Yes ***
AD_TYPE_MANUFACTURER_SPECIFIC_DATA_LENGTH	4
Company identifier	30,00
Number of user defined data item(s)	1
User defined data 1	00
Comment data 1	

3.13.4. BLE Pairing:

Pairing parameters:

PAIRING_PARAMETERS	**ON ***
CFG_BONDING_MODE	No-bonding mode(0x00)
CFG_ENCRYPTION_KEY_SIZE_MAX	16
CFG_ENCRYPTION_KEY_SIZE_MIN	8
CFG_SC_SUPPORT	Secure Connections Paring supported but optional (0x01)
CFG_BLE_IR	12, 34, 56, 78, 9A, BC, DE, F0, 12, 34, 56, 78, 9A, BC, DE, F0
CFG_BLE_ER	FE, DC, BA, 09, 87, 65, 43, 21, FE, DC, BA, 09, 87, 65, 43, 21
CFG_KEYPRESS_NOTIFICATION_SUPPORT	Keypress notification not supported (0x00)

3.13.5. BLE GATT:

Services configuration:

Number of services	**1 ***

Service1:

Service long name	**LED_Server ***
Service short name	**LEDS ***

3.13.6. Service1:

Service1:

Number of characteristics	**2 ***
UUID type	128 bits UUID(0x02)
UUID 128 input type	reduced
UUID	**FE 40 ***
Type	Primary Service(0x01)
Service max attributes record(s)	6

Characteristic1 general:

Characteristic long name	**BLUE_LED_Char ***
Characteristic short name	**B_LED_C ***
UUID type	128 bits UUID(0x02)
UUID 128 input type	reduced

UUID	**FE 41 ***
Value length	**2 ***
Length characteristic	**Variable ***
Encryption Key Size	**0x10 ***

Characteristic1 properties:

CHAR_PROP_BROADCAST	No
CHAR_PROP_READ	**Yes ***
CHAR_PROP_WRITE_WITHOUT_RESP	**Yes ***
CHAR_PROP_WRITE	No
CHAR_PROP_NOTIFY	No
CHAR_PROP_INDICATE	No

Characteristic1 permissions:

ATTR_PERMISSION_AUTHEN_READ	No
ATTR_PERMISSION_AUTHOR_READ	No
ATTR_PERMISSION_ENCRY_READ	No
ATTR_PERMISSION_AUTHEN_WRITE	No
ATTR_PERMISSION_AUTHOR_WRITE	No
ATTR_PERMISSION_ENCRY_WRITE	No

Characteristic1 GATT events:

GATT_NOTIFY_ATTRIBUTE_WRITE	Yes
GATT_NOTIFY_WRITE_REQ_AND_WAIT_FOR_APPL_RESP	**No ***
GATT_NOTIFY_READ_REQ_AND_WAIT_FOR_APPL_RESP	**No ***
GATT_NOTIFY_NOTIFICATION_COMPLETION	No

Characteristic2 general:

Characteristic long name	**My_Switch_Char ***
Characteristic short name	**SWITCH_C ***
UUID type	128 bits UUID(0x02)
UUID 128 input type	reduced
UUID	**FE 42 ***
Value length	**2 ***
Length characteristic	**Variable ***
Encryption Key Size	**0x10 ***
Update char value offset	0

Characteristic2 properties:

CHAR_PROP_BROADCAST	No
CHAR_PROP_READ	No
CHAR_PROP_WRITE_WITHOUT_RESP	No
CHAR_PROP_WRITE	No
CHAR_PROP_NOTIFY	**Yes ***

CHAR_PROP_INDICATE	No
Characteristic2 permissions:	
ATTR_PERMISSION_AUTHEN_READ	No
ATTR_PERMISSION_AUTHOR_READ	No
ATTR_PERMISSION_ENCRY_READ	No
ATTR_PERMISSION_AUTHEN_WRITE	No
ATTR_PERMISSION_AUTHOR_WRITE	No
ATTR_PERMISSION_ENCRY_WRITE	No
Characteristic2 GATT events:	
GATT_NOTIFY_ATTRIBUTE_WRITE	Yes
GATT_NOTIFY_WRITE_REQ_AND_WAIT_FOR_APPL_RESP	**No ***
GATT_NOTIFY_READ_REQ_AND_WAIT_FOR_APPL_RESP	**No ***
GATT_NOTIFY_NOTIFICATION_COMPLETION	No

*** User modified value**

4. System Configuration

4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
LPUART1	PA2	LPUART1_TX	Alternate Function Push Pull	No pull-up and no pull-down	**Very High ***	
	PA3	LPUART1_RX	Alternate Function Push Pull	No pull-up and no pull-down	**Very High ***	
RCC	PC14-OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
	PC15-OSC32_OUT	RCC_OSC32_OUT	n/a	n/a	n/a	
	OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
	OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
RF	RF1	RF_RF1	n/a	n/a	n/a	
SYS	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	
USART1	PB6	USART1_TX	Alternate Function Push Pull	**Pull-up ***	**Very High ***	
	PB7	USART1_RX	Alternate Function Push Pull	**Pull-up ***	**Very High ***	
GPIO	PC0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	TRAFFIC_GR
	PC1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	TRAFFIC_YL
	PA0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	TRAFFIC_GND
	PA1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	TRAFFIC_RD
	PC4	GPIO_EXTI4	External Interrupt Mode with Rising edge trigger detection	**Pull-up ***	n/a	SW1_User
	PD0	GPIO_EXTI0	External Interrupt Mode with Rising edge trigger detection	**Pull-up ***	n/a	SW2_User
	PD1	GPIO_EXTI1	External Interrupt Mode with Rising edge trigger detection	**Pull-up ***	n/a	SW3_User

4.2. DMA configuration

DMA request	Stream	Direction	Priority
LPUART1_TX	DMA1_Channel4	Memory To Peripheral	Low
USART1_TX	DMA2_Channel4	Memory To Peripheral	Low

LPUART1_TX: DMA1_Channel4 DMA request Settings:

Mode:	Normal
Peripheral Increment:	Disable
Memory Increment:	**Enable ***
Peripheral Data Width:	Byte
Memory Data Width:	Byte

USART1_TX: DMA2_Channel4 DMA request Settings:

Mode:	Normal
Peripheral Increment:	Disable
Memory Increment:	**Enable ***
Peripheral Data Width:	Byte
Memory Data Width:	Byte

4.3. NVIC configuration

4.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
RTC wake-up interrupt through EXTI line 19	true	0	0
EXTI line0 interrupt	true	0	0
EXTI line1 interrupt	true	0	0
EXTI line4 interrupt	true	0	0
DMA1 channel4 global interrupt	true	15	0
USART1 global interrupt	true	0	0
LPUART1 global interrupt	true	0	0
IPCC RX occupied interrupt	true	0	0
IPCC TX free interrupt	true	0	0
HSEM global interrupt	true	0	0
DMA2 channel4 global interrupt	true	15	0
PVD/PVM0/PVM2 interrupts through EXTI lines 16/31/33	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
CPU2 SEV interrupt through EXTI line 40 and PWR CPU2 HOLD wake-up interrupt	unused		
PWR switching on the fly, end of BLE activity, end of 802.15.4 activity, end of critical radio phase interrupt	unused		
RNG global interrupt	unused		
FPU global interrupt	unused		

4.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false

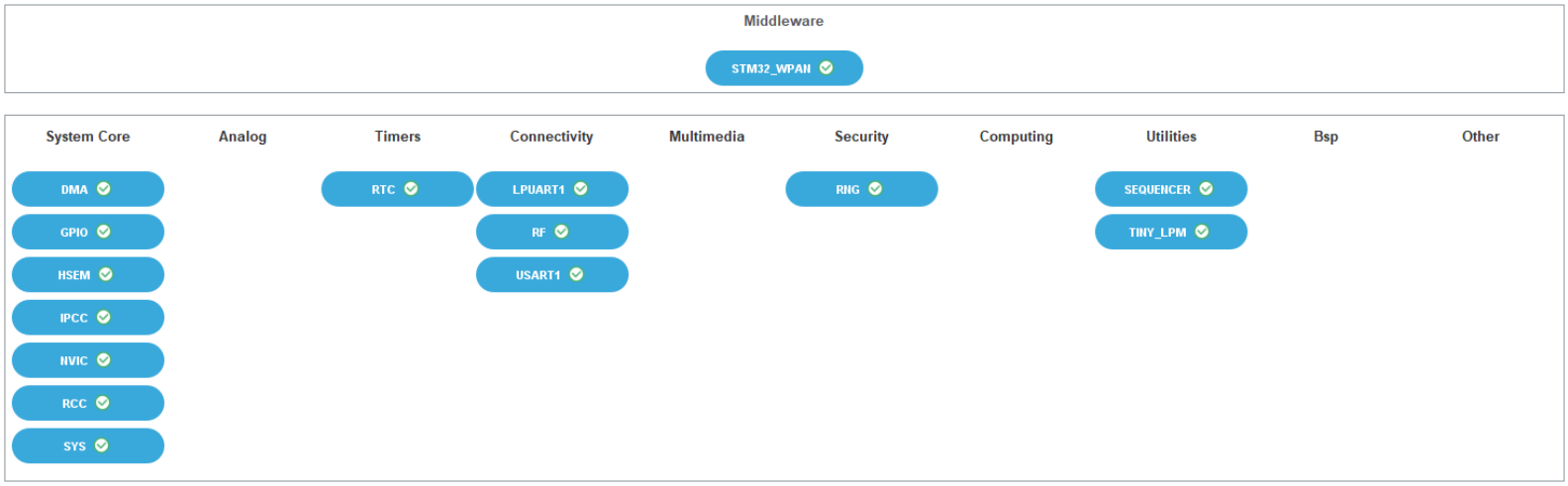
Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Prefetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
RTC wake-up interrupt through EXTI line 19	false	true	true
EXTI line0 interrupt	false	true	true
EXTI line1 interrupt	false	true	true
EXTI line4 interrupt	false	true	true
DMA1 channel4 global interrupt	false	true	true
USART1 global interrupt	false	true	true
LPUART1 global interrupt	false	true	true
IPCC RX occupied interrupt	false	true	true
IPCC TX free interrupt	false	true	true
HSEM global interrupt	false	true	true
DMA2 channel4 global interrupt	false	true	true

*** User modified value**

5. System Views

5.1. Category view

5.1.1. Current



6. Docs & Resources

Type	Link
BSDL files	https://www.st.com/resource/en/bsdl_model/stm32wb_bsdl.zip
IBIS models	https://www.st.com/resource/en/ibis_model/stm32wb_ibis.zip
System View Description	https://www.st.com/resource/en/svd/stm32wb_svd.zip
Board Manufacturing Specifications	https://www.st.com/resource/en/board_manufacturing_specification/stm32wb55rg_mb1479_ref_board.zip
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