1. Description

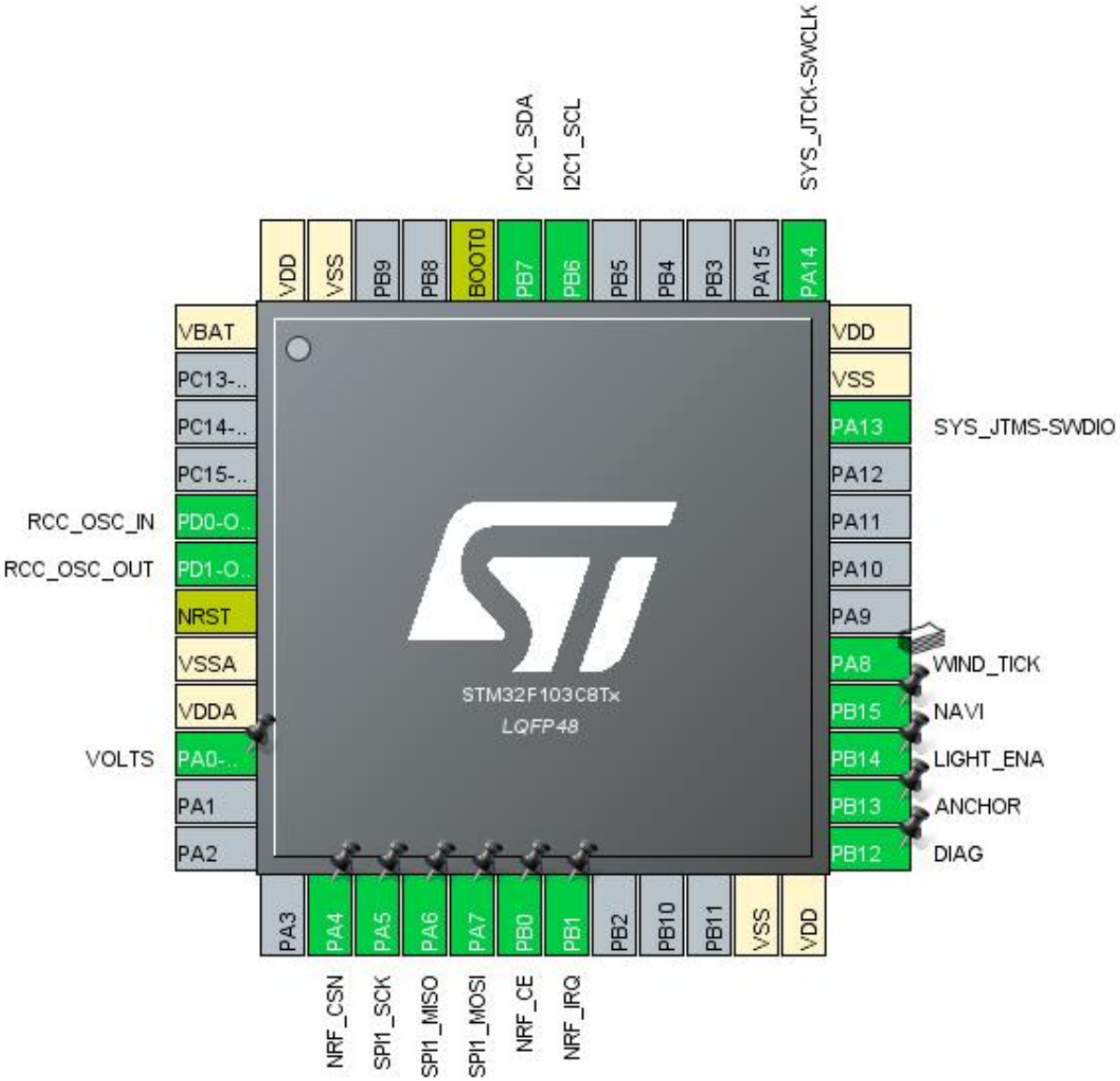
1.1. Project

Project Name	mech_mast_module
Board Name	custom
Generated with:	STM32CubeMX 5.2.1
Date	06/06/2019

1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103C8Tx
MCU Package	LQFP48
MCU Pin number	48

2. Pinout Configuration

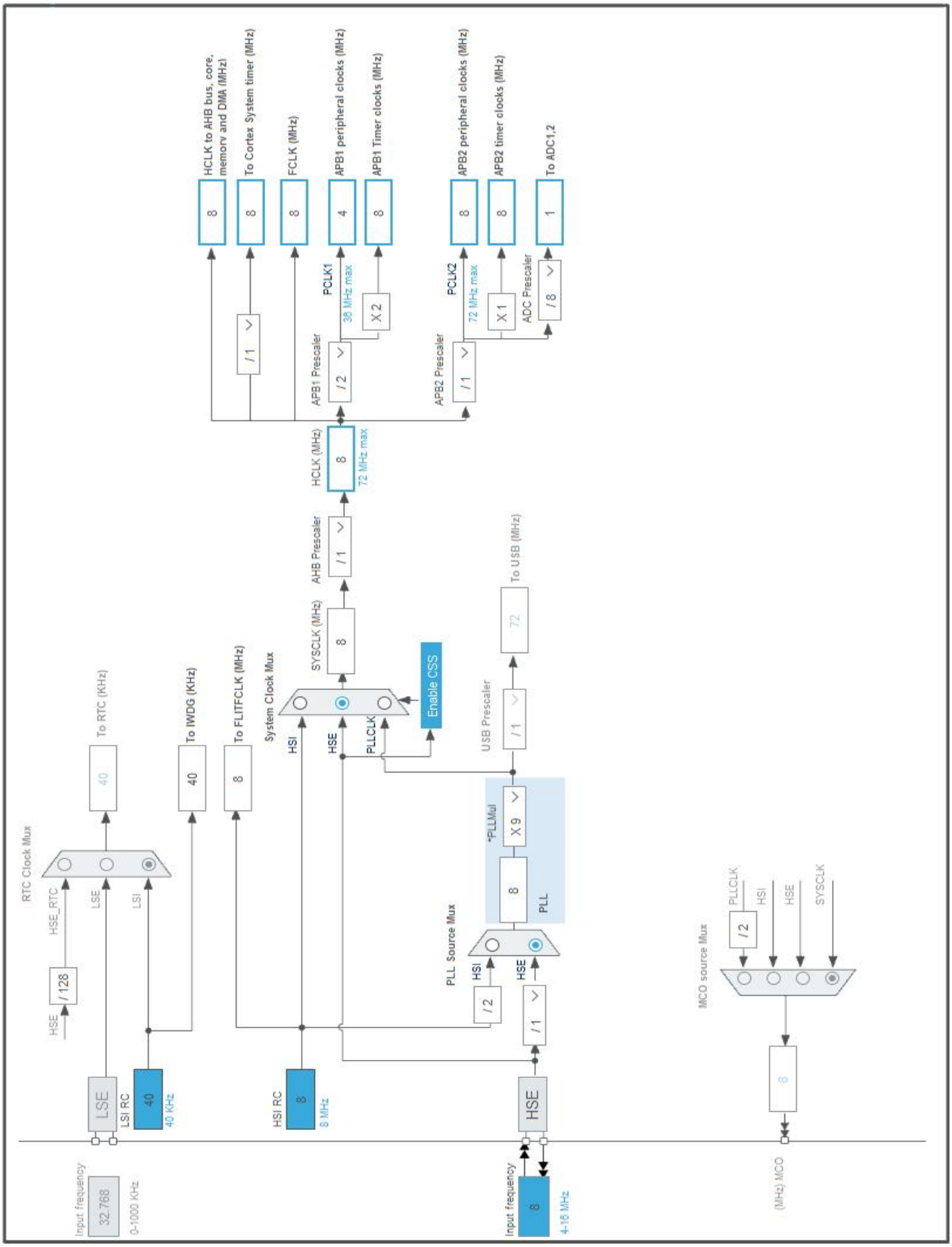


3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
5	PD0-OSC_IN	I/O	RCC_OSC_IN	
6	PD1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
10	PA0-WKUP	I/O	ADC1_IN0	VOLTS
14	PA4 *	I/O	GPIO_Output	NRF_CSN
15	PA5	I/O	SPI1_SCK	
16	PA6	I/O	SPI1_MISO	
17	PA7	I/O	SPI1_MOSI	
18	PB0 *	I/O	GPIO_Output	NRF_CE
19	PB1	I/O	GPIO_EXTI1	NRF_IRQ
23	VSS	Power		
24	VDD	Power		
25	PB12 *	I/O	GPIO_Output	DIAG
26	PB13 *	I/O	GPIO_Output	ANCHOR
27	PB14 *	I/O	GPIO_Output	LIGHT_ENA
28	PB15 *	I/O	GPIO_Output	NAVI
29	PA8	I/O	TIM1_CH1	WIND_TICK
34	PA13	I/O	SYS_JTMS-SWDIO	
35	VSS	Power		
36	VDD	Power		
37	PA14	I/O	SYS_JTCK-SWCLK	
42	PB6	I/O	I2C1_SCL	
43	PB7	I/O	I2C1_SDA	
44	BOOT0	Boot		
47	VSS	Power		
48	VDD	Power		

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

Name	Value
Project Name	mech_mast_module
Project Folder	D:\projects\windsensor\mech_mast_module
Toolchain / IDE	SW4STM32
Firmware Package Name and Version	STM32Cube FW_F1 V1.7.0

5.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	Yes

6. Power Consumption Calculator report

6.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
MCU	STM32F103C8Tx
Datasheet	13587_Rev17

6.2. Parameter Selection

Temperature	25
Vdd	3.3

7. IPs and Middleware Configuration

7.1. ADC1

mode: IN0

mode: Vrefint Channel

7.1.1. Parameter Settings:

ADCs_Common_Settings:

Mode	Independent mode

ADC_Settings:

Data Alignment	Right alignment
Scan Conversion Mode	Enabled
Continuous Conversion Mode	Disabled
Discontinuous Conversion Mode	Disabled

ADC_Regular_ConversionMode:

Enable Regular Conversions	**Disable ***

ADC_Injected_ConversionMode:

Number Of Conversions	**2 ***
External Trigger Source	Injected Conversion launched by software
Injected Conversion Mode	None
Rank	1
Channel	Channel 0
Sampling Time	**239.5 Cycles ***
Injected Offset	0
Rank	**2 ***
Channel	**Channel Vrefint ***
Sampling Time	**239.5 Cycles ***
Injected Offset	0

WatchDog:

Enable Analog WatchDog Mode	false

7.2. I2C1

I2C: I2C

7.2.1. Parameter Settings:

Master Features:

I2C Speed Mode	Standard Mode

I2C Clock Speed (Hz)	100000

Slave Features:

Clock No Stretch Mode	Disabled
Primary Address Length selection	7-bit
Dual Address Acknowledged	Disabled
Primary slave address	0
General Call address detection	Disabled

7.3. IWDG

mode: Activated

7.3.1. Parameter Settings:

Clocking:

IWDG counter clock prescaler	4
IWDG down-counter reload value	4095

7.4. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

7.4.1. Parameter Settings:

System Parameters:

VDD voltage (V)	3.3
Prefetch Buffer	Enabled
Flash Latency(WS)	0 WS (1 CPU cycle)

RCC Parameters:

HSI Calibration Value	16
HSE Startup Timout Value (ms)	100
LSE Startup Timout Value (ms)	5000

7.5. SPI1

Mode: Full-Duplex Master

7.5.1. Parameter Settings:

Basic Parameters:

Frame Format	Motorola

Data Size	8 Bits
First Bit	MSB First

Clock Parameters:

Prescaler (for Baud Rate)	**16 ***
Baud Rate	**500.0 KBits/s ***
Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

Advanced Parameters:

CRC Calculation	Disabled
NSS Signal Type	Software

7.6. SYS

Debug: Serial Wire

Timebase Source: SysTick

7.7. TIM1

Clock Source : Internal Clock

Combined Channels: PWM Input on CH1

7.7.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)	**799 ***
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value)	**65535 ***
Internal Clock Division (CKD)	No Division
Repetition Counter (RCR - 8 bits value)	0
auto-reload preload	Disable

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)
Trigger Event Selection	**Compare Pulse (OC1) ***

PWM Input CH1 :

Input Trigger	TI1FP1
Slave Mode Controller	Reset Mode
____ Parameters for Channel 1 ____	
Polarity Selection	Rising Edge
IC Selection	Direct
Prescaler Division Ratio	No division

Input Filter (4 bits value)	0
____ Parameters for Channel 2 ____	
Polarity Selection (opposite CH1)	Falling Edge
IC Selection	Indirect
Prescaler Division Ratio	No division
Input Filter (4 bits value)	0

7.8. TIM2

Clock Source : Internal Clock

7.8.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)	**7999 ***
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value)	**99 ***
Internal Clock Division (CKD)	No Division
auto-reload preload	Disable

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)
Trigger Event Selection	Reset (UG bit from TIMx_EGR)

*** User modified value**

8. System Configuration

8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
ADC1	PA0-WKUP	ADC1_IN0	Analog mode	n/a	n/a	VOLTS
I2C1	PB6	I2C1_SCL	Alternate Function Open Drain	n/a	**High ***	
	PB7	I2C1_SDA	Alternate Function Open Drain	n/a	**High ***	
RCC	PD0-OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PD1-OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SPI1	PA5	SPI1_SCK	Alternate Function Push Pull	n/a	**High ***	
	PA6	SPI1_MISO	Input mode	No pull-up and no pull-down	**n/a**	
	PA7	SPI1_MOSI	Alternate Function Push Pull	n/a	**High ***	
SYS	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	
TIM1	PA8	TIM1_CH1	Input mode	No pull-up and no pull-down	n/a	WIND_TICK
GPIO	PA4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	NRF_CSN
	PB0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	**High ***	NRF_CE
	PB1	GPIO_EXTI1	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	**n/a**	NRF_IRQ
	PB12	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DIAG
	PB13	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	ANCHOR
	PB14	GPIO_Output	**Output Open Drain ***	No pull-up and no pull-down	Low	LIGHT_ENA
	PB15	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	NAVI

8.2. DMA configuration

nothing configured in DMA service

8.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
EXTI line1 interrupt	true	0	0
TIM1 capture compare interrupt	true	0	0
TIM2 global interrupt	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
ADC1 and ADC2 global interrupts	unused		
TIM1 break interrupt	unused		
TIM1 update interrupt	unused		
TIM1 trigger and commutation interrupts	unused		
I2C1 event interrupt	unused		
I2C1 error interrupt	unused		
SPI1 global interrupt	unused		

*** User modified value**

9. Software Pack Report