1. Description

1.1. Project

Project Name	erm240128-2
Board Name	No information
Generated with:	STM8CubeMX 1.5.0
Date	04/24/2021

1.2. MCU

MCU Series	STM8S
MCU Line	STM8S103/105
MCU name	STM8S105C6T6
MCU Package	LQFP48
MCU Pin number	48

2. Pinout Configuration

3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	NRST	Reset		
4	VSSIO	Power		
5	VSS	Power		
6	VCAP	Power		
7	VDD	Power		
8	VDDIO	Power		
9	PA3 *	I/O	GPIO_Output	NRST
10	PA4 *	I/O	GPIO_Output	DC
11	PA5 *	I/O	GPIO_Output	WR
12	PA6 *	I/O	GPIO_Output	RD
13	VDDA	Power		
14	VSSA	Power		
15	PB7 *	I/O	GPIO_Output	D7
16	PB6 *	I/O	GPIO_Output	D6
17	PB5 *	I/O	GPIO_Output	D5
18	PB4 *	I/O	GPIO_Output	D4
19	PB3 *	I/O	GPIO_Output	D3
20	PB2 *	I/O	GPIO_Output	D2
21	PB1 *	I/O	GPIO_Output	D1
22	PB0 *	I/O	GPIO_Output	D0
26	PC1 *	I/O	GPIO_Input	SB3
27	PC2 *	I/O	GPIO_Input	TS1
28	PC3 *	I/O	GPIO_Input	SB4
29	PC4	I/O	TIM1_CH4	
31	VSSIO	Power		
32	VDDIO	Power		
41	PD0 *	I/O	GPIO_Output	LED1 [Green]
42	PD1	I/O	SYS_SWIM	ST LINK SWIM
46	PD5	I/O	UART2_TX	
47	PD6	I/O	UART2_RX	

 * The pin is affected with an I/O function

4. Clock Tree Configuration