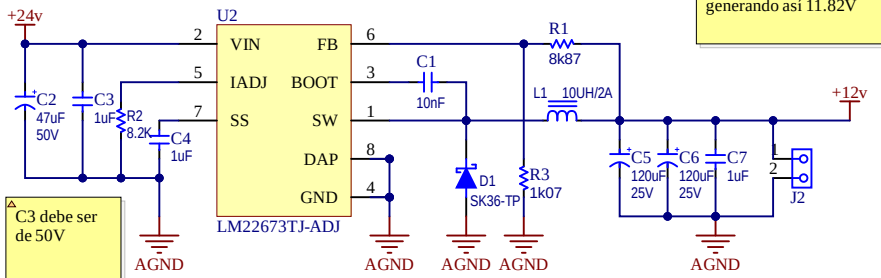
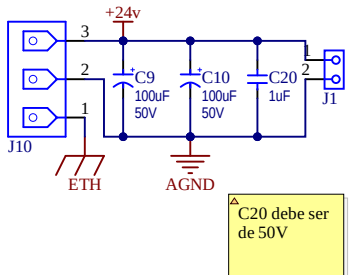
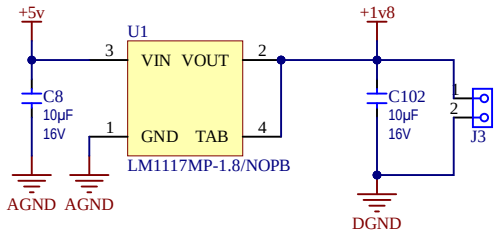
SUPPLY 12v/3A



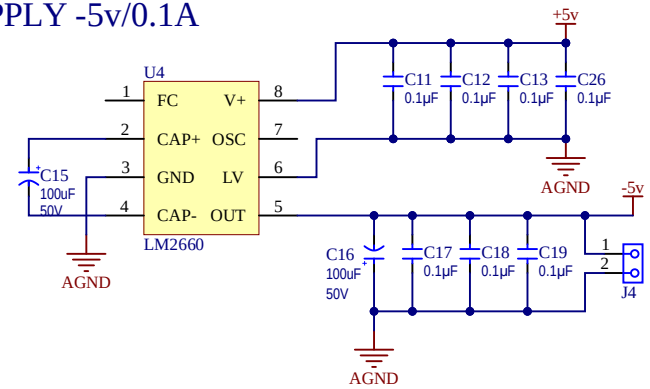
+24v CONNECTOR



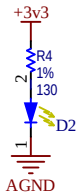
SUPPLY 1v8/0.8A



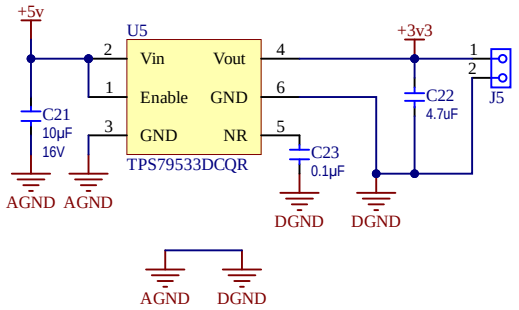
SUPPLY -5v/0.1A



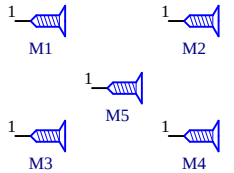
BOARD LED



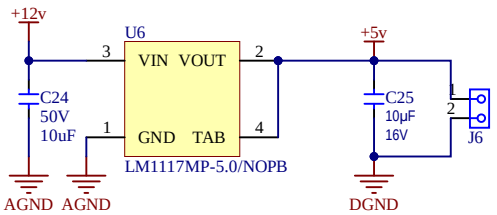
SUPPLY 3v3/0.5A



BOARD SCREWS



SUPPLY 5v/0.8A



TITLE:	Supply		
PROJECT:	Calibrador y caracterizador de sondas de corriente		
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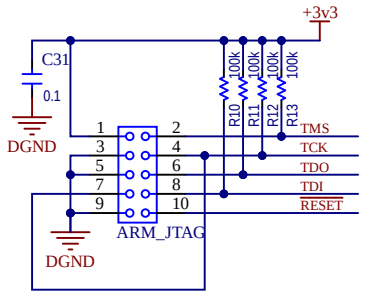
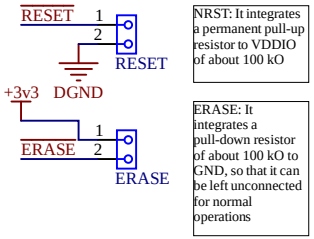
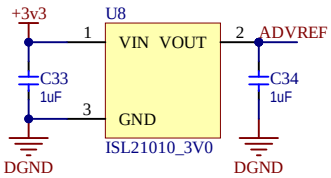
ARM - JTAG



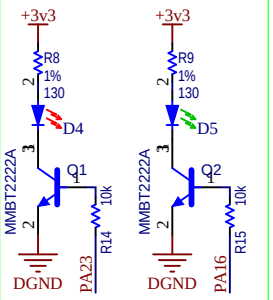
ARM - RESET AND ERASE



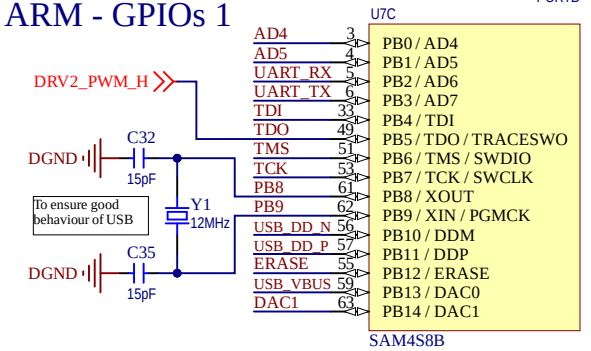
ARM - 3V3 ADC REF (OPTIONAL)



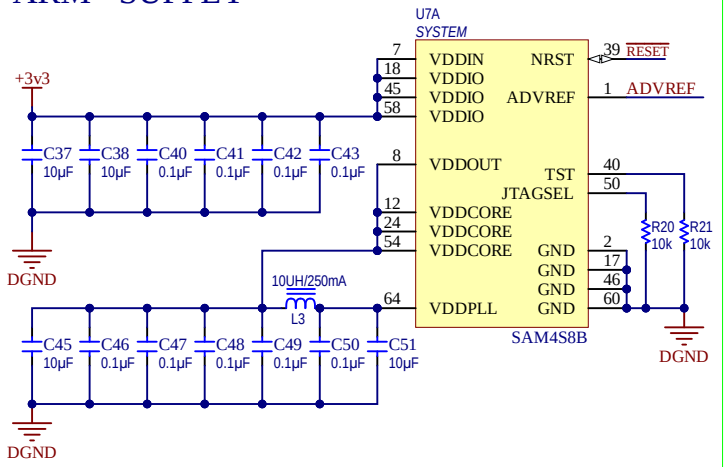
ARM - LEDs



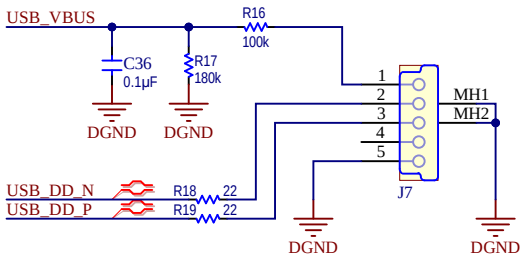
ARM - GPIOs 1



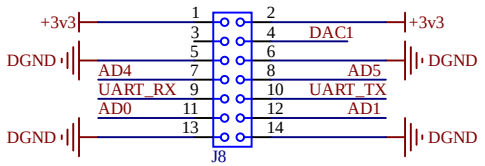
ARM - SUPPLY



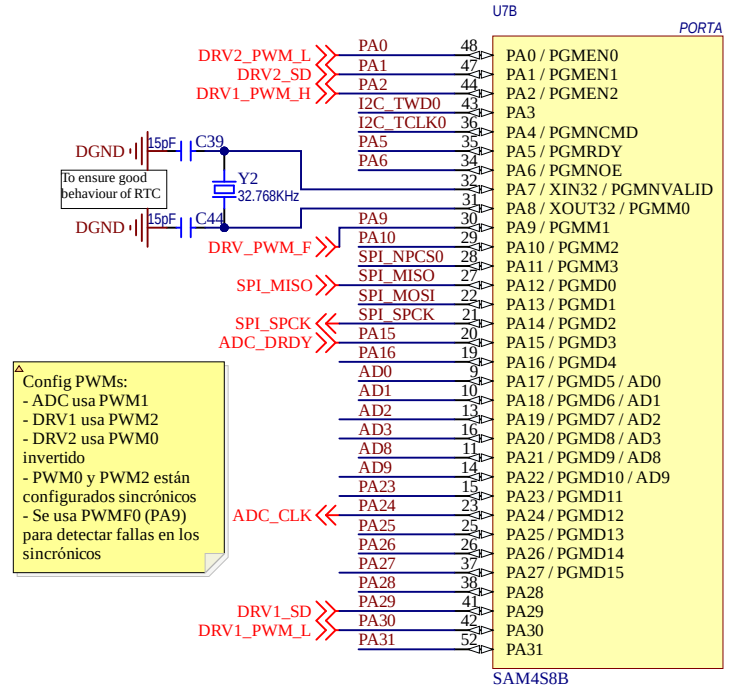
ARM - USB



ARM - HEADER

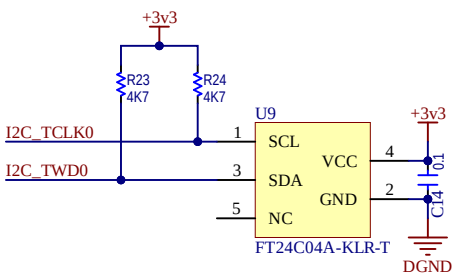


ARM - GPIOs 2

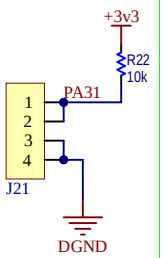


Config PWMs:
- ADC usa PWM1
- DRV1 usa PWM2
- DRV2 usa PWM0 invertido
- PWM0 y PWM2 están configurados sincrónicos
- Se usa PWMF0 (PA9) para detectar fallas en los sincrónicos

ARM - EEPROM



ARM - BTN



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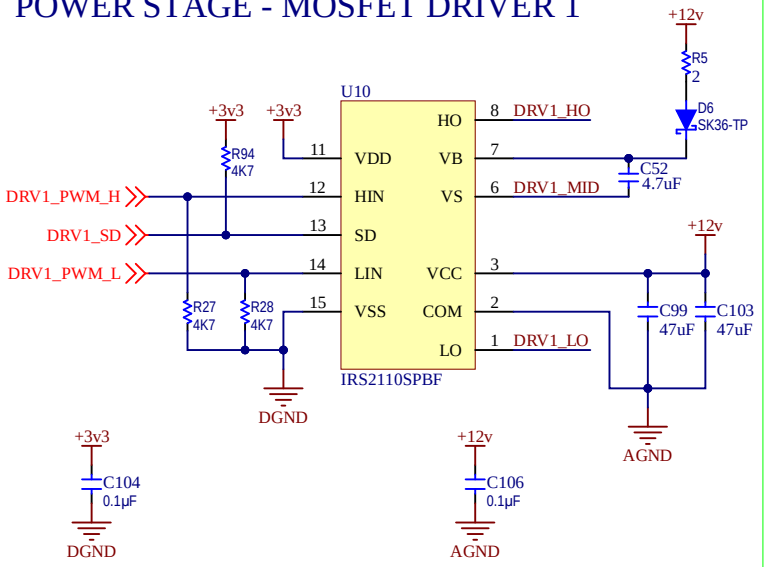
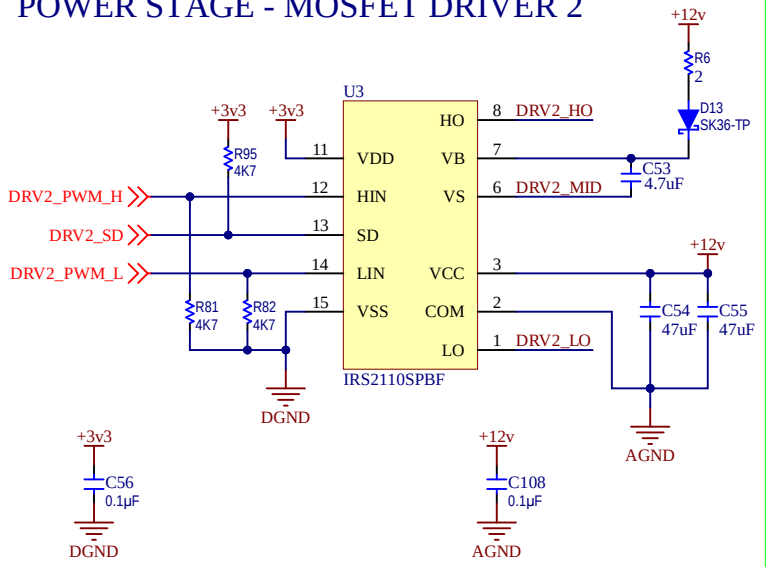
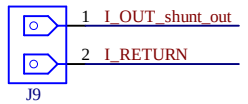
POWER STAGE - MOSFET DRIVER 1



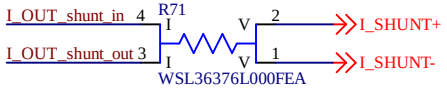
POWER STAGE - MOSFET DRIVER 2



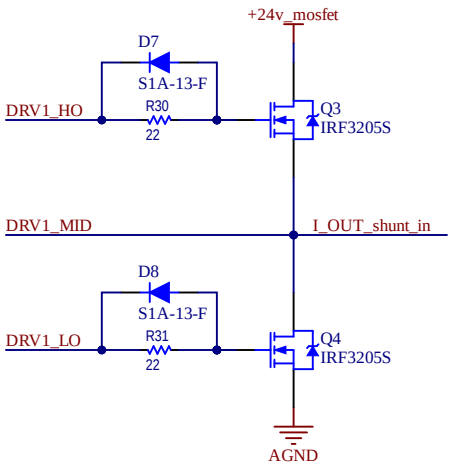
POWER STAGE - CONN OUT



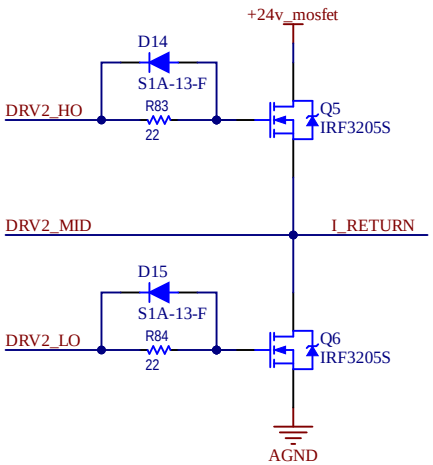
POWER STAGE - SENSOR



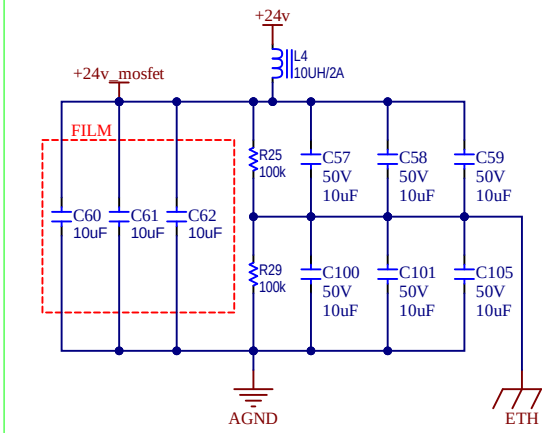
POWER STAGE - MOSFET1



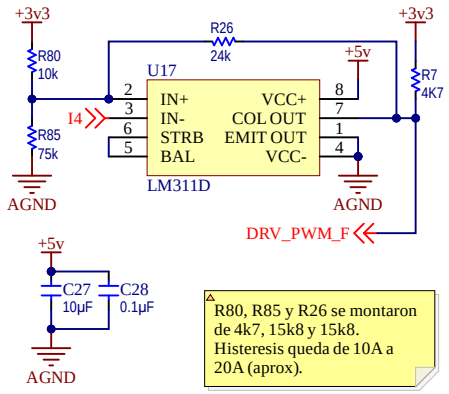
POWER STAGE - MOSFET1



POWER STAGE - SUPPLY



POWER STAGE - OC DETECTION



TITLE:	Power Stage		
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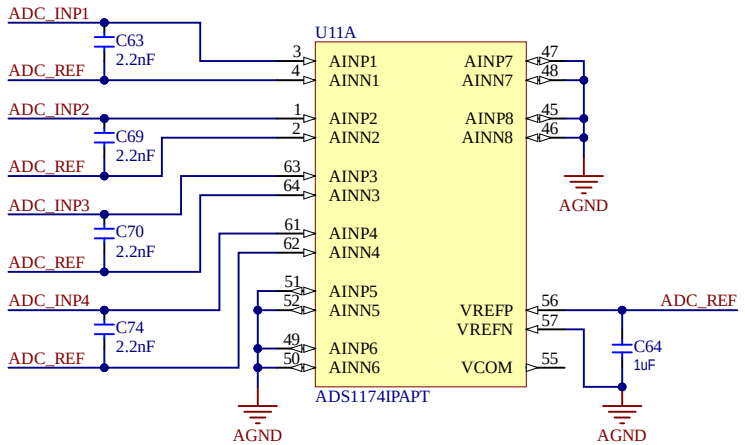
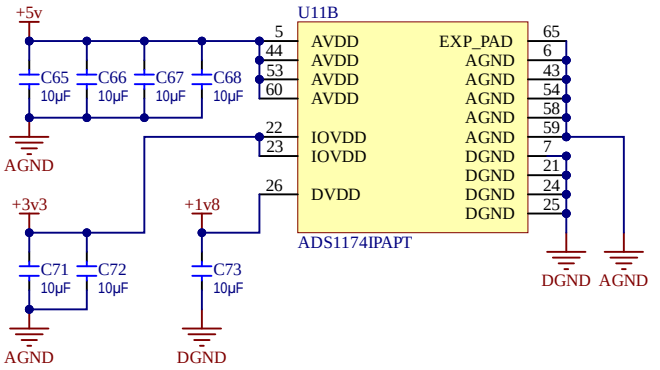
DESIGNER:	Agustín Aon Sanchez	**REVISOR:**	Ignacio Carugati

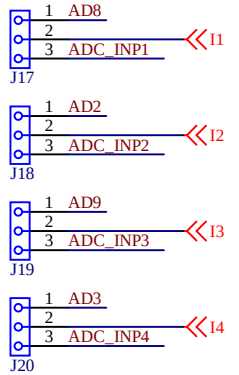
ADC - ANALOG INPUTS



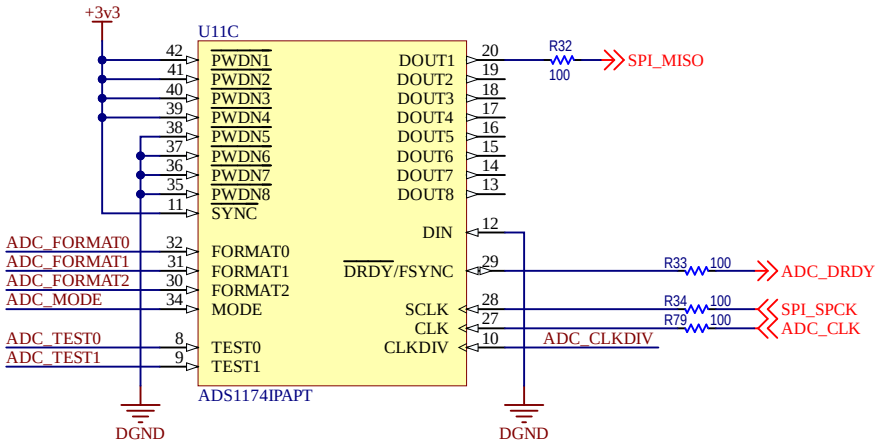
ADC - SUPPLY



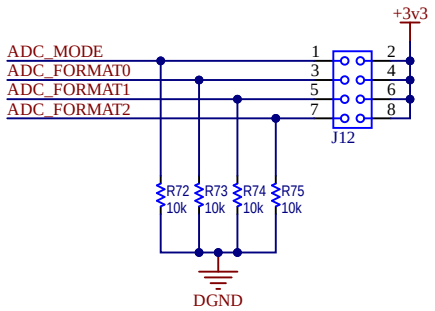
ADC - SOURCE SELECTOR



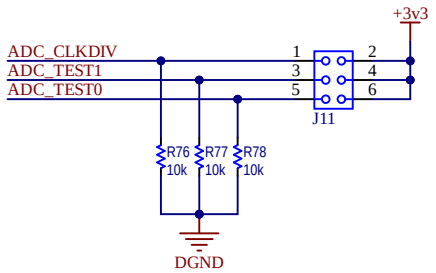
ADC - DIGITAL IO



ADC - MODE SELECTOR



ADC - TEST SELECTOR



TITLE:	ADC		
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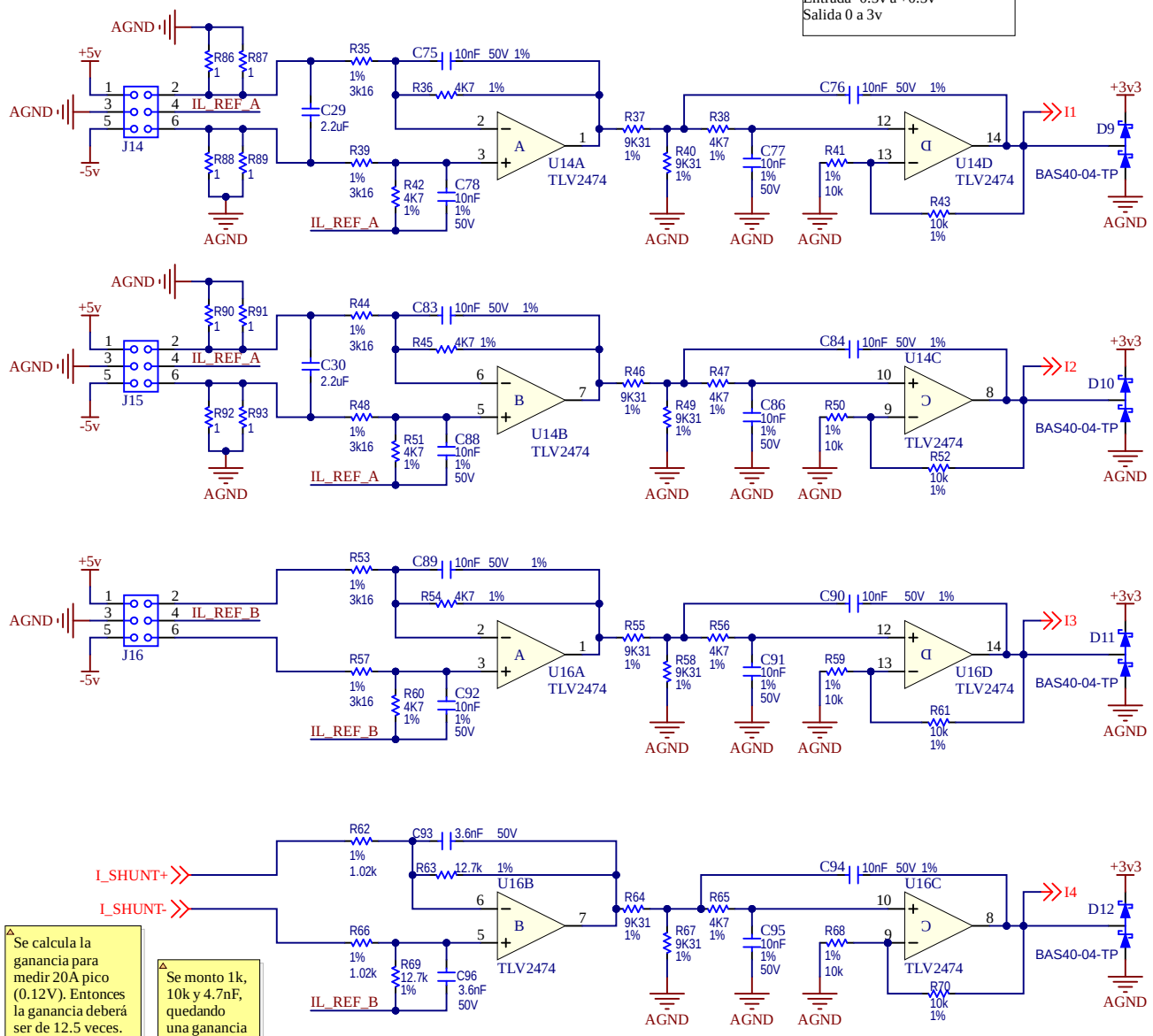
DESIGNER:	Agustín Aon Sanchez	**REVISOR:**	Ignacio Carugati

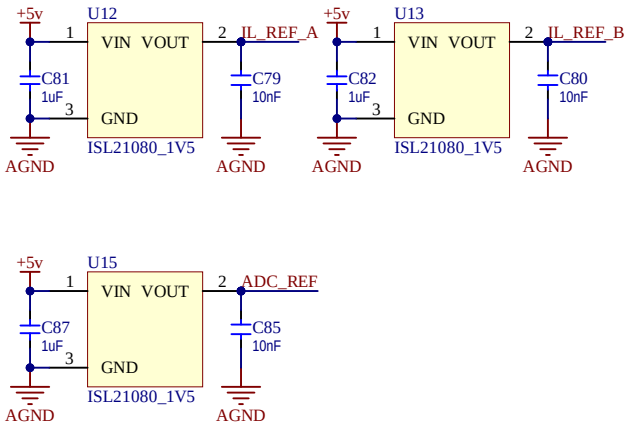
ADC - CURRENT CHANNELS

Butterworth de 3er orden
Frecuencia de corte de 3400Hz
Entrada -0.5v a +0.5v
Salida 0 a 3v

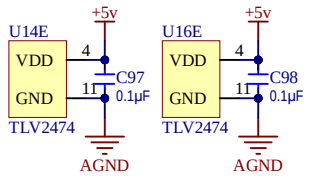


ADC - CURRENT CHANNELS A REF

Reference output voltages: 1.5V, 2.5V
Output current source capability: -1mA to 25mA
Resistencia de salida = 0.025 (peor caso segun curvas del fabricante).
NOTA: Peor caso (simulación) Iout = -254uA por canal. Utilizo 2 referencias, una cada 2 canales.



ADC - OPAMP SUPPLY



TITLE:	ADC Channels		
PROJECT:	Calibrador y caracterizador de sondas de corriente		
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DESIGNER:	Agustín Aon Sanchez	**REVISOR:**	Ignacio Carugati