

1 2 3 4 5 6

A

A

B

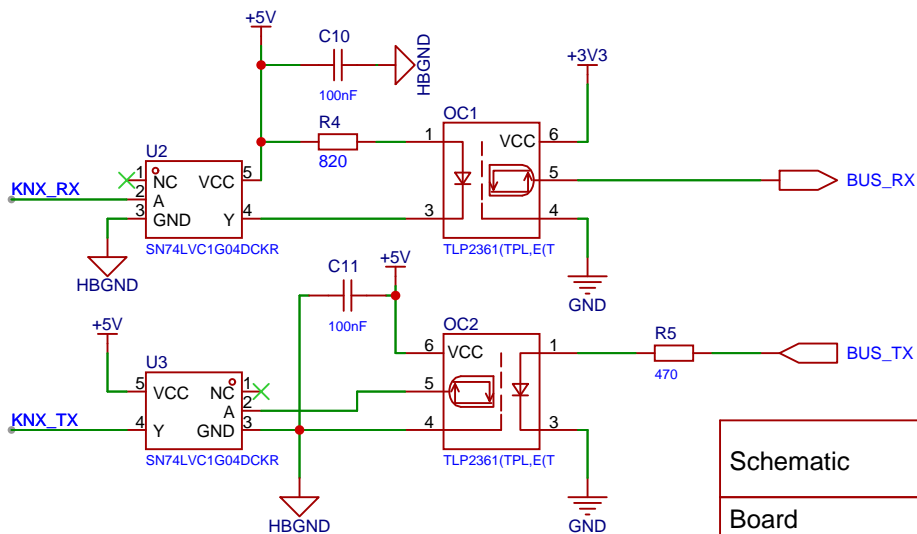
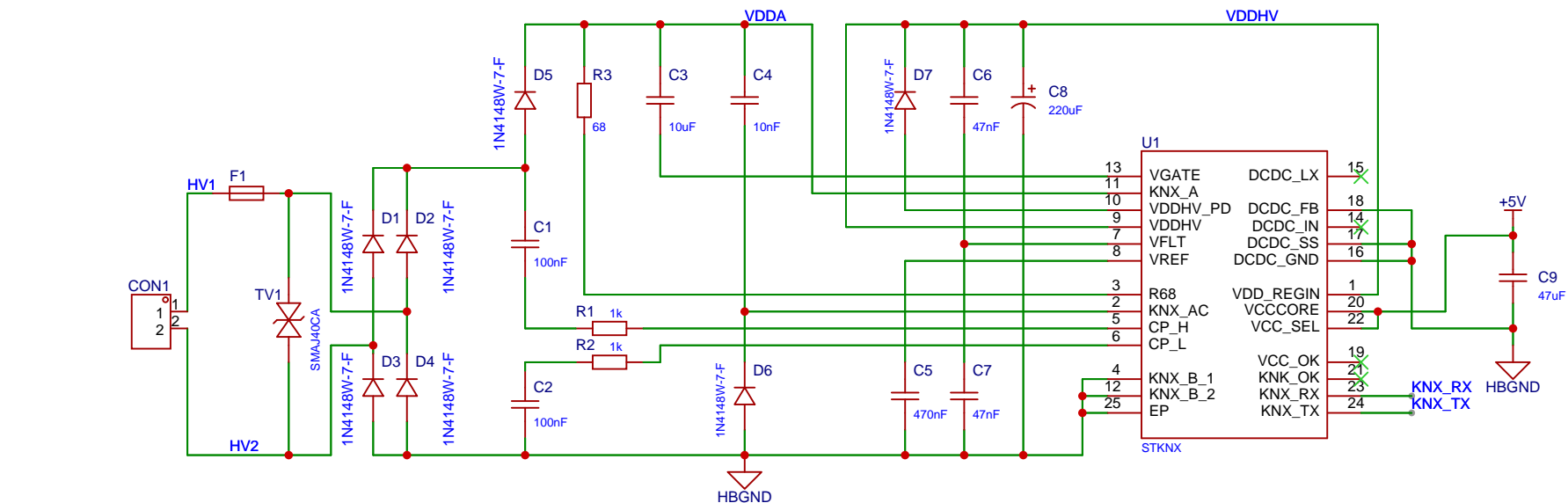
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C

C

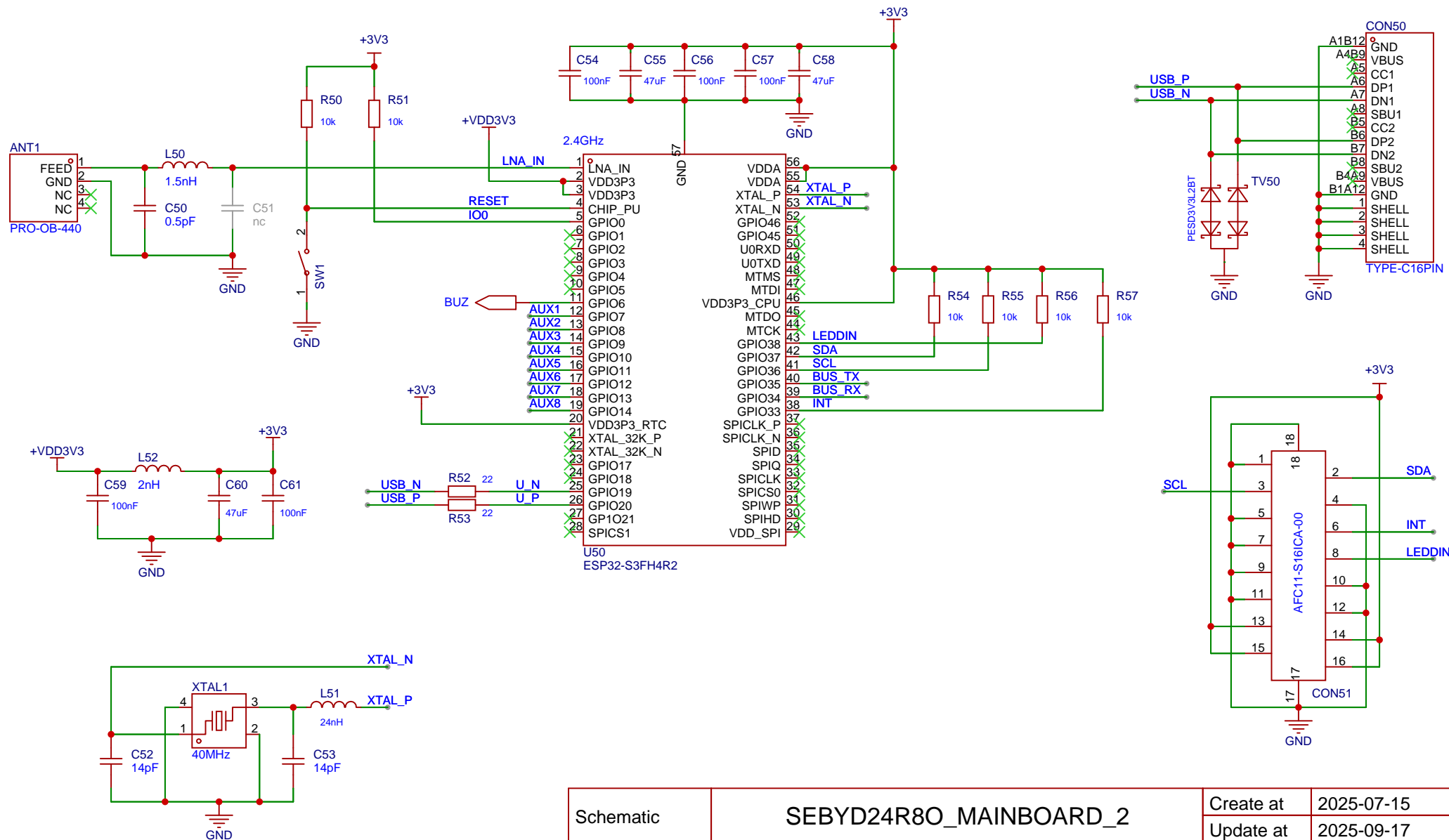
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D

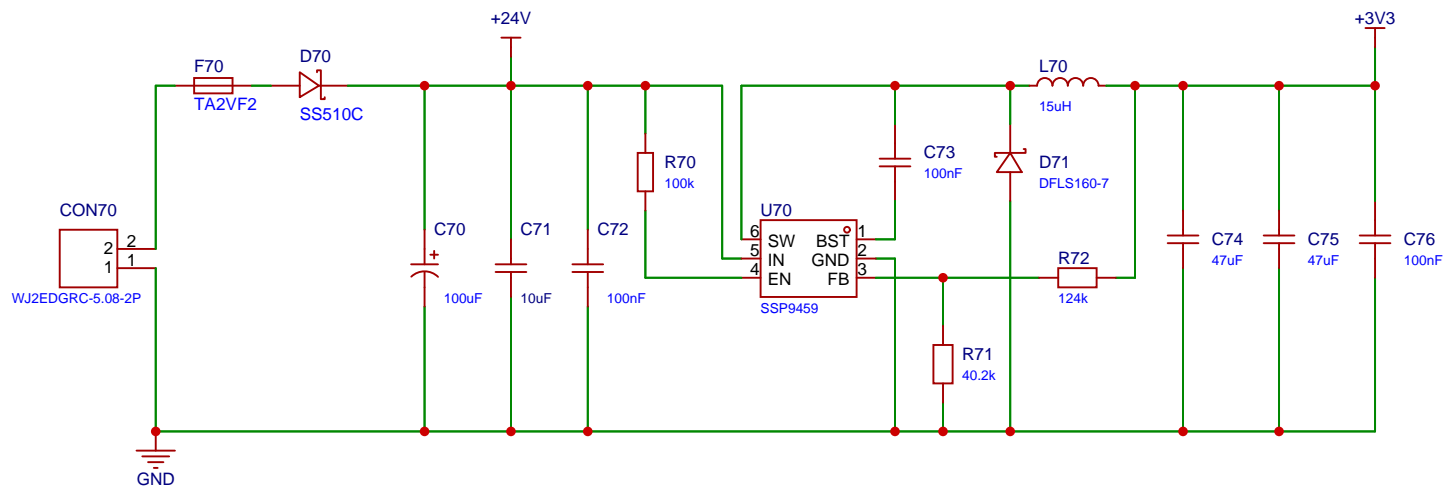



Schematic	SEBYD24R8O_MAINBOARD_2			Create at	2025-07-15
				Update at	2025-09-17
Board	SEBYD24R8O_1.1_PW			Page	BUS Interface
Drawn		SebyD24R8O			
Reviewed					
		Version	Size	Page 1 Total 5	
		V1.1	A4	Elseby	

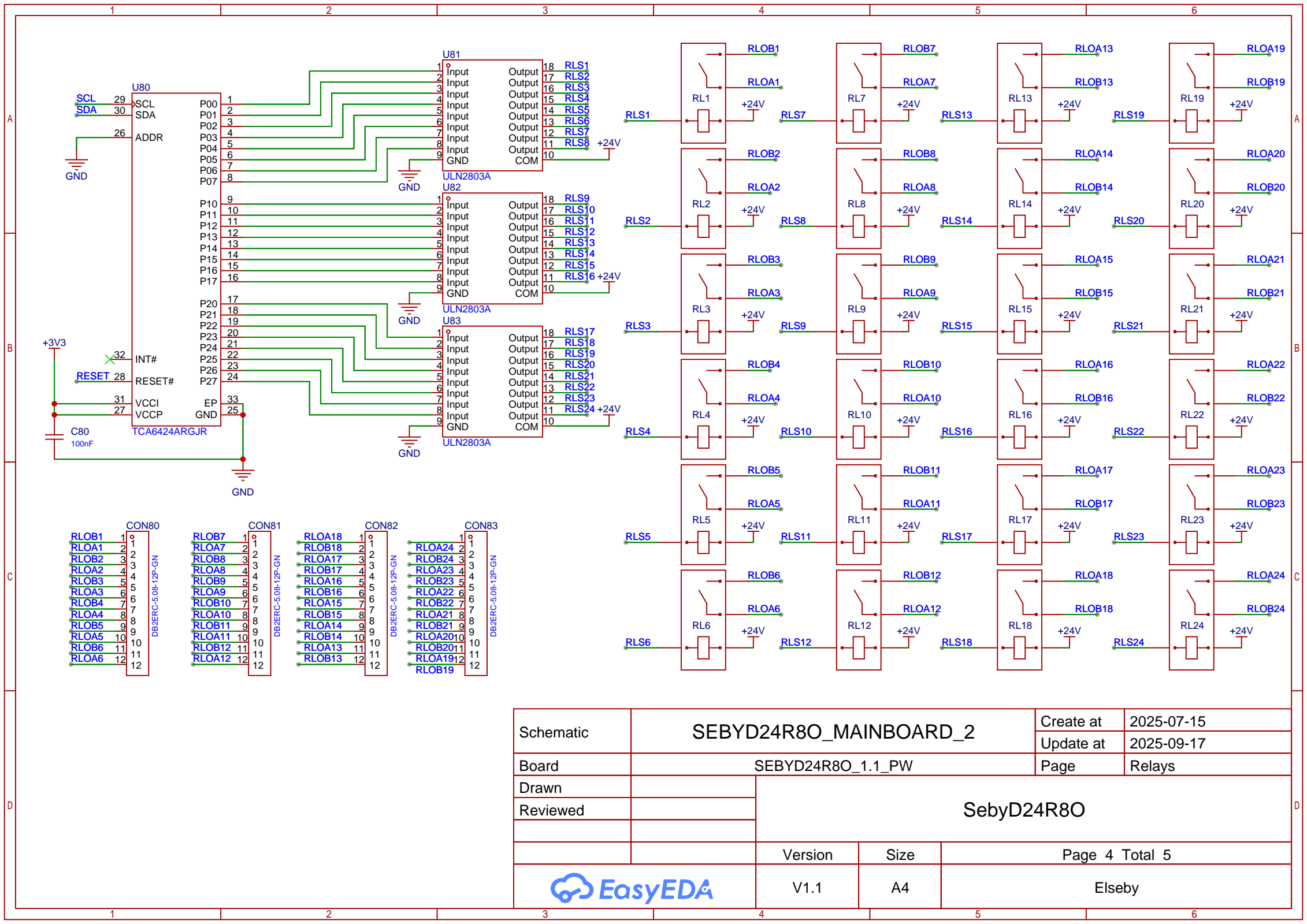




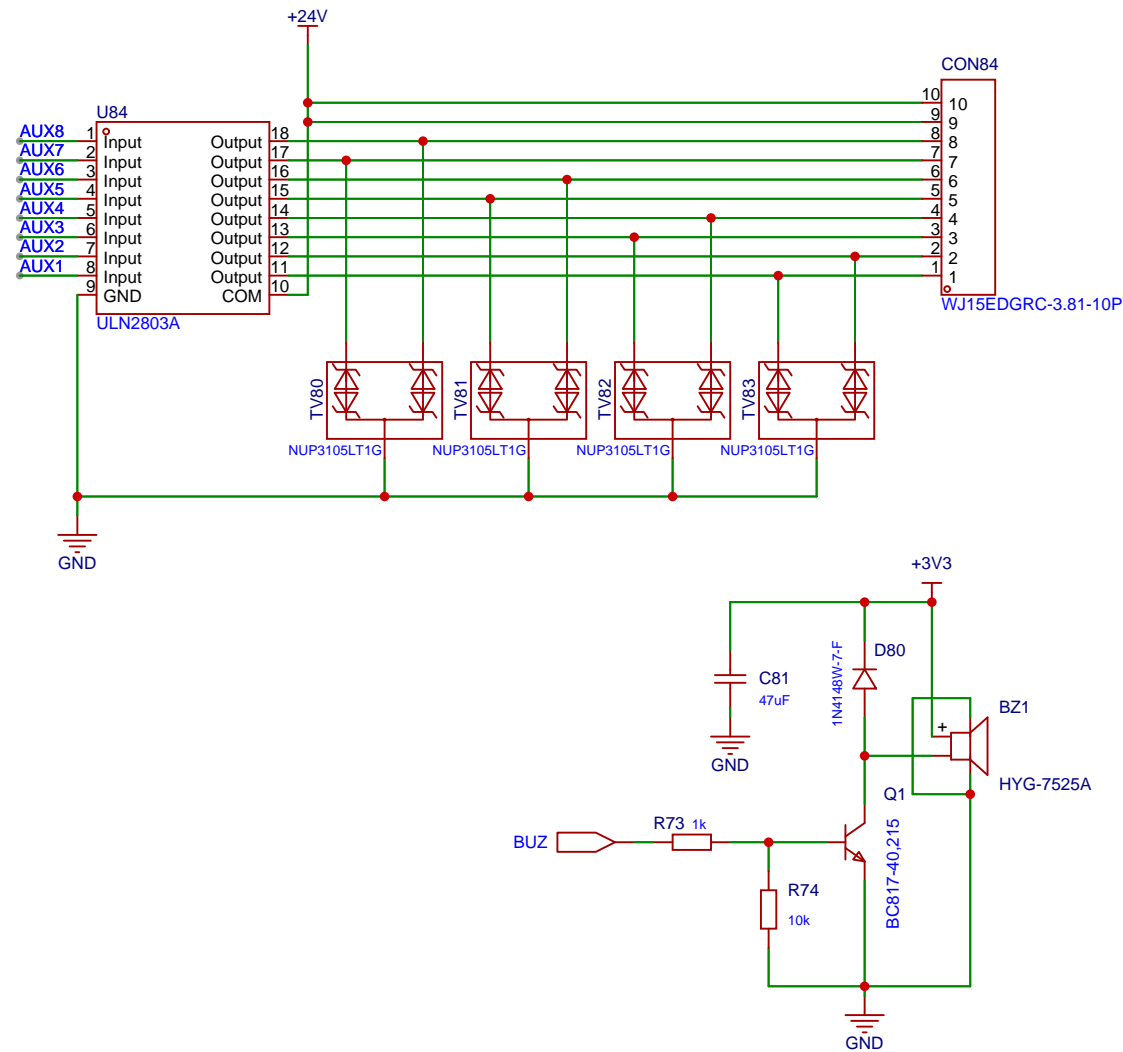
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Board	SEBYD24R8O_1.1_PW		Page	CPU
Drawn	SebyD24R8O			
Reviewed				
		Version	Size	Page 2 Total 5
		V1.1	A4	Elseby




Schematic	SEBYD24R8O_MAINBOARD_2	Create at	2025-07-15
		Update at	2025-09-17
Board	SEBYD24R8O_1.1_PW		Page
Drawn	SebyD24R8O		
Reviewed			
	Version	Size	Page 3 Total 5
		V1.1	Elseby



Schematic	SEBYD24R8O_MAINBOARD_2		Create at	2025-07-15
			Update at	2025-09-17
Board	SEBYD24R8O_1.1_PW		Page	Relays
Drawn	SebyD24R8O			
Reviewed				
		Version	Size	Page 4 Total 5
EasyEDA		V1.1	A4	Elseby



Schematic	SEBYD24R8O_MAINBOARD_2			Create at	2025-07-15
				Update at	2025-09-17
Board	SEBYD24R8O_1.1_PW			Page	Aux Output Buzzer
Drawn		SebyD24R8O			
Reviewed					
		Version	Size	Page 5 Total 5	
		V1.1	A4	Elseby	