

Implementation and Performance Analyses of a Highly Efficient Algorithm for Pressure-Velocity Coupling

Implementierung und Untersuchung einer hoch effizienten Methode zur
Druck-Geschwindigkeits-Kopplung
Master-Thesis von Fabian Gabel
Tag der Einreichung:

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Erklärung zur Master-Thesis

Hiermit versichere ich, die vorliegende Master-Thesis ohne Hilfe Dritter nur mit den angegebenen Quellen und Hilfsmitteln angefertigt zu haben. Alle Stellen, die aus Quellen entnommen wurden, sind als solche kenntlich gemacht. Diese Arbeit hat in gleicher oder ähnlicher Form noch keiner Prüfungsbehörde vorgelegen.

Darmstadt, den 5. März 2015

(Fabian Gabel)

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TODO LIST

- Bilder zum Konvergenzverlauf sind gut
- wall boundary condition treatment could improve convergence
- say that viscous dissipation is not accounted for in the energy balance
- midpoint integration rule or midpoint rule of integration
- remove equation numbering when there is no reference
- change the matrix coefficient indexing to $a_p^{u_i,p}$ like in [15].
- extend nomenclature
- align equations using the **alignat** environment
- check pressure correction equation on iteration indices (gradients)
- check simple chapter, since the right hand side lacks of deferred corrector and under-relaxed velocities
- mention the boundary conditions for the pressure correction
- check all headings for correct spelling
- mention that for an unknown velocity field the partial differential equation for the temperature is non-linear as well
- consistent use of either temperature or energy equation
- check the signs in the Boussinesq approximation
- pressure weighted interpolation method for large body forces?
- not only speed but also improvement of robustness
- instationary flows?
- align exponents in the equation for the Newton-Raphson linearization
- define the term consistent
- add citations to clipper, opencascade and maple, ICEM CFD
- a priori exact solution use this formulation
- Falsche Wahl der problem domain, führt zu global nicht erfüllter kontinuieritätsgleichung. residuum der druckkorrektur entspricht dem massenfluss
- reference Comparison of finite-volume numerical methods with staggered and colocated
- read klaij again and use some of its arguments grids
- emphasize the flexibility of the implementation
- This is often referred to as "matrix-free", though it is still a Mat in PETSc (Mat in PETSc means "finite-dimensional linear operator").

8 Comparison of Solver Concepts

8.1 Parallel Performance

In many cases, scientific code is used to solve complex problems regarding memory requirements to make calculation results available within short time. In both scenarios, code that is able to run in parallel can alleviate the mentioned challenges. Code that runs in parallel can allocate more memory resources which makes the calculation of complex problems feasible. If the code is scalable the program execution can be shortened by using more processors to solve a problem of constant size.

The solver framework that has been developed in the course of the present thesis, has been parallelized using the PETSc library. After introducing the used hardware and software, the central measures of parallel performance are presented. Then preliminary test results using low-level benchmarks are performed, which establish upper performance bounds on the parallel efficiency and the scalability of the developed solver framework. The results of the efficiency evaluation of the solver framework is presented in the last subsections.

8.1.1 Employed Hardware and Software – The Lichtenberg-High Performance Computer

All performance analyses that are presented in this thesis were conducted on the Lichtenberg-High Performance Computer, also known as *HHLR* (*Hessischer Hochleistungsrechner*) [1]. The cluster consist of different sections according to the used hardware. Throughout the thesis, tests were performed using the first and the second MPI section of the cluster. The first section consists of 705 nodes of which each runs two Intel®Xeon®E5-2670 processors and offers 32GB of memory. The second section consists of 356 nodes of which each runs two Intel Xeon E5-2680 v3 processors and offers 64GB of memory. As interconnect for both sections FDR-14 InfiniBand is used.

All tests programs were compiled using the Intel compiler suite version 15.0.0 and the compiler options

—O3 —xHost

As MPI implementation Open MPI version 1.8.2 was chosen. Furthermore the PETSc version 3.5.3 was configured using the options

```
—with—blas—lapack—dir=/shared/apps/intel/2015/composer_xe_2015/mkl/lib/intel64/ \
—with—mpi—dir=/shared/apps/openmpi/1.8.2_intel \
COPTFLAGS="—O3 —xHost" \
FOPTFLAGS="—O3 —xHost" \
CXXOPTFLAGS="—O3 —xHost" \
—with—debugging=0 \
—download—hypre \
—download—ml
```

It should be noted that as the configurations options show, to maximize the efficiency of PETSc, a math kernel library should be used that has been optimized for the underlying hardware architecture as is in the case of the present thesis the Intel *MKL* (*Math Kernel Library*). It should be noted that also the Open MPI library has been compiled using the Intel compiler suite.

8.1.2 Measures of Performance

This section establishes the needed set of measures to evaluate the performance of a solver program, which will be used in the following sections. The first measure is the plain measure of runtime T_p taken by a computer to solve a given problem, where $P \in \mathbb{N}$ denotes the number of involved processes. This so called *wall-clock* time can be measured directly by calling subroutines of the underlying operating system and corresponds to the human perception of the time, that has passed. It must be noted, that this time does not correspond to the often mentioned *CPU* time. In fact, CPU time is only one contributor to wall-clock time. Wall-clock time further contains the time needed for communication and I/O and hence considers idle states of the processor. On the other side CPU time only considers the time in which the processor is actively working. This makes wall-clock time not only a more complete but also more accurate time measure when dealing with parallel processors, since processor idle times due to communication are actively considered while neglected in CPU time.

While wall-clock time is an absolute measure that can be used to compare different solver programs, further relative measures are needed to evaluate the efficiency of one program regarding the parallelisation implementation. The main purpose of these measures is to attribute the different causes of degrading efficiency due to heavy parallelisation to the different contributing factors. A simple model [22, 56] considers three contributions, that form the total efficiency

$$E_p^{tot} = E_p^{num} \cdot E_p^{par} \cdot E_p^{load}.$$

The *numerical efficiency*

$$E_p^{num} := \frac{\text{FLOPS}(1)}{P \cdot \text{FLOPS}(P)}$$

considers the degradation of the efficiency of the underlying algorithm due to the parallelisation. Many efficient algorithms owe their efficiency to recursions inside the algorithm. In the process of decomposing this recursions, the efficiency of the algorithm degrades. It follows that this efficiency is completely independent of the underlying hardware.

The *parallel efficiency*

$$E_p^{par} := \frac{\text{TIME}(\text{parallel Algorithm on one processor})}{P \cdot \text{TIME}(\text{parallel Algorithm on } P \text{ processors})}$$

describes the impact of the need for inter process communication, if more than one processor is involved in the solution process. It should be noted, that this form of efficiency does explicitly exclude any algorithm related degrading, since the time measured corresponds to the exact same algorithms. It follows that the parallel efficiency only depends on the implementation of the communication and the hardware related latencies.

The *load balancing efficiency*

$$E_p^{load} := \frac{\text{TIME}(\text{calculation on complete domain})}{P \cdot \text{TIME}(\text{calculation on biggest subdomain})}$$

is formed by the quotient of the wall times needed for the complete problem domain and partial solves on subdomains. This measure does neither depend on hardware nor on the used implementation. Instead it directly relates to the size and partition of the grid.

It is not possible to calculate all three efficiencies at the same time using only plain wall clock time measurements of a given application. Different solver configurations have to be used to calculate them separately. Since the focus of investigation of the present thesis does not lie on load balancing, for the remainder of the thesis $E_p^{load} = 100\%$ is assumed. This does not present a considerable drawback, since an ideal load balancing is easily obtainable nowadays by the use of sophisticated grid partitioning algorithms [?] REFERENCES. Using identical algorithms for different numbers of involved processes implicitly achieves $E_p^{num} = 100\%$. In this case the parallel efficiency of an application can be measured through the quotient of the needed wall clock time. To measure the numerical efficiency of an algorithm the respective hardware counters have to be evaluated. This can be done using the built in log file functionality of PETSc as presented in section REFERENCE. Hence the determination of numerical efficiency does not rely on wall clock time.

Another common performance measure is the *Speed-Up*

$$S_p = \frac{T_1}{T_p} = P \cdot E_p^{tot}.$$

Speedup and parallel efficiency characterize the parallel scalability of an application and determine the regimes of efficient use of hardware resources.

8.1.3 Preliminary Upper Bounds on Performance – The STREAM Benchmark

Scientific applications that solve partial differential equations rely on sparse matrix computations, which usually exhibit the sustainable memory bandwidth as bottleneck with respect to the runtime performance of the program [29]. The purpose of this section is to establish a frame in terms of an upper bound on performance in which the efficiency of the developed solver framework can be evaluated critically. As common measure for the maximum sustainable bandwidth, low-level benchmarks can be used, which focus on evaluating specific properties of the deployed hardware architecture. In this case the STREAM benchmark suite [44, 45] provides apt tests, which are designed to work with data sets that exceed the cache size of the involved processor architecture. This forces the processors to stream the needed data directly from the memory instead of reusing the data residing in their caches. These types of tests can be used to calculate an upper bound on the memory bandwidth for the CAFFA framework.

In terms of parallel scalability, the STREAM benchmark can also be used as an upper performance bound. According to [5] the parallel performance of memory bandwidth limited codes correlates with the parallel performance of the STREAM benchmark, i.e. a scalable increase in memory bandwidth is necessary for scalable application performance. The intermediate results of the benchmark can then be used to test different configurations that bind hardware resources to the involved processes. Before presenting results the different binding configurations will be explained.

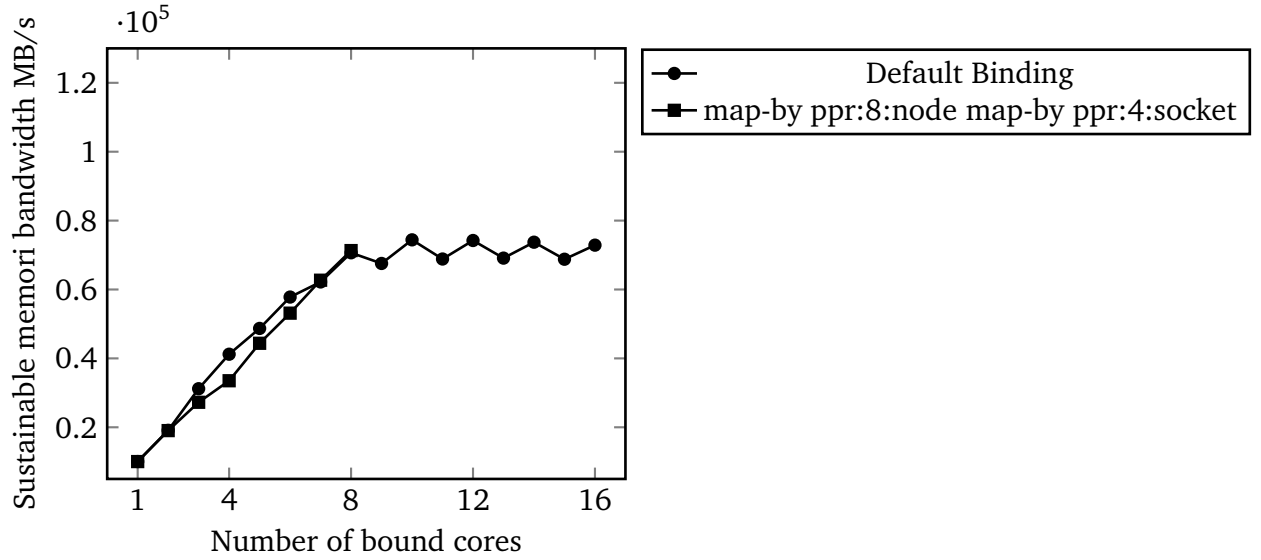


Figure 10: Sustainable memory bandwidth as determined by the STREAM benchmark (Triad) for different process binding options on one node of the MPI1 section

The first configuration sequentially binds the processes to the cores beginning on the first socket. When every core has a bound process the binding algorithm binds the following processes to cores of the second socket. The second configuration binds the processes in a round robin manner regarding the sockets. This configuration in difference to the second configuration binds one process to three cores. Figures ??,?? and ?? demonstrate the different binding options for two sockets and processors with twelve cores each, when eight processes are to be bound to the resources.

As can be seen from figures REFERENCE, the scaling of the sustainable bandwidth behaves rather erratic, such that for process counts up until eight for the MPI1 section no reliable results can be obtained from the STREAM benchmark. It is assumed that this kind of behaviour is due to the automatic turbo boost the deployed processors apply, which can be controlled other than by turning it off. Since all performance measures are relative but the plain measurement of wall clock time the reference value for the following performance measurements will be taken from the program execution for the maximum number of processes that can be bound without overlap to one deployed socket.

8.1.4 Optimization of Sequential Solver Configuration

Compare runtime for different solver configurations BiCGStab+ICC, different multigrid algorithms

8.1.5 Speedup Measurement and Impact of Coupling Algorithm for Analytic Test Cases

This section presents the results from the speedup measurements conducted on the supercomputer HHLR which has been presented in section ??. Furthermore the impact of the scaling algorithm on the running time of the corresponding solver program will be shown.

8.2 Realistic Testing Scenario – Complex Geometry

The flow inside closed systems is This section compares the single process performance of the segregated and the fully coupled solution algorithm for a flow problem within a complex geometry. The geometry of the domain is based on a channel flow problem with square cross section, with the difference that inside the channel reside two obstacles with a square cross section of which one has been twisted against the other. Figure 14 shows a sketch of the problem domain.

This case exercises all of the previously introduced boundary conditions for flow problems including the treatment of non-matching block boundaries. For the velocities at the inflow boundary the parabolic distribution

$$\mathbf{u}(x_1, x_2, x_3) = \begin{bmatrix} u_1 \\ u_2 \\ u_3 \end{bmatrix} = \begin{bmatrix} \frac{16 * 0.45 * x_2 * x_3 * (0.41 - x_2) * (0.41 - x_3)}{0.41^4} \\ 0 \\ 0 \end{bmatrix}$$

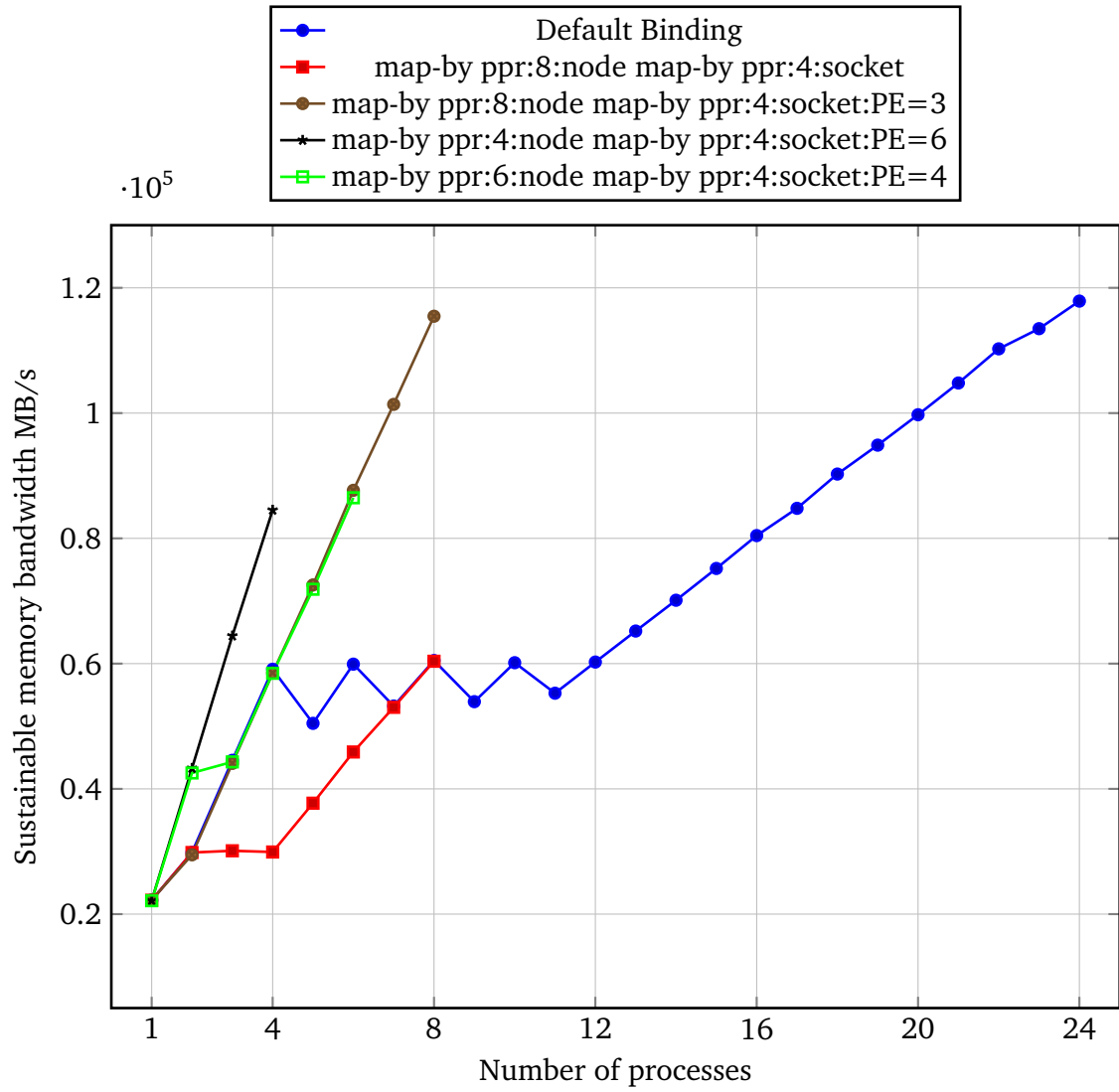


Figure 11: Sustainable memory bandwidth as determined by the STREAM benchmark (Triad) for different process binding options on one node of the MPI2 section

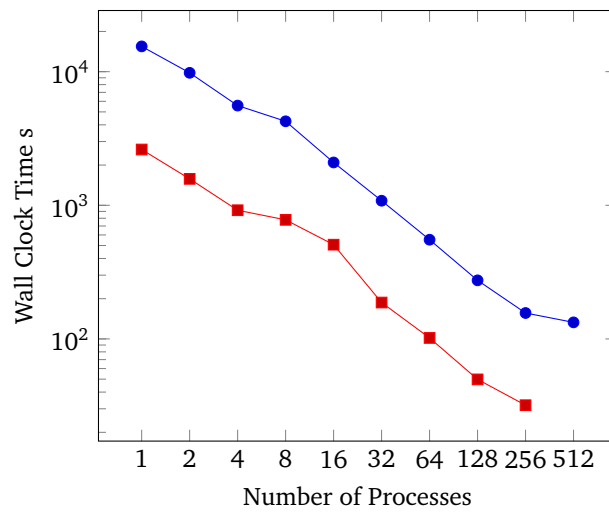


Figure 12: Wall clock time comparison for segregated and fully-coupled solution algorithm solving for an analytical solution on a grid with 128^3 cells

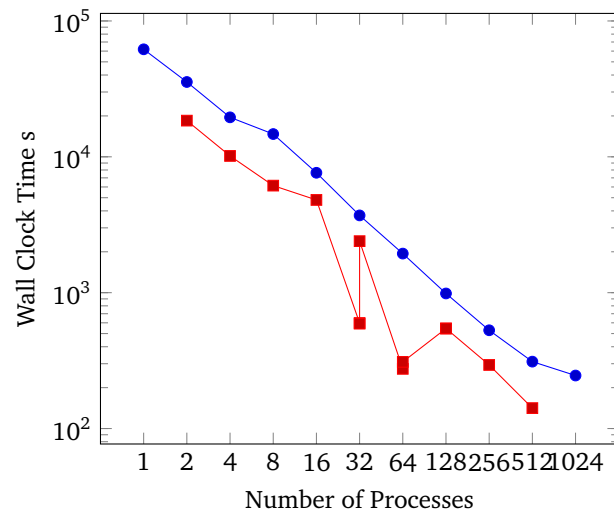


Figure 13: Wall clock time comparison for segregated and fully-coupled solution algorithm solving for an analytical solution on a grid with 256^3 cells

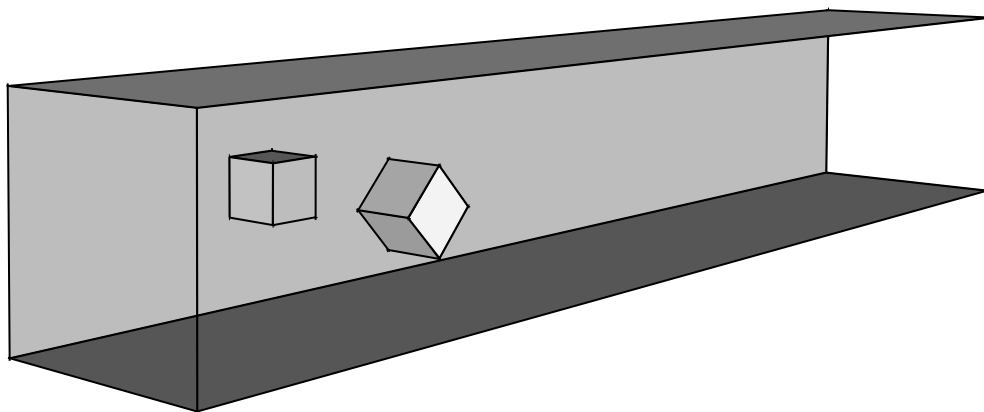


Figure 14: Sketch of the channel flow problem domain

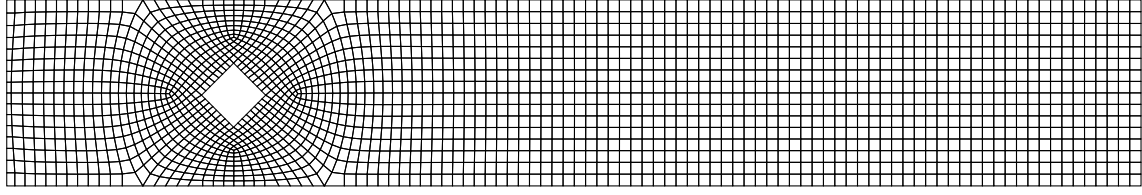


Figure 15: NONE

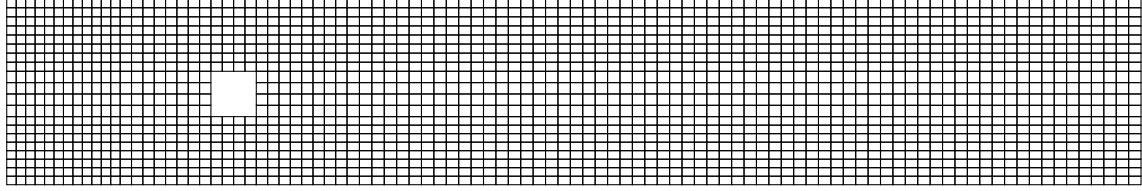


Figure 16: NONE

was chosen. All the other problem parameters were chosen such that the flow problem resides in the regime of a non-turbulent stationary flow for which the presented solver framework has been developed. Table 4 lists the remaining material and geometrical characteristics of the test case.

Property	Value	Unit
Density	1E-3	$\frac{kg}{m^3}$
Viscosity	1E-0	K.A.
Height	0.41	m
Length	2.5	m
Sidelength	0.1	m
Under relaxation u	0.9	
Under relaxation p	0.1	

Table 4: Characteristic problem properties used in the channel flow test case

The present test case shows one advantage of the treatment of block boundaries, which has been introduced in section REFERENCE. Since no assumptions on the geometry of a neighboring block are necessary each block can be constructed independently which increases the flexibility of the meshing of geometries. Furthermore, because of the fully implicit handling of block boundaries, the number of used blocks does not impact on the convergence properties of the deployed linear solvers. Figure ?? and figure ?? show the mesh at the left and right bounding walls. It is evident that this mesh leads to non-trivial transitions between the blocks.

The solution of the linear systems resulting from the discretization of the problem takes up more time for the coupled solution algorithm than in the segregated coupled algorithm. For small problem sizes the additional overhead for the solution methods for linear systems and the property that the segregated solution algorithm does not need many outer iterations to converge leads to the conclusion that moderate to big problems with respect to the number of involved unknowns are necessary for the coupled solution algorithm to dominate. In contrast to the segregated algorithm, the fully coupled solution algorithm achieves a approximately constant amount of needed outer iterations, independent of the number of involved unknowns. The tests regarding the weak scaling of the coupled solution algorithm emphasize this property. Table 5 compares the measured wall clock time for different numbers of unknowns. The mesh shown in ?? and ?? was generated for the first number of unknowns. The two other numbers result from up to two times bisectioning the mesh in each direction, every time scaling the number of unknowns by a factor of eight.

8.3 Classical Benchmarking Case – Heat-Driven Cavity Flow

This section deals with the evaluation and comparison not only of the pressure-velocity coupling but also of the velocity-to-temperature coupling through the Boussinesq approximation and the temperature-velocity/pressure coupling through the Newton-Raphson linearization of the convective term in the temperature equation. For this the standard heat-driven

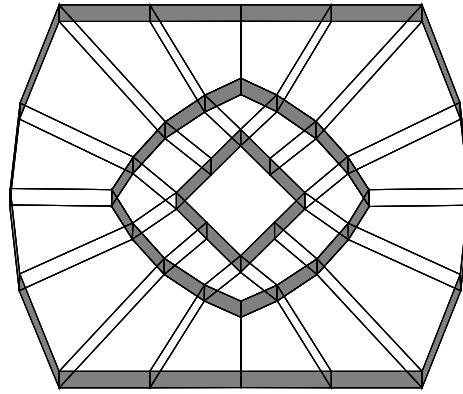


Figure 17: NONE

No. of Unknowns	Seg. - time	Cpld - time	Seg. - its	Cpld - its
75768	0.2226E+02	0.3645E+02	151	67
408040	0.4053E+03	0.1500E+03	355	42
2611080	1.1352E+05	0.3105E+04	1592	39

Table 5: Characteristic problem properties used in the channel flow test case

cavity flow CITATION is adapted for a three dimensional domain and the material and geometric parameters are chosen such that a non-turbulent stationary flow exists, which means that the solution lies within the regime of approximations made by the solver.

Essential for this benchmarking case is the nature of the flow. The fluid motion is a consequence of the effect of volume forces caused by temperature differences in the solution domain. This relation is represented by the Rayleigh and Prandtl number. It is assumed that for this kind of flow the fully implicit treatment of the temperature coupling will yield further benefits with respect to wall time, compared with solution approaches that solve for the temperature separately.

- http://www.featflow.de/en/benchmarks/cfdbenchmarking/mit_benchmark.html

Describe Testing Setup (Boundary conditions and grid). Present results and compare them with literature.

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