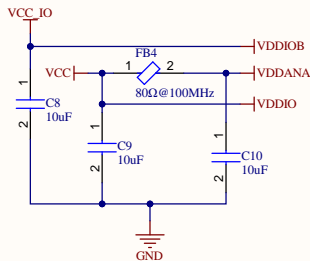
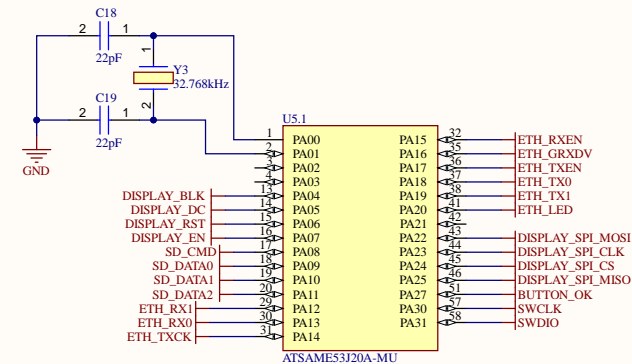


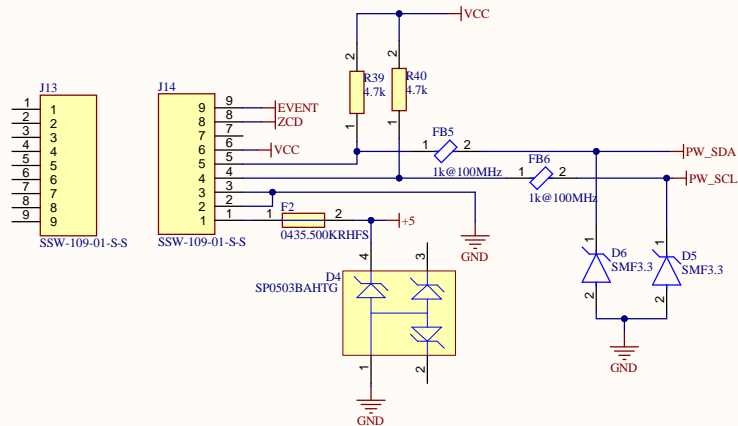
Napájení MCU



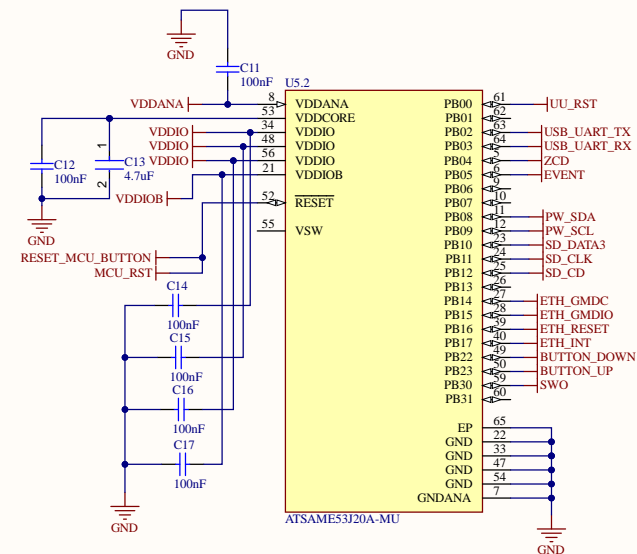
MCU



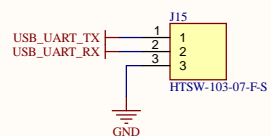
Headery pro připojení "power-board"



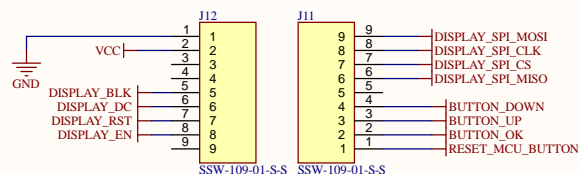
MCU



UART Debug Piny



Headery pro připojení "user-board"



Title			MCU
Size	Number	Revision	
A3	3	1	
Date:	7/22/2024	Sheet of	
File:	\\..DMP_SEM_MCU.SchDoc	Drawn By:	Matěj Tománek

The schematic diagram illustrates the HJF11-2450E-L12RL module and its connections. The module is represented by a yellow rectangle with pins numbered 1 through 12. The connections are as follows:

- Pin 1:** TX_P
- Pin 2:** TX_N
- Pin 3:** RX_P
- Pin 4:** RX_N
- Pin 5:** TCT
- Pin 6:** RCT
- Pin 7:** RJ45_GND
- Pin 8:** RJ45_GND
- Pin 9:** PHY_LED0
- Pin 10:** PHY_LED0
- Pin 11:** ETH_LED
- Pin 12:** ETH_LED

The module is connected to a power supply (VCC) and ground (GND) through resistors R18, R19, and R20. The connections are as follows:

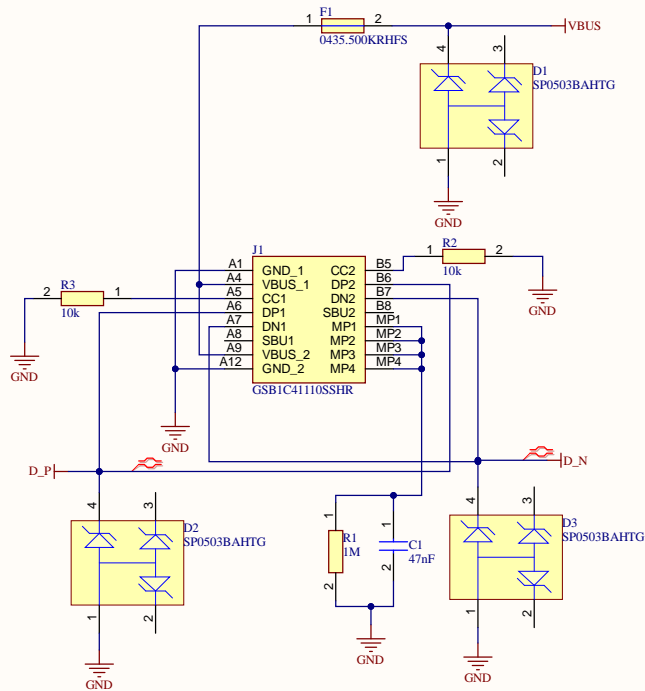
- R18:** Connects Pin 7 to VCC.
- R19:** Connects Pin 8 to VCC.
- R20:** Connects Pin 9 to GND.

The module is also connected to a network of components including a 470KΩ resistor (FB2) and two 100nF capacitors (C24, C25). The connections are as follows:

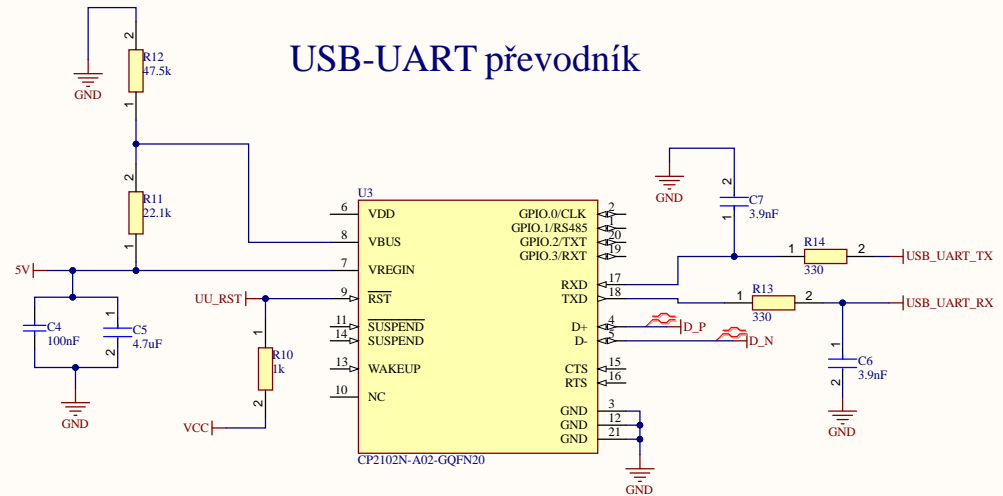
- FB2:** Connects Pin 1 to RJ45_GND.
- C24:** Connects RCT to GND.
- C25:** Connects TCT to GND.

Title		
ETHERNET		
Size	Number	Revision
A3	4	1
Date:	7/22/2024	Sheet of
File:	\\...\DMP_SEM_ETHERNET.SchDoc	Drawn By: Matěj Tománek

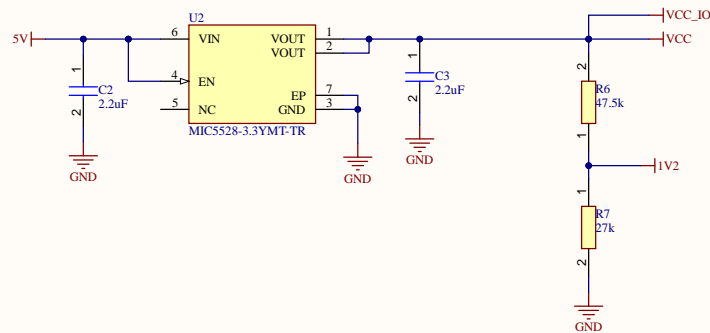
USB-C Konektor



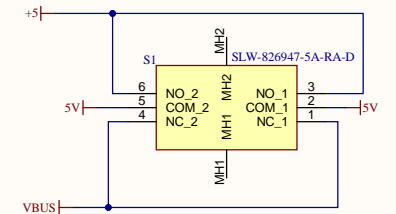
USB-UART převodník



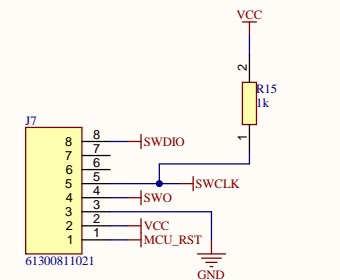
Stabilizátor 5V na 3.3V



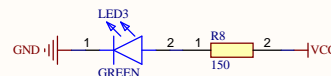
Přepínač napájení



Programátor MCU

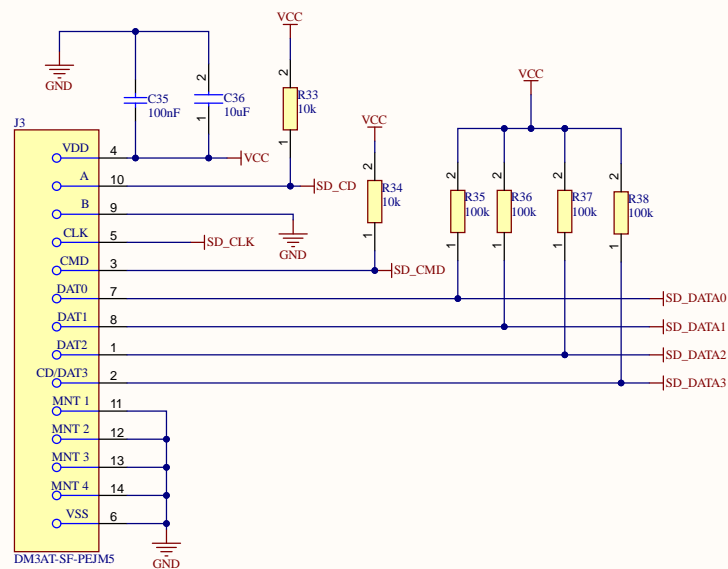


Signalizační led dioda napájení



Title		
Power		
Size	Number	Revision
A3	2	1
Date:	7/22/2024	Sheet of
File:	\\.\DMP_SEM_POWER.SchDoc	Drawn By: Matěj Tománek

Konektor na SD kartu



Title			SD
Size	Number	Revision	
A3	6	1	
Date:	7/22/2024	Sheet of	
File:	\\.\DMP_SEM_SDCARD.SchDoc	Drawn By:	Matěj Tománek