Eric Lee Turner

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EDUCATION

University of California - Berkeley
Ph.D. in Electrical Engineering and Computer Sciences — May 2015
3D and 2D surface reconstruction algorithms for architectural modeling
System hardware design and assembly
GPA: 4.00/4.00

University of California - Berkeley
M.S. in Electrical Engineering and Computer Sciences — May 2013
GPA: 4.00/4.00

Carnegie Mellon University
B.S. in Electrical and Computer Engineering — May 2011
QPA: 3.91/4.00 - Dean's List
Minors in Physics, Computer Science

WORK EXPERIENCE

Indoor Reality, Inc. — 09/2015 - Present
Chief Technical Officer (CTO)
Principal Investigator on multiple grants.
Technology lead in developing hardware and software for indoor building modeling

Signetron, Inc. — 07/2015 - Present
Software Architect
Developed algorithms and software for automatic indoor modeling

EECS Department - UC Berkeley — 01/2015 - 05/2015
Graduate Student Instructor
Taught discussion sections, held office hours, graded homeworks/exams

Speir Technologies — 01/2013 - 01/2014
Software Development Consultant
Developed prototype demo application and 3D modeling algorithms

MIT Lincoln Laboratory — 05/2011 - 08/2011
Summer Intern - Group 104: Intelligence and Decision Theory
Developed algorithms for creation of synthetic test data for SAR CCD track-finding

Qualcomm — 05/2010 - 08/2010
Software Summer Intern - QCT Modem Integration Team
Developed/automated methodology for optimizing and removing redundancies in client specs of processor builds

COMPUTER SKILLS

Programming Languages: Java, C/C++, BASH, Python, SML, Basic, NASM, x86, Perl, JavaScript
Markup Languages: HTML, LaTeX
Software: Matlab, Mathematica, Maple, Unity, Autodesk, Visual Studio
Frameworks: Eigen, OpenCV, OpenGL, Qt, Android, Spring, Processing, XStream

AWARDS

Awarded Best Student Paper - GRAPP 2014 — 01/2014
Awarded NSDEF Fellowship — 09/2013 - 05/2016
Presented at CMU Meeting of the Minds — 05/2011
1st Place Lockheed Martin ECE Undergraduate Project
3rd Place CIT Honors Research Poster Competiton