



28-PIN SOIC, SPDIP, SSOP (TOP VIEW) (1,2,3)

PIC32MX210F016B
PIC32MX220F032B
PIC32MX230F064B
PIC32MX230F256B
PIC32MX250F128B
PIC32MX270F256B

Pin #	Full Pin Name	Pin #	Full Pin Name
1	MCLR	15	VDD1
2	PGEDVDD1<QW>+ANNC3NCRPACTED11PMD7RA0	16	TDORPBE1CTED3PMD5INTURB7
3	PGEDVDD1<QW>+ANNC3NCRPACTED11PMD7RA1	17	TDORPBE1CTED3PMD5INTURB8
4	PGEDVDD1<QW>+ANNC3NCRPACTED11PMD7RA2	18	TDORPBE1CTED3PMD5INTURB9
5	PGEDVDD1<QW>+ANNC3NCRPACTED11PMD7RA3	19	VDD1
6	ANNC1NCRPACTED11PMD7RA4	20	VDD1
7	ANNC1NCRPACTED11PMD7RA5	21	PGEDVDD1<QW>+ANNC3NCRPACTED11PMD7RA6
8	VDD1	22	PGEDVDD1<QW>+ANNC3NCRPACTED11PMD7RA7
9	PGEDVDD1<QW>+ANNC3NCRPACTED11PMD7RA8	23	VDD1
10	PGEDVDD1<QW>+ANNC3NCRPACTED11PMD7RA9	24	ANNC1NCRPACTED11PMD7RA10
11	PGEDVDD1<QW>+ANNC3NCRPACTED11PMD7RA11	25	ANNC1NCRPACTED11PMD7RA11
12	PGEDVDD1<QW>+ANNC3NCRPACTED11PMD7RA12	26	ANNC1NCRPACTED11PMD7RA12
13	VDD1	27	ANNC1NCRPACTED11PMD7RA13
14	TDORPBE1CTED3PMD5INTURB5	28	ANNC1NCRPACTED11PMD7RA14

Note:

- The RPN pins can be used by remappable peripherals. See Table 1 for the available peripherals and Section 11.3 "Peripheral Pin Select" for restrictions.
- Every I/O port pin (RAN-RCX) can be used as a change notification pin (CNM-CHN). See Section 11.8 "I/O Ports" for more information.
- Shaded pins are 5V tolerant.

Figure 10-2. Debugger Connector Pinout

