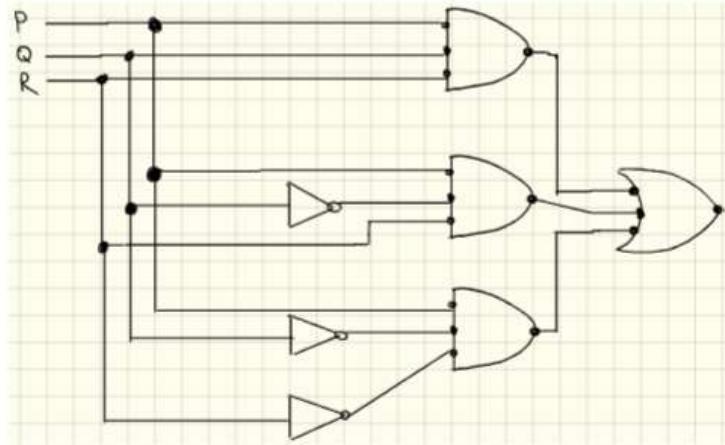


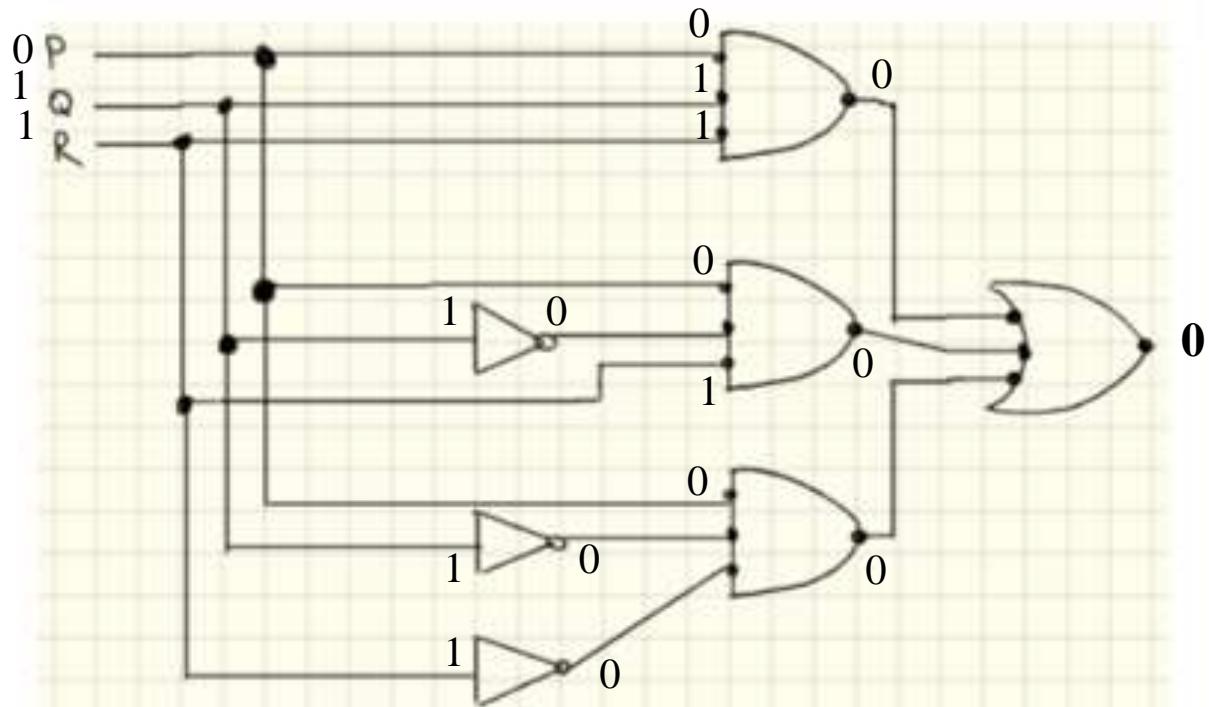
IX.2. For the circuit given below



- What is the output signal for the input signals  $P = 0, Q = 1, R = 1$ ?
- What logical formula in Disjunctive Normal Form (DNF) describes the behaviour of the circuit?
- Use equivalence laws to simplify the formula from item (b).

## Solution:

(a):



**(b):**

$$(P \wedge Q \wedge R) \vee (P \wedge \neg Q \wedge R) \vee (P \wedge \neg Q \wedge \neg R)$$

**(c):**

$$(P \wedge Q \wedge R) \vee (P \wedge \neg Q \wedge R) \vee (P \wedge \neg Q \wedge \neg R)$$

$$= P \wedge ((Q \wedge R) \vee (\neg Q \wedge R) \vee (\neg Q \wedge \neg R))$$

$$= P \wedge ((Q \wedge R) \vee (\neg Q \wedge \underbrace{(R \vee \neg R)}_{True}))$$

*True*

$$= P \wedge ((Q \wedge R) \vee \underbrace{(\neg Q \wedge T)}_{\neg Q})$$

$\neg Q$

$$= P \wedge ((Q \wedge R) \vee \neg Q)$$

$$= P \wedge ((\underbrace{\neg Q \vee Q}_{True}) \wedge (\neg Q \vee R))$$

$$= P \wedge (T \wedge (\neg Q \vee R))$$

$$= P \wedge (\neg Q \vee R)$$