





















1	2 3		4		5	6
U1D	U1F	U1H		•		
TMS320F28388D 60	TMS320F28388D	TMS320F28388D		Sheet: OMODRI_ETHERCAT	C_CONNECT	
EDWM17 C8 M1 DWM1 CHY	CPIO6/1 L1/		CAT1_INT	Sheet: OMODRI_ETHERCAT ESC_TX1_CLKE	ESC_TX1_CLK	
	SPIR CLIN K16 DCOM SPI CIK	= = = = = = = = [1() FS	(C Σ1 ΕΝΙΔ	FSC TX1 FNAC	ESC_TX1_ENA	
FPWM2A A7 DM1 PWM2 CHA	GPID66 K1/			2302.7722.6747	TESC TY1 DO	
EPWM2A A7 — DM1_PWM2_CHA EPWM2B B7 — DM1_PWM2_CHB	GPI067 B19 X			ESC_TX1_D0<	ESC_TX1_D0	
EPWM3A D7 DM1_PWM3_CHA	ESC_PHY1_LINKSTATUS C18 ESC_PHY1_LINKSTATUS		C_TX1_D1	ECC TV4 D4 /	S E DC IVI DI	
EPWM3B D7 DM1_PWM3_CHA	SPIC_SIMO B18 DSPI_SIMO			ECC TV4 DO	FDC IXI DZ	
EPWM3B A6 DM1_PWM3_CHB	SPIC_SIMO SEPI_SIMO SPIC_SOMI A17 GSPI_SOMI		C TY1 D2	ESC TX1 D3<	ESC_TX1_D3	
EPWM4A A6 DM2_PWM1_CHA	SPIC_SOMI ATT OSPI_SOMI	ESC_TX1_DATA2 V10 ES ESC_TX1_DATA3 U18 ES	C TV1 D7		ESC DV1 CIV	۸ ا
A EPWM4B B6 DM2_PWM1_CHB	SPIC_CLK B17 DSPI_CLK	ESC_TX1_DATA3 010 ES	C DV4 DV	ESC_RX1_CLKE	ESC_RX1_CLK	[
EPWM5A G2 DM2_PWM2_CHA	SPIC_STEN_BOOT_MODE_1 A16 DSPI_CSn_BOOT_MODE1	ESC_TX1_DATA3 010 13 ESC_RX1_DV T18 ESC	C_RXI_DV	ECC DV4 EDDE	A ESC KYT EKK	
EPWMSB G3 DM2_PWM2_CHB	GPIO73 ALO Y	ESC_RX1_DV T17 ES ESC_RX1_CLK T10 ES	C_RX1_CLK	ESC_RX1_DVD	ESC_KXT_DA	
	GPI074 C17 X	ESC_RX1_CLK	C_RX1_ERR		ESC_RX1_D0	
EDWINED C1 DM2 DWMZ CUB	GPI075 D16 X	ESC RX1 DATAO N19 ES	C_RX1_D0_	ESC_RX1_D0E	ESC BY1 D1	
CANR TY CZ DCAN TY	CD1076 C16	ESC_RX1_DATA0 M19 ES ESC_RX1_DATA1 M19 ES	C_RX1_D1_	ESC_RX1_D1	ESC_RX1_D1	
	CD1077 A15 ACD104	ESC DV1 DATA3 M18 ES	C_RX1_D2_	ESC_RX1_D2	ESC_RX1_D2	
	GPI077 → GPI01 GPI078 → GPI00	119 FS	C PX1 D3	ESC_RX1_D3D	ESC_RX1_D3	
$\begin{array}{c c} & GPIO14 & DZ \times \\ & GPIO15 & DZ \times \\ & & & & & & & \\ & & & & & & & \\ \end{array}$	GPI078 B13 \$GPI00 C15 \$					
GPI015 S	DIE	ESC_LED_LINKO_ACTIVE F17 FC	C LED LINKS ACTIVE		ECC TVO CIV	
SPIA_SIMO E1 DDRV_SPI_SIMO	GPI080 D15 X			ESC_TXO_CLKE	ESC_TX0_CLK	
SPIA_SOMI E2 DRV_SPI_SOMI F3	GPI080 A14	ESC_LED_LINK1_ACTIVE E17 ESC_LED_ERR D18 ESC	C LED DIN	ESC_TXO_ENA<	1 L3C_IXO_LINA	
	GPI082 D14 → GPI02	ESC I ED BIIN DIO L3	C_LED_RUN	EGG TVO DO	FSC TX0 D0	
	CDIO07 C14 V					
	POOT MODE OF A11 FRONT MODES	1=== D.U.S	C_PHYO_LINKSTATUS	ESC_IXO_D1<	FSC TXO D2	
	GDIU82 DIT					
CDIODAL J4 V	GPI086 C11 X			ESC_TX0_D3<	_ <u>F3C_IXO_D3</u>	
EOEDI I NA GENCI CHI	CD1007 D11 V				FSC RXO CIK	
EQEP1_I K4 GENC1_CHI						
	GP1088 C6 X			ESC_RXO_ERRE	ESC_RXO_DV	
	DRV1_EN D6 DRV1_GPIO_EN	ESC_MDIO_DATA B12 EC ECATO_INT C12 ES	ATO INT	ESC_RX0_DVD	255_KX0_B*	
B EQEP2_I H GENC2_CHI	INDVA FAIL INVIAS ANDVA CDIO FAILLE	ECATO_INT C12 EC	C DUV DECET-	ESC BYD DOE	ESC_RXO_DO	B
CDIO27I LL V	SPID_SIMO B5 DWS2812B_CMD	ESC_PHY_RESET D12 ESC_PHY_RESE	C TVO FNA			
GPI028 V11 ×	GP1092 A4 ×					
CDIOCOL WITH	CDIOOZI B4 v			ESC_RXO_DZL	ESC_RX0_D3	
CDIOZO III V	6D1007 A3 14			ESC_RX0_D3E	LSC_KXO_DS	
GPI030 U11 ×	GP1094 B3 X	ESC_TXO_DATA1 D10 ES	C_TX0_D1_			
011031	di 1033			ESC_PHY_MDIO_CLK<	ESC_MDIO_CLK	
U1E	U1G	U1I		ESC_PHT_MDIO_CLK	ESC MDIO DATA	
THCZ20F20Z00D	THC700F00700D	TMS320F28388D BO FGG TVO BY	2	ESC_PHY_MDIO_CLK < ESC_PHY_MDIO_DATA < ESC_PHY_RESETn <	FSC PHY PESETS	
CDIOZO ULO	601006	ESC_TXO_DATA2 B9 ESC_TXO_D2		ESC_PHY_RESETn <	<u>ESC_ITII_RESEIII</u>	
	CDIOO7 A/ X	ESC TXO DATA3 C9 ESC_IXO_D		ANALOG_IND	PANALOG_IN2	
GP1033 U14 X	GPI097 F1 X	FSC RXO DV D9 ESC_RXO_D		ESC_PHY_CLK<	ESC_PHY_CLK	
DRV2_CSn T14 DDRV2_GPIO_CSn	CDIOCOL GI V	ESC_RXO_CLK A8 ESC_RXO_C	:LK			
DRV2_CSn		ESC_RXO_ERR B8 ESC_RXO_E	RR	ESC_PHY1_LINKSTATUSE	ESC_PHY1_LINKSTATUS	
DRV2_FAULTn V16 JDRV2_GPIO_FAULTn		CE FCC DVO D				
DRV2_FAULIN DRV2_EN U16 DDRV2_GPIO_FAULIN GPIO38 T16 X M47		DE ESC DVO D		ESC_I2C_SDA<	SSC_I2C_SDA	
GPI038 110 X	GPI0102 H3 X	C/ 550 DV0 D		ESC_I2C_SDA< ESC_I2C_SCL<	FSC I2C SCI	
CDIOZOL WI/ V	GPI0102 J1 × GPI0103	LUC-IVAC-DATAZ DA FCC DVO D		ESC_I2C_SCL<	250,120,302	
GPIO40 VI/X	CDIO404 JZ X	ESC_RXO_DATA3 D4 ESC_RXO_D	. <u></u>			
CDIO/4 U1/	GPI0105 J3 V	-		ESC LED LINKO ACT	ESC_LED_LINKO_ACTIVE	
USBODM D19 AUSBO N	6010105	Sheet: OMODRI_ANALOG				
LICENDE C19 ALIEBO B	CD104.07 L3 V	M1_la < M1_la				
CDIO/// K18 ×	CD104.00 L4 X			ESC_LED_ERR< ESC_LED_RUN<	FSC LED RUN	C
CDIO/FI K19 W	CDIO400 N2 V	M1_Ib		ESC_LED_RUN<	LUCED_KON	
L L SDIOLE F19		M1_lc				
GP1046 E18 × GP1047 P16 P18 C18		M1_Va << M1_Va		ECATO INT	ECATO_INT	
GPIO47 R16 ESC_PHY_CLK		M1_Vb< M1_Vb		FCAT1 INT	ECAT1_INT	
ESC_PHY_CLK RIO ESC_PHY_CLK		M1_Vc			1	
FSITXA_DO R17 DFSI_TXA_DO	GPI0112 N4 NA	MITTAG COMITTAG		פטו כועי	Laspi cik	
FSITXA D1 NIO DFSI TXA D1	GPI0114 N3 X	M2_la << M2_la		SFI_CLK	GPI0125_SPI_CS2_n	
FSITXA_CLK R19 DFSI_TXA_CLK GPI052 P16 X GPI053 P17 X	GPI0114 N3 × FSIRXC_D0 V12 QFSI_RXC_D0	M2_Ib << M2_Ib				
GPI052 P16 ×	ECIDAC DAI MIO GEGI DAG DA	M2_lc <			SPI_CSn_BOOT_MODE1	
GPI053 P17 V	ECIDAC CITA 015 GECI DAC CITA				dSPI_SIMO	
CDIOE/I F10 V		M2_Va <□M2_Va		SPI_SOMIE	SPI_SOMI	
CRIOSEI P19	CDIO440 III5 v	M2_Vb<──☐M2_Vb				
CDIOECL NED W	CDIO4201 UID V	M2_Vc < ✓ M2_Vc		File: OMODRI_ETHERCAT_0	CONNECT.sch	
GP1056 N18 X GP1057 N18 X						
GPI057 N15 X GPI058 N17 X		ANALOG_IN1 — ANALOG_IN1				
		ANALOG_IN2 — ANALOG_IN2				
SPIB_STEN M17 DCOM_SPI_CSn	GPI0123 U8 X	M1_Vbus (M1_Vbus				
SPIB_SIMO MI / DCOM_SPI_SIMO	GPI0124 V8 × GPI0125 T9 GPI0125_SPI_CS2_n	HIT ADDR COMIT ADDR				
SPIB_SOMI LID COM_SPI_SOMI	GPI0125 19 GPI0125_SPI_CS2_n	File: OMODRI_ANALOG.sch	LAAS/CNRS			
GPI062 J1/×	CDIO126 U9 X		Sheet: /OMODRI_GPIO/			
SPIB_SIEN M12	GPI0127 V9 X		Sheet: /UMUURI_GPIO/			
011000	0.70127		_			
			Title: Open MOtor DRiver Initiative (OMODRI)			
					(
				Date:		Rev: 1.0
			KiCad E.D.A. kicad	(5.1.6)-1		ld: 12/19
1	2 3		4	<u> </u>	5	













