# Intermediate Representation

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# Introduction

We give here a brief introduction on how the el compiler is implemented so far what can be done to improve it.

Right now there isn't a proper Intermediate Representation (IR) code generation (it is like PyPy before Just-in-Time compiler was introduced). el code is tokenized by a lexer (lexer.go), parsed by Bison (parser.y), and an Abstract Syntax Tree (AST) of the code is built; then, evaluating each node, the code is compiled. Each statement and operation is executed in go (e.g. an el sum becomes a sum in go and then the result is returned). In such way the Abstract Syntax Tree itself is used as an high level IR.

This approach has the advantage of simplicity (the AST itself is the IR), but using an explicit IR (which is the "standard way" to build a compiler) turns into a simpler and more readable representation of the code.

## IR Instructions

All the instructions are defined by this data structure:

```
        1
        type Intruction struct {

        2
        op int

        3
        arg1 Symbol

        4
        arg2 Symbol

        5
        result Symbol

        6
        true *Instruction

        7
        false *Instruction

        8
        next *Instruction

        9
        }
```

## **Binary Operations**

The binary operations defined in **EL** are:

- arithmetical operations (SUM, SUB, MUL, DIV);
- logic operations (AND, OR);
- comparison operations (EQUAL, NOT\_EQUAL, LOWER, GREATER, LOWER\_EQUAL, GREATER\_EQUAL);
- access operations (VALUE\_ACCESS, SQUARE\_ACCESS).

An example of arithmetical operation is:

 ${f EL}$  instruction:

$$x=y+w+z$$

Intermediate representation:

```
Instr_1 op: ADD arg1: $y arg2: $w result: $x_1$ true: NULL false: NULL next: Istr_2 Instr_2 op: ADD arg1: $x_1$ arg2: $z$ result: $x_2$ true: NULL false: NULL next: Istr_3 Instr_3 op: ASSIGN arg1: $x_2$ result: $x$ true: NULL false: NULL next: Istr_4 Instr_4 ...
```

An example of access operation is:

**EL** instruction:

```
x=a[i]
```

Intermediate representation:

```
Instr_1 op: SQUARE_ACCESS arg1: $a arg2: $i result: $x_1$ true: NULL false: NULL next: <math>Istr_2 Instr_2 op: ASSIGN arg1: $x_1$ result: $x$ true: NULL false: NULL next: <math>Istr_3 Instr_3 ...
```

## **Unary Operations**

The unary operations defined in **EL** are:

- arithmetic operation (UNARY\_MINUS);
- logic operation (NOT);
- operations on addresses (ASSIGNMENT).

#### **Unconditional Jumps**

An unconditional jump occurs when there is a *jump* that does not depend on the evaluation of any condition. Keywords such as *break* and *continue* are examples of unconditional jumps. Here's an example of *break* keyword:

**EL** instruction:

```
for \$i = 0; \$i < 10; \$i = \$i + 1 { break }
```

Intermediate representation:

```
Instr_1
        op: ASSIGN arg1: 0 result: i true: NULL false: NULL next: Istr_2
Label
        CONDITION
        op: LOWER arg1: $i$ arg2: 10 result: t_1 true: NULL false: NULL next: Istr_3
Instr_2
Instr_3
        op: JUMP arg1: t_1 true: BODY false: OUT
        BODY
Label
Instr_4
        op: U_{J}UMP next: OUT
        op: ADD arg1: \$i arg2: 1 result: \$i_1 true: NULL false: NULL next: Instr_6
Instr_5
        op: ASSIGN arg1: $i<sub>1</sub> result: $i true: NULL false: NULL next: Instr<sub>7</sub>
        op: U_JUMP next: CONDITION
Instr_7
Label
        OUT
Instr_8
        . . .
```

Here's an example of *continue* keyword:

**EL** instruction:

```
for \$i = 0; \$i < 10; \$i = \$i + 1 { continue }
```

### Intermediate representation:

```
op: ASSIGN arg1: 0 result: i true: NULL false: NULL next: Istr_2
Label
        CONDITION
Instr_2
        op: LOWER arg1: \$i arg2: 10 result: t_1 true: NULL false: NULL next: Istr_3
        op: JUMP arg1: t_1 true: BODY false: OUT
Instr_3
Label
        BODY
Instr_4
        op: U_JUMP \ \ {\rm next:} \ \ CONDITION
Instr_5
        op: ADD arg1: i arg2: 1 result: i true: NULL false: NULL next: Instr_6
        op: ASSIGN arg1: \$i_1 result: \$i true: NULL false: NULL next: Instr_7
        op: U_JUMP \ \ {\rm next} \colon \ CONDITION
Instr_7
Label
        OUT
Instr_8
        . . .
```