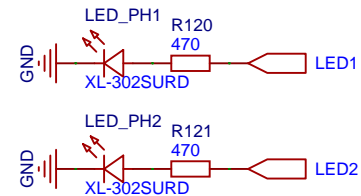
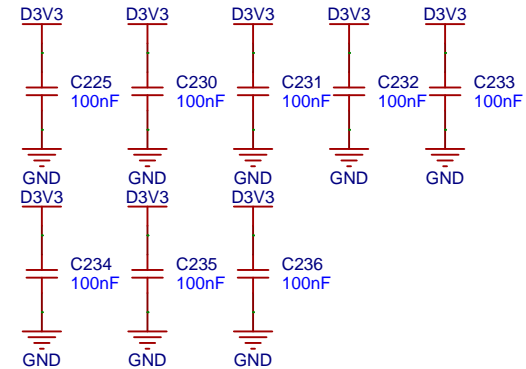
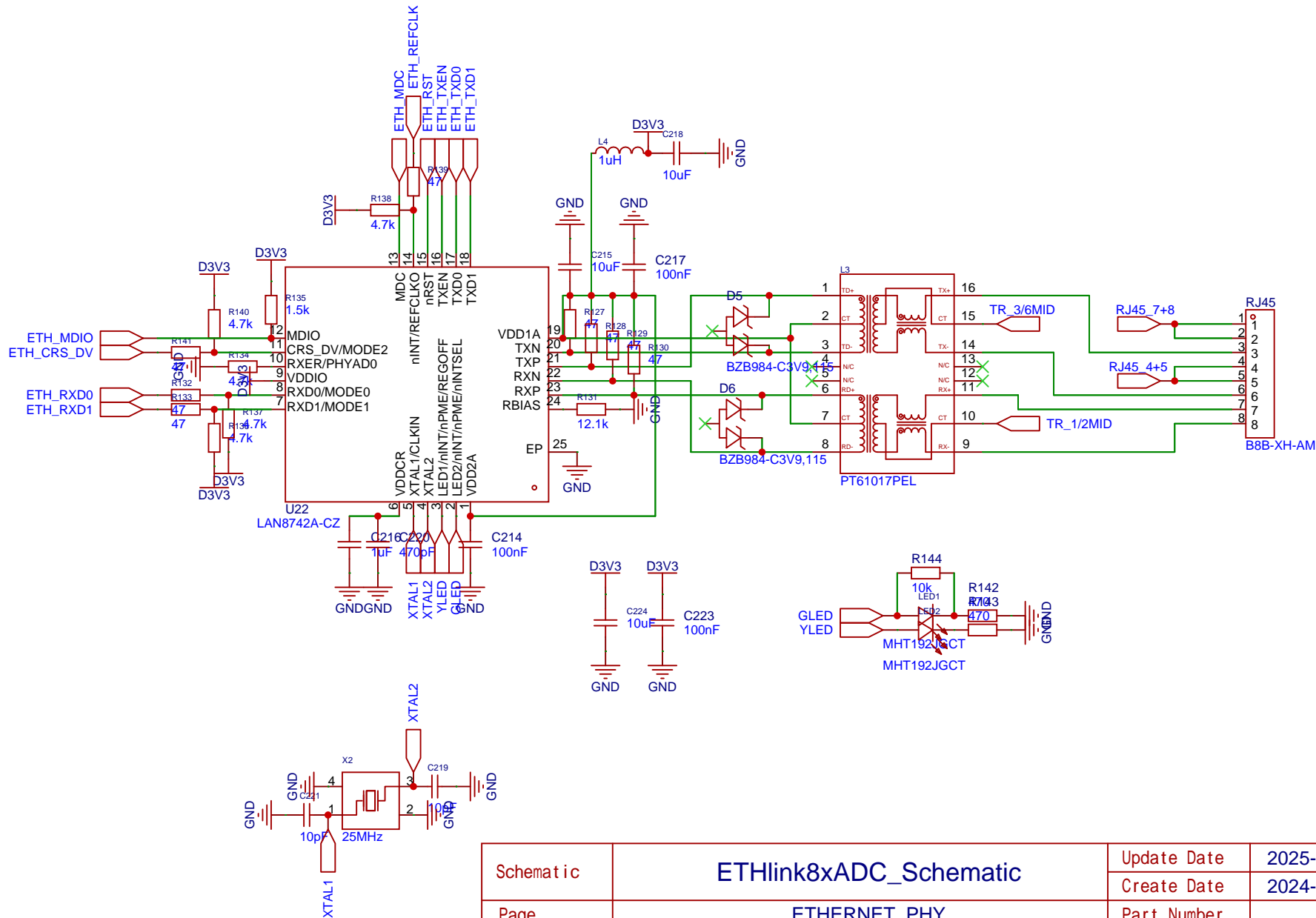


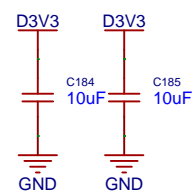
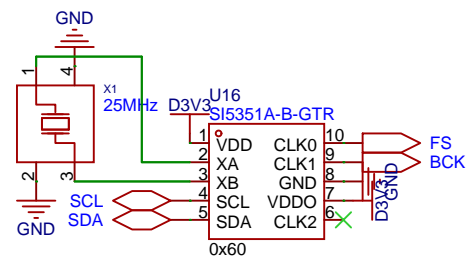
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				Create Date	2024-06-26
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Reviewed					
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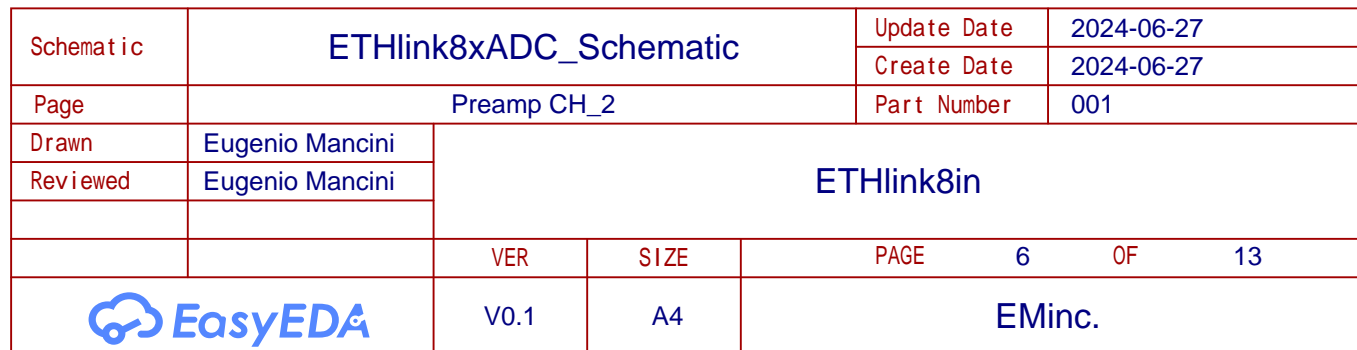


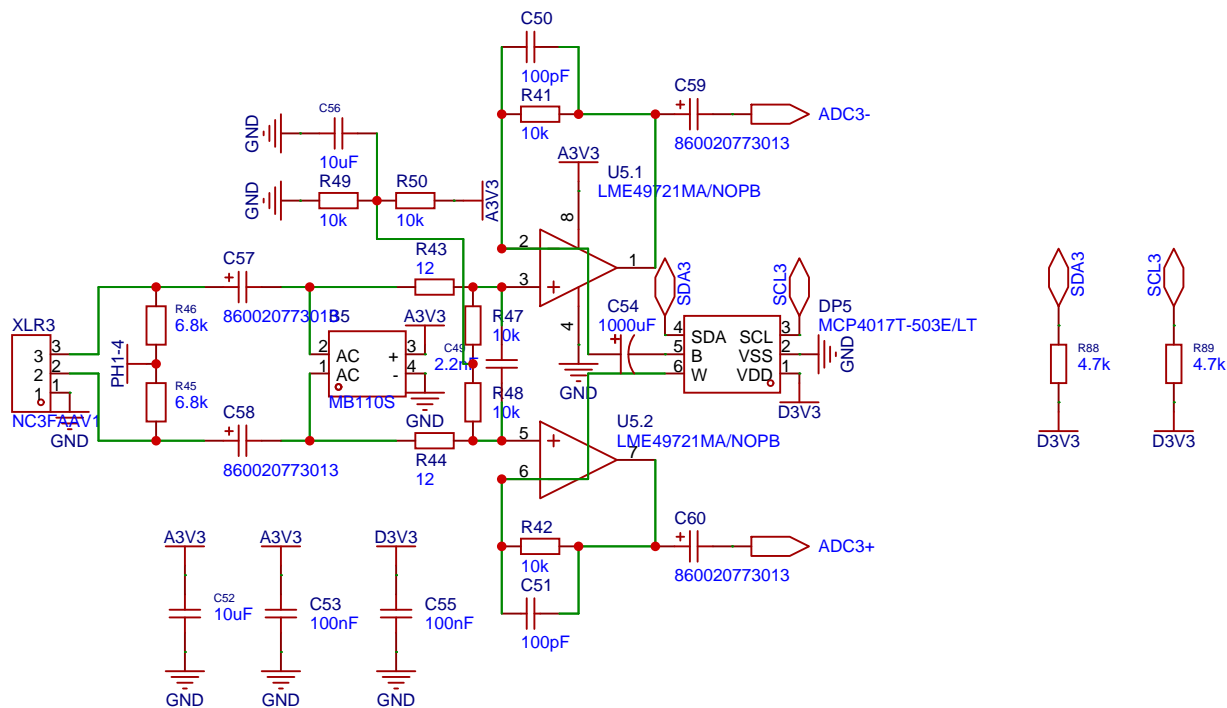
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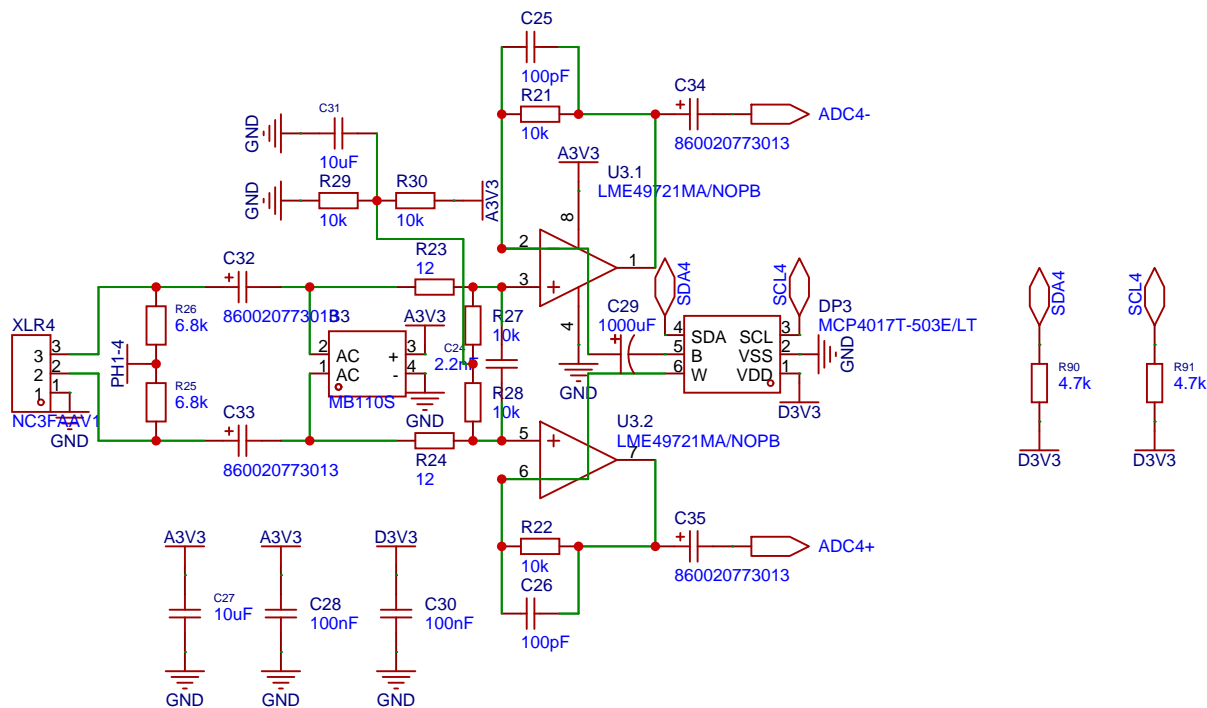
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Reviewed	EasyEDA				
		VER	SIZE	PAGE	4 OF 13
		V0.1	A4	EasyEDA.com	





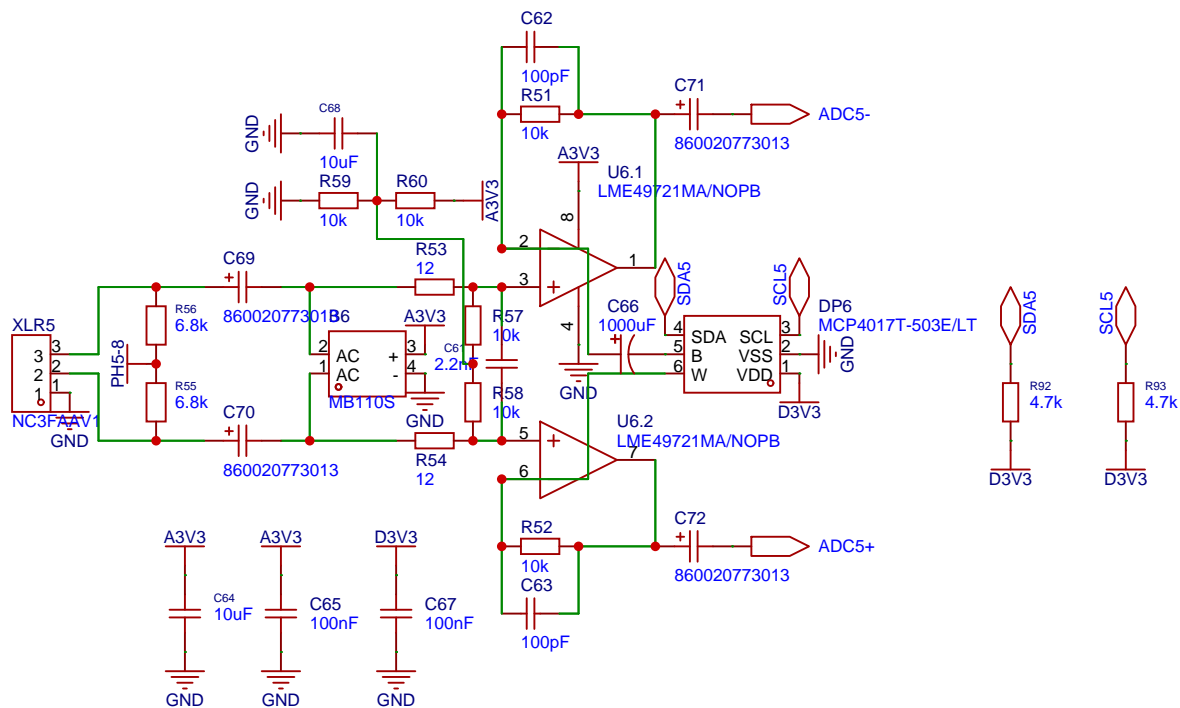


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Reviewed	Eugenio Mancini			
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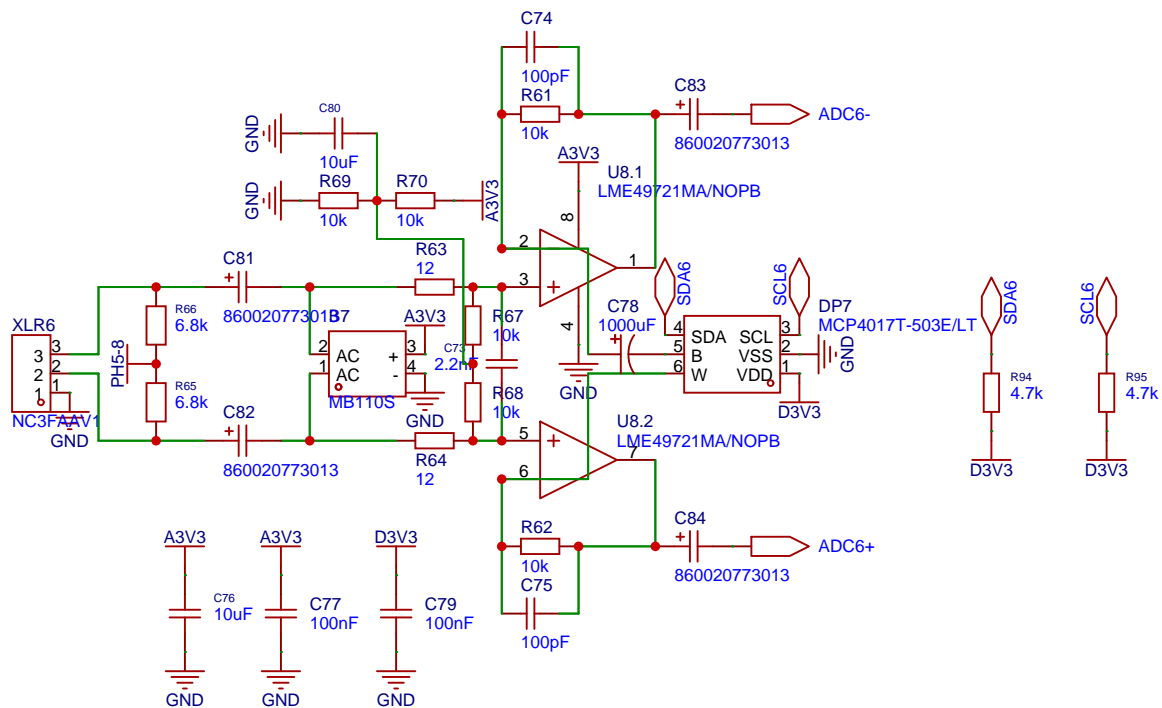


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Reviewed	Eugenio Mancini			
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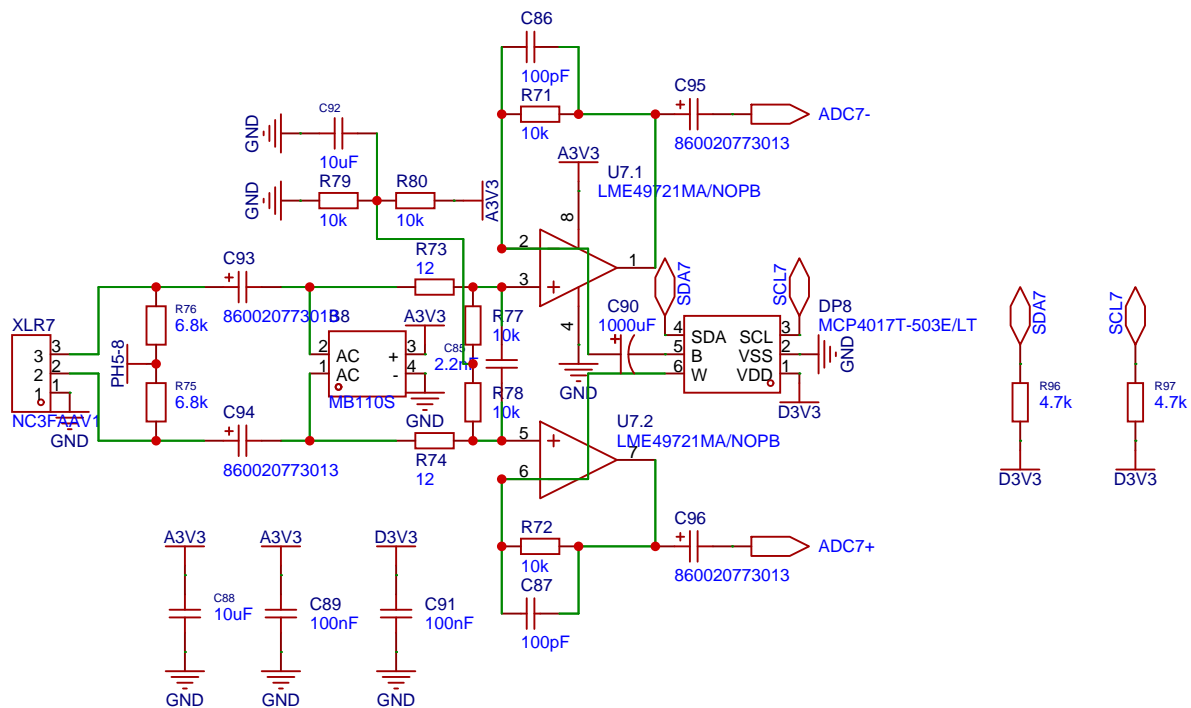




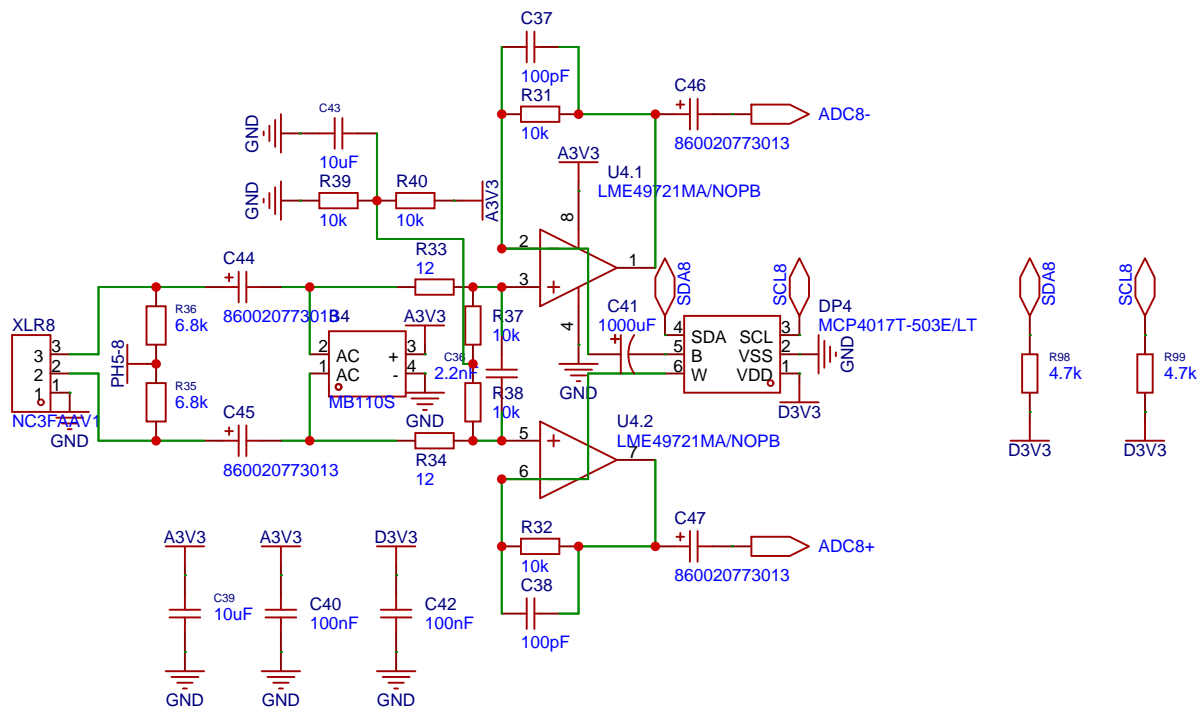
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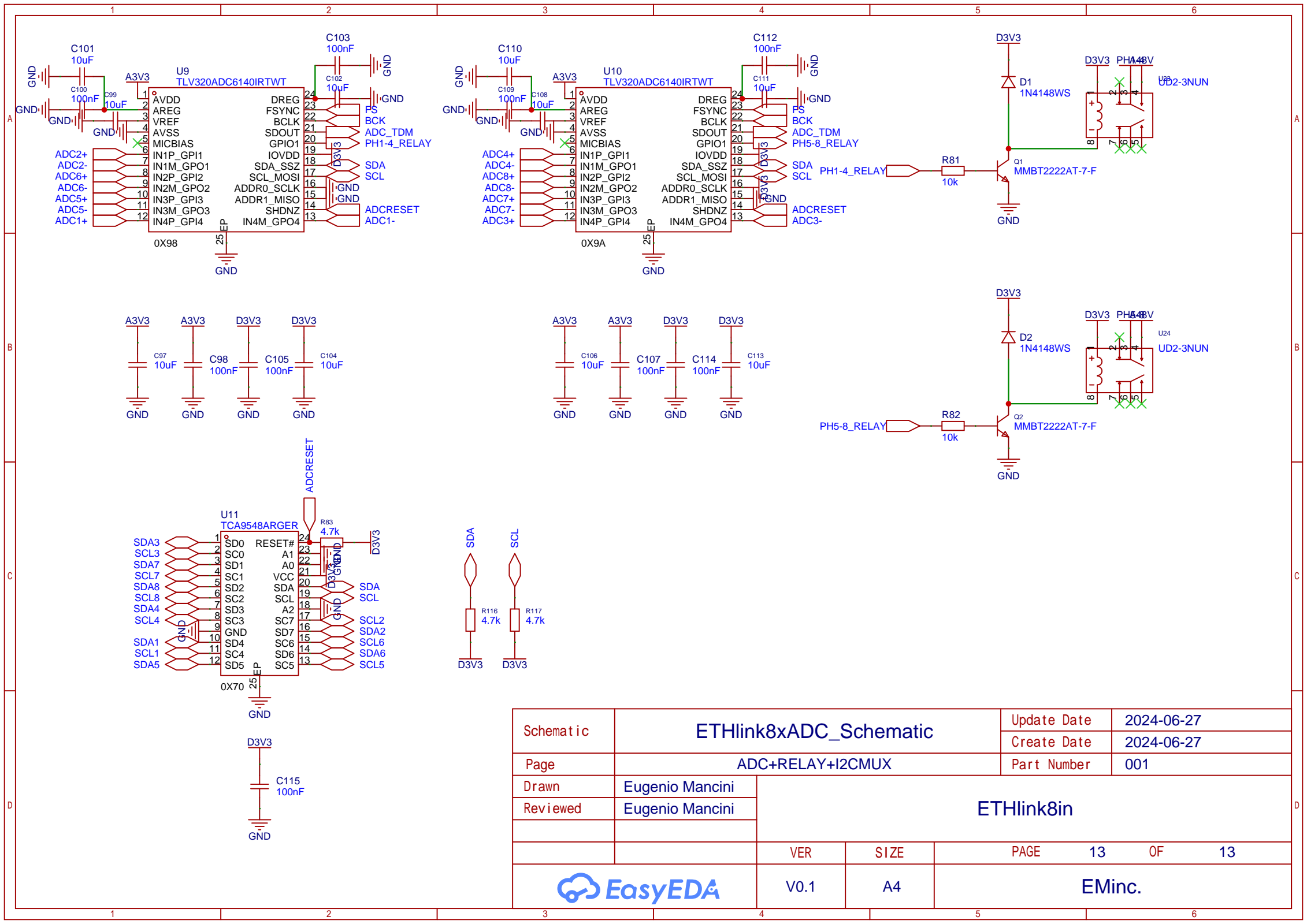
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Reviewed	Eugenio Mancini			
		VER	SIZE	PAGE 10 OF 13
EasyEDA		V0.1	A4	EMinc.



Schematic	ETHlink8xADC_Schematic		Update Date	2024-06-27
			Create Date	2024-06-27
Page	Preamp CH_7		Part Number	001
Drawn	Eugenio Mancini	ETHlink8in		
Reviewed	Eugenio Mancini			
		VER	SIZE	PAGE 11 OF 13
EasyEDA		V0.1	A4	EMinc.



Schematic	ETHlink8xADC_Schematic		Update Date	2024-06-27
			Create Date	2024-06-27
Page	Preamp CH_8		Part Number	001
Drawn	Eugenio Mancini	ETHlink8in		
Reviewed	Eugenio Mancini			
		VER	SIZE	PAGE 12 OF 13
EasyEDA		V0.1	A4	EMinc.



Schematic	ETHlink8xADC_Schematic		Update Date	2024-06-27
			Create Date	2024-06-27
Page	ADC+RELAY+I2CMUX		Part Number	001
Drawn	Eugenio Mancini	ETHlink8in		
Reviewed	Eugenio Mancini			
		VER	SIZE	PAGE 13 OF 13
EasyEDA		V0.1	A4	EMinc.