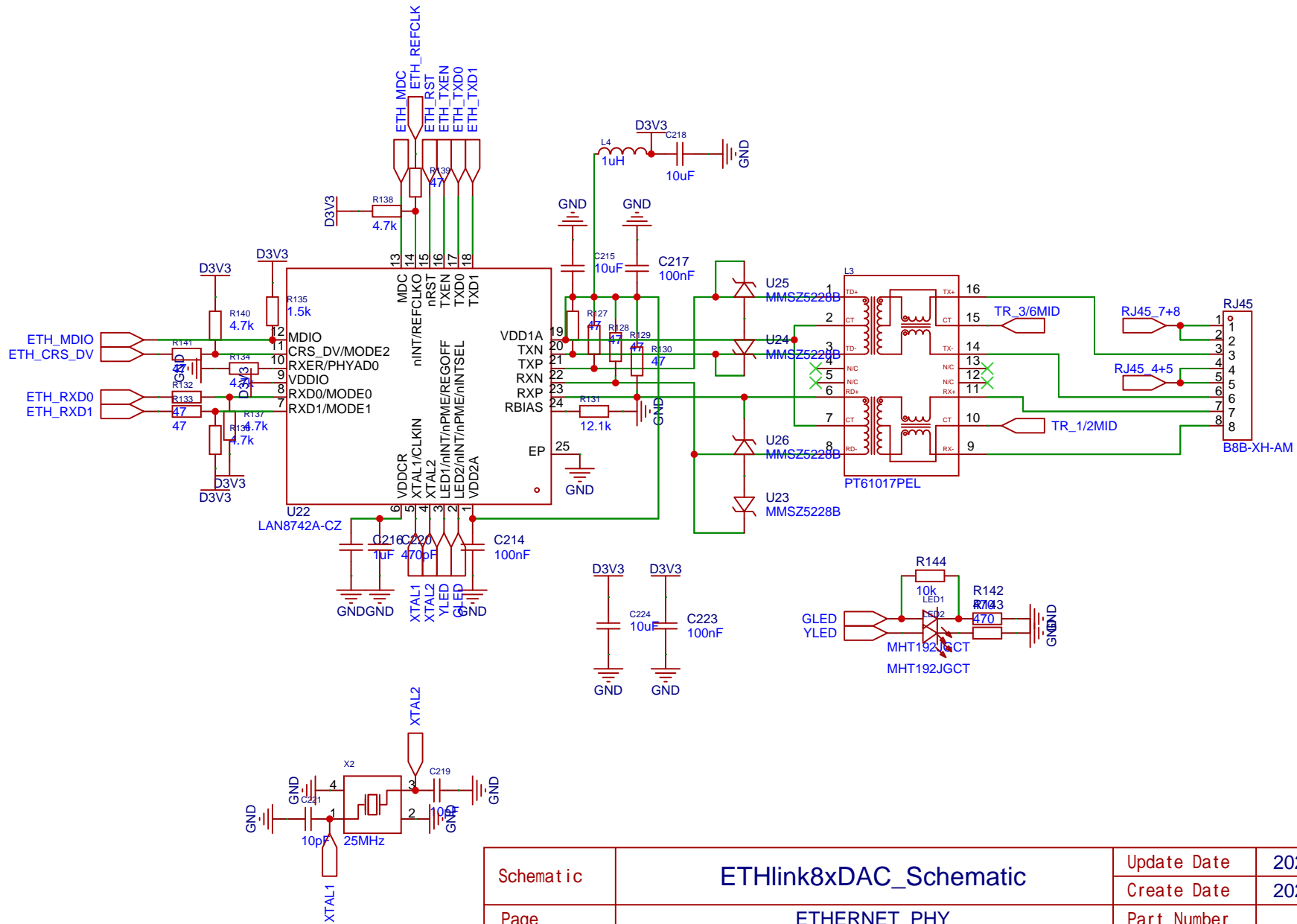

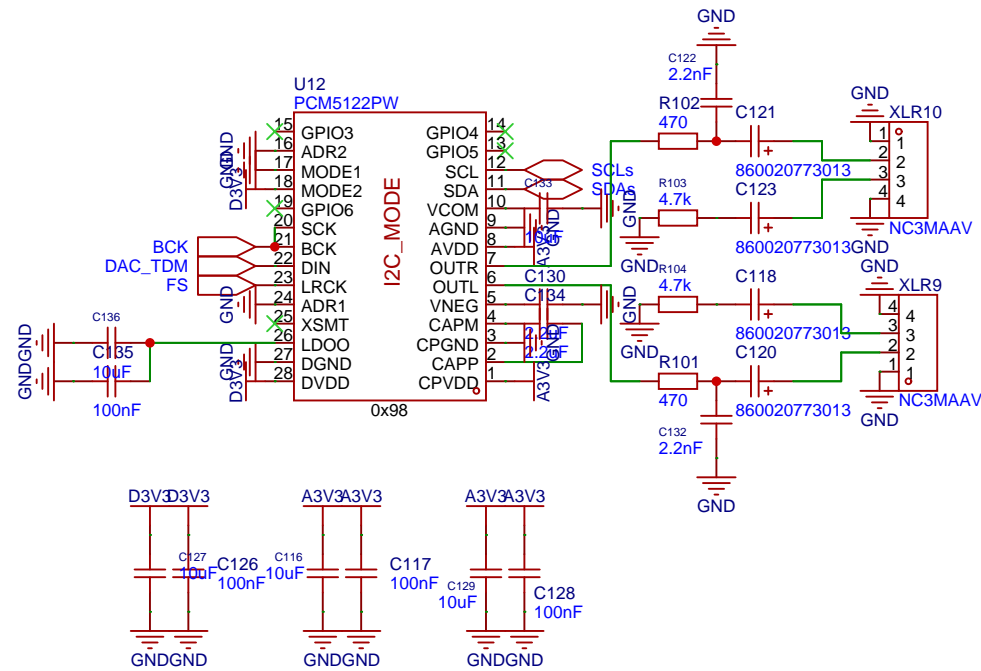



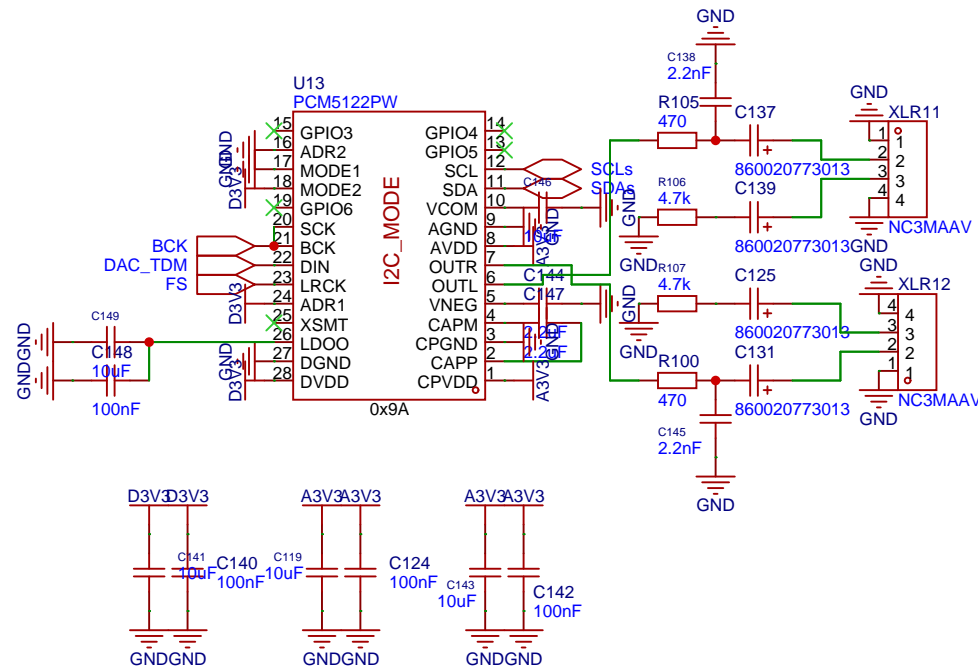
Schematic	ETHlink8xDAC_Schematic			Update Date	2025-03-29
				Create Date	2024-06-12
Page	PSU			Part Number	
Drawn	Eugenio Mancini				
Reviewed					
		VER	SIZE	PAGE	1 OF 8
EasyEDA		V1.0			




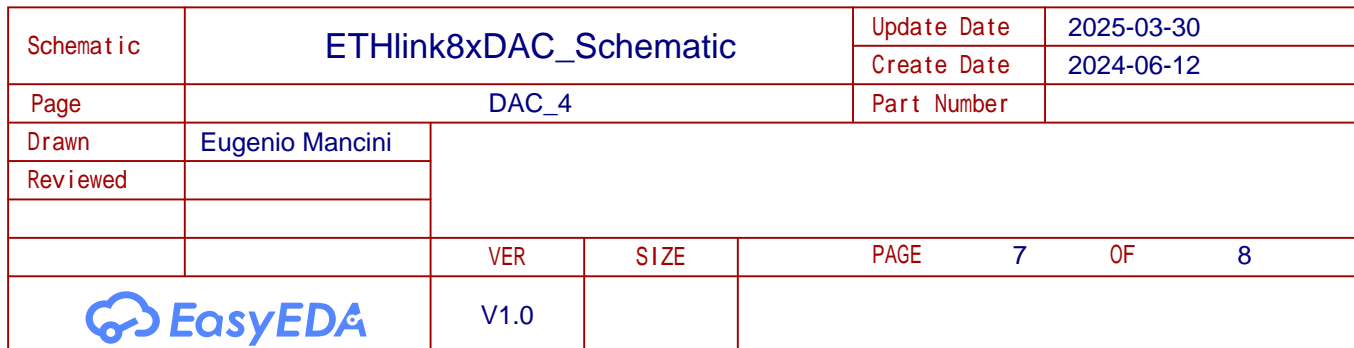
Schematic	ETHlink8xDAC_Schematic			Update Date	2025-03-30
				Create Date	2024-06-12
Page	ETHERNET_PHY			Part Number	
Drawn	Eugenio Mancini				
Reviewed					
		VER	SIZE	PAGE	3 OF 8
		V1.0	A4		

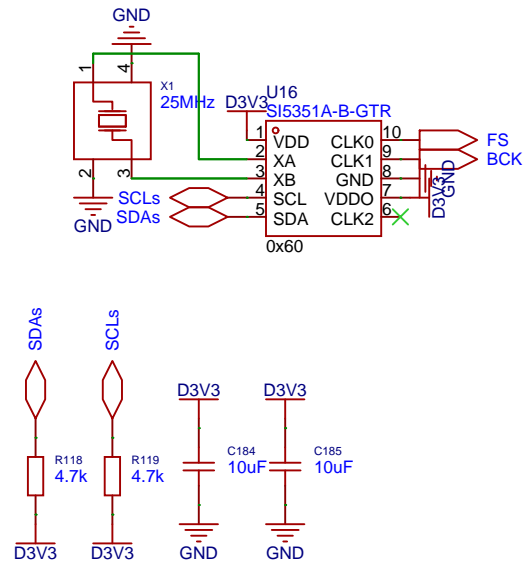


Schematic	ETHlink8xDAC_Schematic		Update Date	2025-03-30
			Create Date	2024-06-12
Page	DAC_1		Part Number	
Drawn	Eugenio Mancini			
Reviewed				
		VER	SIZE	PAGE 4 OF 8
		V1.0		



Schematic	ETHlink8xDAC_Schematic		Update Date	2025-03-30
			Create Date	2024-06-12
Page	DAC_2		Part Number	
Drawn	Eugenio Mancini			
Reviewed				
		VER	SIZE	PAGE 5 OF 8
		V1.0		





Schematic	ETHlink8xDAC_Schematic			Update Date	2025-03-30
				Create Date	2024-06-13
Page	CLOCK			Part Number	
Drawn	eugenio mancini				
Reviewed					
		VER	SIZE	PAGE	8 OF 8
EasyEDA		V0.1	A4	EasyEDA.com	