

[illegible]

N.C. (P)	1	28	VCC
A13 (P)	2	27	A12 (B)
A14 (G)	3	26	BA (P)
A15 (G)	4	25	AEC (H)
VA14 (A)	5	24	RW (H1)
CHAREN (1)	6	23	REFUG (H2)
HEAM (2)	7	22	GAME (J1)
LORAM (1)	8	21	VA13 (H4)
CAS (G)	9	20	VA12 (H5)
ROMR (P)	10	19	OE
ROML (P)	11	18	CASRAM (P0)
1P (P)	12	17	BASIC (P1)
OR.W (P)	13	16	KEFSEL (P2)
VSS	14	15	CHARM (P3)

Inputs		Commodore 64 PLA																							
I0	CAS	x	x	x	x	x	x	x	x	x	0	x	x	x	x	x	x	x	x	-	1	0	0	0	0
I1	LORAM	x	x	x	1	x	x	1	x	1	x	x	x	x	1	x	x	x	1	-	x	x	x	x	x
I2	HIRAM	x	x	1	x	1	1	x	0	1	x	0	x	x	1	0	1	1	1	-	x	x	x	x	x
I3	CHAREN	x	x	x	x	1	x	1	1	x	1	x	x	0	0	0	x	x	x	-	x	x	x	x	x
I4	VA14	x	x	x	x	x	x	x	x	x	x	1	1	x	x	x	x	x	x	-	x	x	x	x	x
I5	A15	x	1	1	1	1	1	1	1	1	1	x	x	1	1	1	1	1	1	-	x	0	x	0	1
I6	A14	x	1	0	0	0	1	1	1	1	1	1	x	x	1	1	1	1	0	-	x	1	0	0	1
I7	A13	x	1	1	0	0	0	0	0	0	0	0	x	x	0	0	1	1	1	-	x	x	1	0	0
I8	A12	x	x	x	x	1	1	1	1	1	1	1	x	x	1	1	1	x	x	-	x	x	x	1	0
I9	BA	x	x	x	x	x	x	x	1	1	1	x	x	x	x	x	x	x	x	-	x	x	x	x	x
I10	AEC	1	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	-	x	x	x	x	x
I11	R/W	x	x	1	x	1	0	0	0	1	1	0	x	x	1	1	1	1	1	-	x	x	x	x	x
I12	EXROM	1	1	0	1	0	x	1	x	1	x	0	1	x	0	0	0	x	0	-	x	1	1	1	1
I13	GAME	0	0	0	0	x	x	0	x	x	0	x	x	1	1	1	0	1	0	-	x	0	0	0	0
I14	VA13	1	x	x	x	x	x	x	x	x	x	x	0	x	x	x	x	x	x	-	x	x	x	x	x
I15	VA12	1	x	x	x	x	x	x	x	x	x	x	1	x	x	x	x	x	x	-	x	x	x	x	x
Outputs																									
F0	CASRAM	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	1	1	1	1	1
F1	BASIC	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	0	1	x	x	x	x	x
F2	KERNAL	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	0	0	1	x	x	x	x	x
F3	CHAROM	x	x	x	x	x	x	x	x	x	x	x	0	0	0	0	0	x	x	1	x	x	x	x	x
F4	GR/W	x	x	x	x	x	x	x	x	x	x	0	x	x	x	x	x	x	x	1	x	x	x	x	x
F5	I/O	x	x	x	x	0	0	0	0	0	0	0	x	x	x	x	x	x	x	1	x	x	x	x	x
F6	ROM L	x	x	x	0	0	x	x	x	x	x	x	x	x	x	x	x	x	x	1	1	x	x	x	x
F7	ROM H	0	0	0	x	x	x	x	x	x	x	x	x	x	x	x	x	x	x	1	x	x	x	x	x