\$40	( read_n / write )	ID \$08 ID \$D4	MSX2+ FS-A1 Series 1chipMSX, Zemmix Neo, etc.	Z80B 5.37MHz OCM-PLD v2.4 or later			SMART COMMANDS TABLE
		BIT 0 BIT 1			\$00 \$01	(000) (001)	Null Command (reserved) Set Turbo Pana Redirection OFF (default)
\$41	( read_n / write )	BIT 2 BIT 3		CD1	\$02 \$03	(002)	Set Turbo Pana Redirection ON Set Standard Speed 3.58MHz
		BIT 4 BIT 5	Smart Command ID	CPL or \$FF (Null)	\$04 \$05	(004)	Set Custom Speed 4.10MHz Set Custom Speed 4.48MHz
		BIT 6 BIT 7			\$06 \$07	(006)	Set Custom Speed 4.90MHz Set Custom Speed 5.39MHz
Ć42	( read / write_n )	BIT 0 BIT 1	CPU Clock Video Output (MSB)	Virtual DIP-SW1 Virtual DIP-SW2	\$08 \$09	(008)	Set Custom Speed 6.10MHz Set Custom Speed 6.96MHz
		BIT 2 BIT 3	Video Output (LSB) Cartridge Slot-1	Virtual DIP-SW3 Virtual DIP-SW4	\$0A \$0B	(010) (011)	Set Custom Speed 8.06MHz (aka "Turbo 10MHz") (default) Set Turbo MegaSD OFF
\$42		BIT 4 BIT 5	Cartridge Slot-2 (MSB) Cartridge Slot-2 (LSB)	Virtual DIP-SW5 Virtual DIP-SW6	\$0C \$0D	(012)	Set Turbo MegaSD ON (default) Set External Slot-1 + External Slot-2
		BIT 6 BIT 7	Current Mapper Size Current MegaSD Mode	Virtual DIP-SW7 Virtual DIP-SW8	\$0E \$0F	(014) (015)	Set Internal SCC-I Slot-1 + External Slot-2 Set External Slot-1 + Internal SCC-I Slot-2
\$43	( read / write_n )	BIT 0 BIT 1	•	CPU Clock Video Output	\$10 \$11	(016) (017)	Set Internal SCC-I Slot-1 + Internal SCC-I Slot-2 Set External Slot-1 + Internal ASCII-8K Slot-2
		BIT 2 BIT 3		Audio Mixer & SCRLK Cartridge Slot-1	\$12 \$13	(018)	Set Internal SCC-I Slot-1 + Internal ASCII-8K Slot-2 Set External Slot-1 + Internal ASCII-16K Slot-2
		BIT 4 BIT 5	Lock Mask of the Toggles	Cartridge Slot-2 Hard Reset Key	\$14 \$15	(020)	Set Internal SCC-I Slot-1 + Internal ASCII-16K Slot-2 Set Japanese Keyboard Layout
	( read / write_n )	BIT 6 BIT 7	6	Internal Mapper Internal MegaSD	\$16 \$17	(022)	Set Non-Japanese Keyboard Layout Set Display Mode 15KHz Composite/S-Video
		BIT 0 BIT 1		Led 1 Status Led 2 Status	\$18 \$19	(024)	Set Display Mode 15KHz RGB w/ Audio Out Set Display Mode 31Khz VGA for LED TV or LED Display also HDMI AV on SM-X
		BIT 2 BIT 3	Lights I/O	Led 3 Status Led 4 Status	\$1A \$1B	(026)	Set Display Mode 31Khz VGA+ for CRT Monitor (legacy output) also HDMI AV on SM-X Set VDP Speed Normal Mode (default)
\$44		BIT 4 BIT 5	(SM-X and SX-2 only use Led 8 but this register works the same way)	Led 5 Status Led 6 Status	\$1C \$1D	(028)	Set VDP Speed Fast Mode (V9958 only) Reserve MegaSD OFF (warm reset to set)
\$45	( read / write_n )	BIT 6 BIT 7	this register works the same way,	Led 7 Status Led 8 Status	\$1E \$1F	(030)	Reserve MegaSD ON (warm reset to set) Set MegaSD Blink OFF
		BIT 0 BIT 1	PSG Volume Level	BIT 0 (LSB) BIT 1	\$20 \$21	(032)	Set MegaSD Blink ON (default) Set Lights Mode OFF w/ Auto LEDs Control (default)
		BIT 2 BIT 3	PSG Mute	BIT 2 (MSB) Status	\$22 \$23	(034)	Set Lights Mode ON + Red Led OFF Set Lights Mode ON + Red Led ON
	( read / write_n )	BIT 4 BIT 5	Master Volume Level	BIT 0 (LSB) BIT 1	\$24 \$25	(036)	Internal Audio Preset #1 "Mute Sound" Internal Audio Preset #2 "Middle Sound"
	( read only )	BIT 6 BIT 7	Master Mute	BIT 2 (MSB) Status	\$26 \$27	(038)	Internal Audio Preset #3 "High Sound" (default) Set CMT OFF (default) (disabled w/ MSXtR BIOS)  n/a on SM-X / SX-2
\$46	( read / write_n )	BIT 0 BIT 1	OPLL Volume Level	BIT 0 (LSB) BIT 1	\$28 \$29	(040) (041)	Set CMT ON (needs a cassette recorder) (disabled w/ MSXtR BIOS)  n/a on SM-X / SX-2  Lock Turbo Toggles
	(read only)	BIT 2 BIT 3	OPLL Mute	BIT 2 (MSB) Status	\$29 \$2A \$2B	(041)	Unlock Turbo Toggles Lock Display Toggles
	( read / write_n )	BIT 4 BIT 5	SCC-I Volume Level	BIT 0 (LSB) BIT 1	\$2C \$2D	(044)	Unlock Display Toggles Lock Audio Mixer & SCRLK Toggles
	(read only)	BIT 6 BIT 7	SCC-I Mute	BIT 2 (MSB) Status	\$2E \$2F	(046)	Unlock Audio Mixer & SCRLK Toggles Lock Slot-1 Toggles
	, ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	BIT 0 BIT 1	CPU Custom Speed Level	BIT 0 (LSB) BIT 1	\$30 \$31	( 048 ) ( 049 )	Unlock Slot-1 Toggles Lock Slot-2 Toggles
\$47	( read only )	BIT 2 BIT 3	Turbo MegaSD (tMSD)	BIT 2 (MSB) Status	\$32 \$33	(050)	Unlock Slot-2 Toggles Lock Slot-1 & Slot-2 Toggles
		BIT 4 BIT 5	Turbo Megasb (twsb)  Turbo Pana Redirection (tPR)  VDP Speed Mode	Status 0=Normal, 1=Fast	\$34 \$35	(052)	Unlock Slot-1 & Slot-2 Toggles Lock Hard Reset Key
		BIT 6 BIT 7	Next Mapper Size Next MegaSD Mode	0=2048kB, 1=4096kB Status	\$36 \$37	(054)	Unlock Hard Reset Key Lock Mapper Toggle
\$48	( read only )	BIT 0	Turbo Pana	Status	\$38	( 056 )	Unlock Mapper Toggle Lock MegaSD Toggle
		BIT 1 BIT 2	Current Keyboard Layout SCRLK Toggle	0=JP, 1=Non-JP Status	\$39 \$3A \$3B	(057)	Unlock MegasD Toggle Lock All Toggles
		BIT 3 BIT 4 BIT 5	Lights Mode  Red Mode (Led 0)  Last Reset Flag	0=Auto, 1=ON Status	\$3C \$3D	(059) (060) (061)	Unlock All Toggles (default) Set Pseudo-Stereo OFF (default)
		BIT 6 BIT 7	Reset Required Flag MegaSD Blink	0=Cold, 1=Warm Status Status	\$3E \$3F	(062)	Set Pseudo-Stereo ON (needs an external sound cartridge) Sync External Bus Clock to CPU Speed (default)
\$49	( read only )	BIT 0	Pseudo Stereo	Status	\$40	(064)	Set External Bus Clock 3.58MHz
		BIT 1 BIT 2	External Clock Mode Machine Type ID	0=Sync to CPU, 1=3.58Mhz BIT 0 (LSB)	\$41 \$42	(065)	Set Turbo Pana 5.37MHz Set Right Inverse Audio OFF (default)
		BIT 3 BIT 4	(0=1chipMSX, 1=Zemmix Neo / SX-1, 2=SM-X, 3=SX-2, 4=SM-X Mini,	BIT 1 BIT 2	\$43 \$44	(067)	Set Right Inverse Audio ON Internal Audio Preset #4 "Emphasis PSG Sound"
		BIT 5 BIT 6 BIT 7	5=DEOCV, 6-14=Free, 15=Unknown) NTSC/PAL Type	BIT 3 (MSB) 0=Forced, 1=Auto 0=60Hz (NTSC), 1=50Hz (PAL)	\$45 \$46 \$47	(069) (070) (071)	Internal Audio Preset #5 "Emphasis SCC-I Sound" Internal Audio Preset #6 "Emphasis OPLL Sound" Vertical Offset 16 (useful for Ark-A-Noah)
	( read only )	BIT 0	Forced Video Mode Right Inverse Audio	Status	\$48	(072)	Vertical Offset 17
		BIT 1 BIT 2 BIT 3	Pixel Ratio 1:1 for LED Display	BIT 0 (LSB) BIT 1	\$49 \$4A \$4B	( 073 ) ( 074 )	Vertical Offset 18 Vertical Offset 19 (default) Vertical Offset 20
\$4A		BIT 4 BIT 5	Centering YJK Modes/R25 Mask Assignment of Legacy Output	Status	\$4B \$4C \$4D	( 075 ) ( 076 ) ( 077 )	Vertical Offset 21  Vertical Offset 21  Vertical Offset 22
		BIT 6 BIT 7	Internal Slot-1 Linear Internal Slot-2 Linear	0=To VGA, 1=To VGA+ Status Status	\$4E \$4F	(077)	Vertical Offset 22  Vertical Offset 23  Vertical Offset 24 (useful for Space Manbow)
		BIT 0	VGA Scanlines Level (ID*25%)	BIT 0 (LSB) only for SM-X / SX-2	\$50 \$53	( 080 083 )	Set VGA Scanlines 0% 25% 50% 75% (default is 0%) only for SM-X / SX-2
\$4B	( read only )	BIT 1 BIT 2		BIT 1 (MSB) only for SM-X / SX-2 BIT 0 (LSB)	\$7F	(127)	Pixel Ratio 1:1 for LED Display (default is 0) (range 0-7) (60Hz only)
		BIT 3 BIT 4	Free	BIT 1 BIT 2	\$80 \$81	(128)	Null Command (useful for programming) Assign Legacy Output to VGA
		BIT 5 BIT 6 BIT 7	(p,000000)	BIT 3 BIT 4	\$82 \$83 \$84	(130) (131) (132)	Assign Legacy Output to VGA+ (default) Set Internal Slot-1 Linear OFF (default) Set Internal Slot-1 Linear ON (needs SCC-I preset)
		BIT 0	CPU Clock	BIT 5 (MSB) Hard DIP-SW1	\$85	(133)	Set Internal Slot-2 Linear OFF (default)
		BIT 1 BIT 2	Video Output (MSB) Video Output (LSB)	Hard DIP-SW2 Hard DIP-SW3	\$86 \$87 \$88	(134) (135136)	Set Internal Slot-2 Linear ON (needs SCC-I or ASCII-8K/16K preset) Set Internal OPL3 OFF ON (default is off) only for SM-X / SX-2
\$4C	( read only )	BIT 3 BIT 4	Cartridge Slot-1 Cartridge Slot-2 (MSB)	Hard DIP-SW4 Hard DIP-SW5	\$B0 \$B7	(176 183)	Set Master Volume 0 7 (default level is 7)
		BIT 5 BIT 6	Cartridge Slot-2 (LSB) Internal Mapper	Hard DIP-SW6 Hard DIP-SW7 Hard DIP SW9	\$B8 \$BF \$C0 \$C7	(184 191) (192 199)	Set PSG Volume 0 7 (default level is 4) Set SCC-I Volume 0 7 (default level is 4) Set ODI I Volume 0 7 (default level is 4)
	( read / write_n )	BIT 7	Internal MegaSD	Hard DIP-SW8 BIT 0 (LSB)	\$C8 \$CF \$D0	(200207)	Set OPLL Volume 0 7 (default level is 4) Force NTSC Mode
		BIT 1 BIT 2	64kB VRAM Slot ID (Page 0)	BIT 1 BIT 2	\$D1 \$D2	(209)	Standard NTSC/PAL Mode (bound by Control Register 9) (default) Force PAL Mode
\$4D		BIT 3 BIT 4		BIT 3 (MSB) BIT 0 (LSB)	\$D3 \$D4	(211)	Restore Default Keyboard Layout Null Command (reserved)
		BIT 5 BIT 6	64kB VRAM Slot ID (Page 1)	BIT 1 BIT 2	\$D5 \$D6	(213) (214)	Restore Default Turbo Mode Set Centering YJK Modes/R25 Mask OFF (default)
		BIT 7		BIT 3 (MSB) BIT 0 (LSB)	\$D7 	(215)	Set Centering YJK Modes/R25 Mask ON
\$4E		BIT 1 BIT 2	OCM PLD	BIT 1 BIT 2	\$F9 \$FA	(249) (250)	Reserve Slot-O Primary Mode (warm reset to set) (internal OPLL disabled) Reserve System Logo ON (warm reset only)
	( read only )	BIT 3 BIT 4	OCM-PLD main version X.Y(.z) (range 0.0.z - 25.5.z)	BIT 3 BIT 4	\$FB \$FC	(251)	Cold Reset Warm Reset w/ Mapper 2048kB
		BIT 5 BIT 6		BIT 5 BIT 6	\$FD \$FE	(253) (254)	Warm Reset Warm Reset w/ Mapper 4096kB
\$4F	( read only )	BIT 7		BIT 7 (MSB) BIT 0 (LSB)	\$FF	( 255 )	Restore All Default + Reserve Default Mapper & MegaSD
		BIT 1 BIT 2 I/O Revision ID (0 - 31 BIT 3 BIT 4	I/O Revision ID (0 - 31)	BIT 1 BIT 2	More info	on Switched I/O	ports at MSX Assembly Page! < http://map.grauw.nl/resources/msx_io_ports.php#switch_io >
				BIT 3 BIT 4 (MSB)		Positive	0 = OFF (read / write )
		BIT 5 BIT 6	OCM-PLD sub version (x.y).Z (range x.y.0 - x.y.3)	BIT 0 (LSB) BIT 1 (MSB)	R/W Logic	Negative	1 = ON 0 = ON
		BIT 7	Default Keyboard Layout	Status		CPU Clo	1 = OFF ock [F12] or [DIP-SW1]
	Reserved to IPL-ROM						ttput [(SHIFT+)PRTSCR] or [DIP-SW2/3] SCRLK key could handle & SCRLK [(SHIFT+)PGDP/PGDOWN)F9/F10/F11] & [SCRLK] CMT or OPL3 depending
\$4E	( write_n only)	BIT 0 - 6		-	Toggles	Cartridge S	Slot-2 [SHIFT+SCRLK] or [DIP-SW5/6]
\$4F	( write_n only)	BIT 7 BIT 0 - 6	JIS2 Enabler	0=JIS1+JIS2, 1=JIS1 only - 0=Normal, 1=Invested		System R Internal M	apper [DIP-SW7] only
	- "	BIT 7	F4 Device Mode	0=Normal, 1=Inverted	/anogni elter	Internal M	egaSD [DIP-SW8] only
				KdL Index! < https://	enogni.aitervisi	ca.org >	