

REG_IF_ID	REG_ID_EX	REG_EX_MEM	REG_MEM_WB
Instruction[63:32]	MemToReg&RegWrite(1:0) (WB)	WB_ID_EX[1:0]	WB_EX_MEM[1:0]
PCp4[31:0]	MemWrite&Branch(3:2) (M)	M_ID_EX[3:2]	MemData[33:2]
	ALUOp&ALUSrc&RegDst[8:4] (EX)	BranchAddress[35:4]	ALURes1[65:34]
	PCp4_IF_ID[40:9]	Zero[36]	REG_EX_MEM[105:101] [70:66]
	RD1[72:41]	ALURes[68:37]	
	RD2[104:73]	RD2_ID_EX[100:69]	
	Instruction_IF_ID[10:6] [109:105]	MUXOutput_ID_EX[105:101]	
	EXT_Imm[141:110]		
	Instruction_IF_ID[5:0] [147:142]		
	Instruction_IF_ID[20:16] [152:148]		
	Instruction_IF_ID[15:11] [157:153]		