

Instruction	RegDst	ExtOP	ALUSrc	Branch	Jump	ALUOp	MemWrite	MemToReg	RegWrite						
add	1	0	1	0	0	0	0	0	1	TYPE R				000000_sssss_tttt_dddd_00000_100000	
sub	1	0	1	0	0	0	0	0	1					000000_sssss_tttt_dddd_00000_100010	
sll	1	0	1	0	0	0	0	0	1					000000_00000_tttt_dddd_hhhh_000000	
srl	1	0	1	0	0	0	0	0	1					000000_00000_tttt_dddd_hhhh_000010	
and	1	0	1	0	0	0	0	0	1					000000_sssss_tttt_dddd_00000_100100	
or	1	0	1	0	0	0	0	0	1					000000_sssss_tttt_dddd_00000_100101	
xor	1	0	1	0	0	0	0	0	1					000000_sssss_tttt_dddd_00000_100110	
noop	1	0	1	0	0	0	0	0	1					000000_00000_00000_00000_00000_000000	
addi	0	1	1	0	0	0,1	0	0	1	TYPE I				001000_sssss_tttt_iiiiiiiiiiii	
lw	0	1	1	0	0	0,1	0	1	1					100011_sssss_tttt_oooooooooooooooo	
sw	X	1	1	0	0	0,1	1	X	0					101011_sssss_tttt_oooooooooooooooo	
beq	X	1	0	1	0	1,0	0	X	0					000100_sssss_tttt_oooooooooooooooo	
bgtz	X	1	0	1	0	1,0	0	X	0					000111_sssss_00000_oooooooooooooooo	
bgez	X	1	0	1	0	1,0	0	X	0					000001_sssss_00000_oooooooooooooooo	
jmp	X	X	X	X	1	XX	0	X	0	TYPE J				000110_aaaaaaaaaaaaaaaaaaaaaaaaaa	