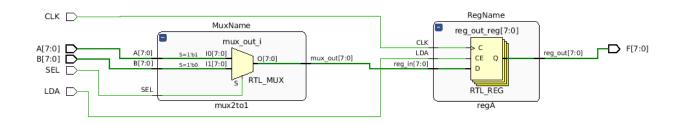
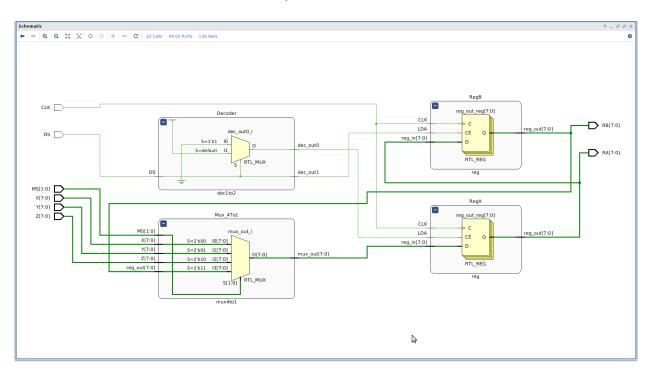
Jeffrey Novotny Embedded Systems HW 3 Schematics

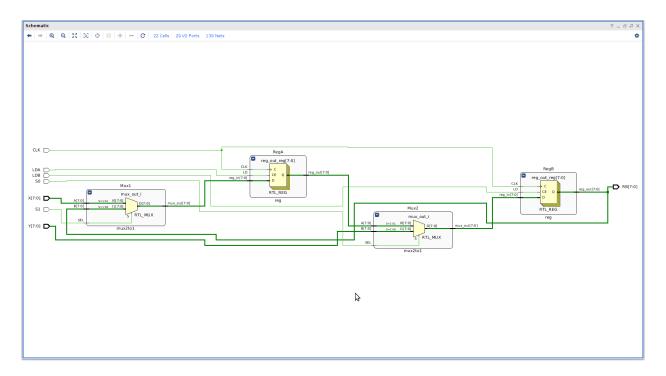
Chapter 7 Exercise 1



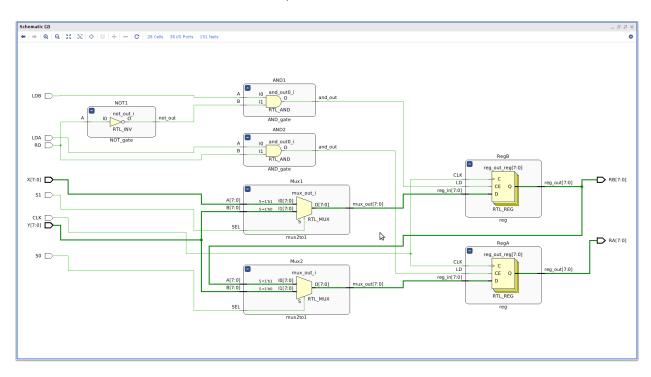
Chapter 7 Exercise 2



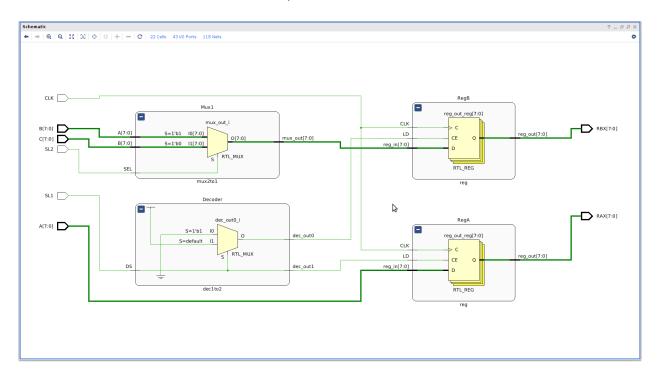
Chapter 7 Exercise 3



Chapter 7 Exercise 4



Chapter 7 Exercise 5



Chapter 7 Exercise 6

