

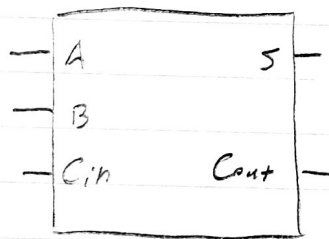
Embedded Systems Prelab 2

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1. Full adder eq.

$$Y = A \oplus B \oplus C_{in}$$
$$C_{out} = AB + C_{in}(A \oplus B)$$

2. Diagram for Full adder



3. 4 bit ripple carry adder

