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ne pages the MMU
emory manager
r it is moved
djust in a RAM.

K.

Due to 3 things, SP will modify

1) PUSH & POP
2) Function Call
3) Interrupt

Upon RESET

SP = 0X07

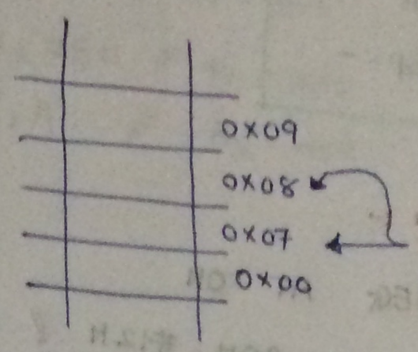
PC = 0X0000

P0 = 0XFF

P1 = 0XFF

P2 = 0XFF

P3 = 0XFF



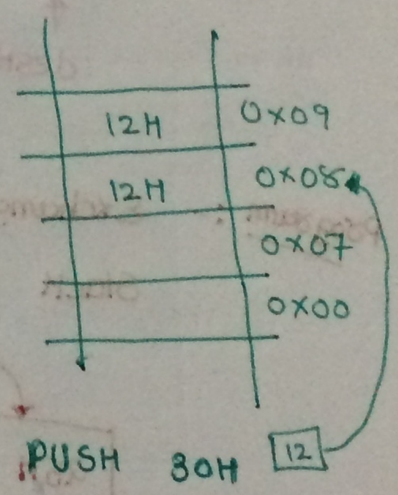
Syntax :-

1) PUSH address (8-bit)
2) POP address (8-bit)

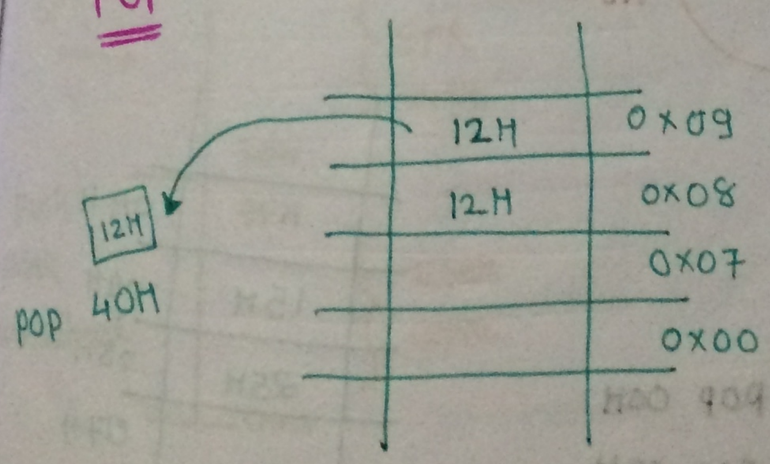
only internal RAM address.

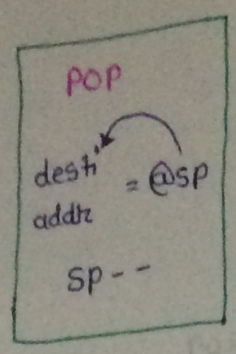
Program :-

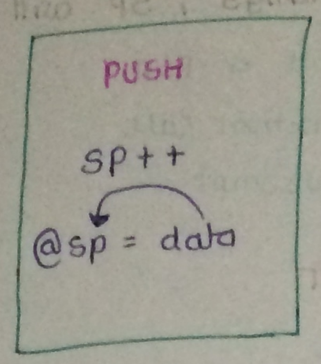
```
CSEG AT 0H
    MOV 30H, #12H   // 30H = 12
    PUSH 30H        // 0X08 = 12H
    PUSH 30H        // 0X09 = 12H
END
```



POP







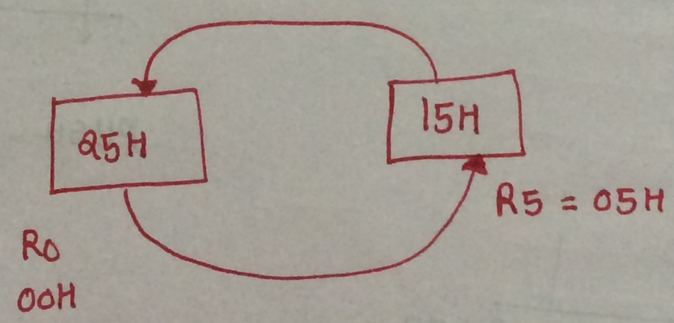
Program :-

```
CSEG   AT   0H
    mov  30H, #12H   // 30H = 12
    PUSH 30H         // 0X08 = 12H
    pop  40H         // 40H = 12H
END
```

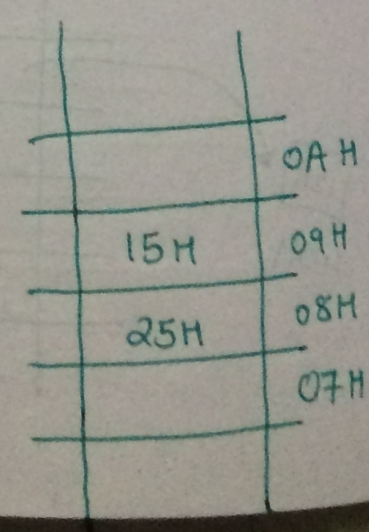
PUSH address
↑
source address

Pop address
↑
destination address

Program :- Exchange Contain of R0 and R5 using Stack operation.

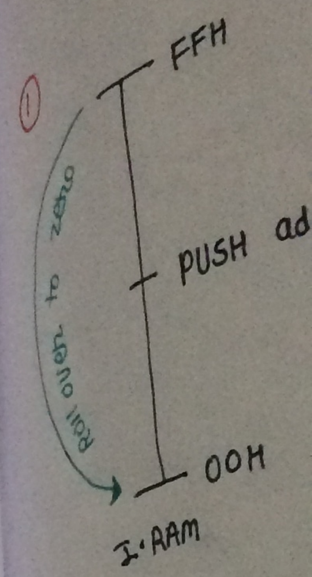


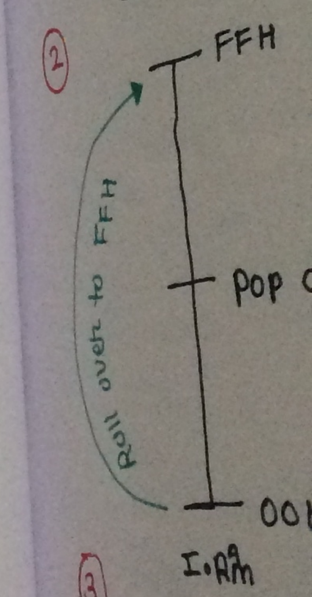

```
CSEG   AT   0H
    MOV  R0, #25H
    MOV  R5, #15H
    PUSH 00H          POP 00H
    PUSH 05H          POP 05H
                      END
```

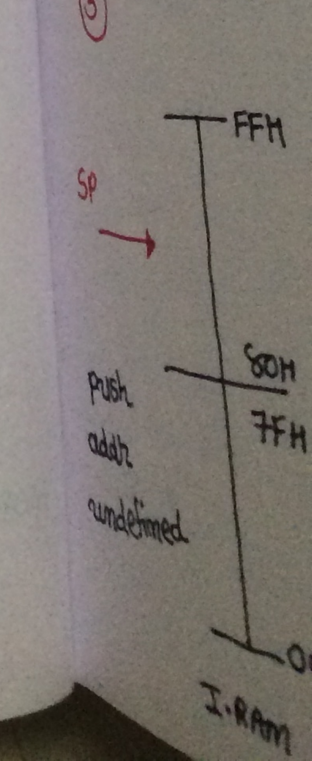


without PUSH & POP Instruction

```
CSEG  AT  0H
mov  R7, 05H
mov  R5, 00H
mov  R0, 07H
```







PUSH

SP++

sp = data

and R5 using

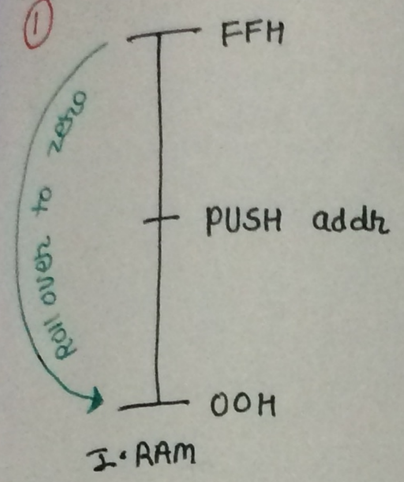
	0A H
15H	09H
25H	08H
	07H

without PUSH & POP Instruct'on

```
CSEG   AT   0H
mov    R7, 05H
mov    R5, 00H
mov    R0, 07H
```

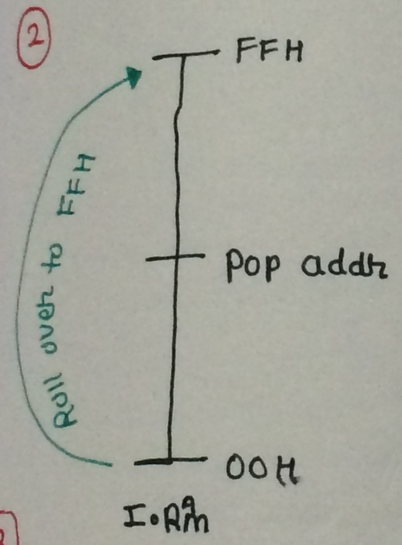
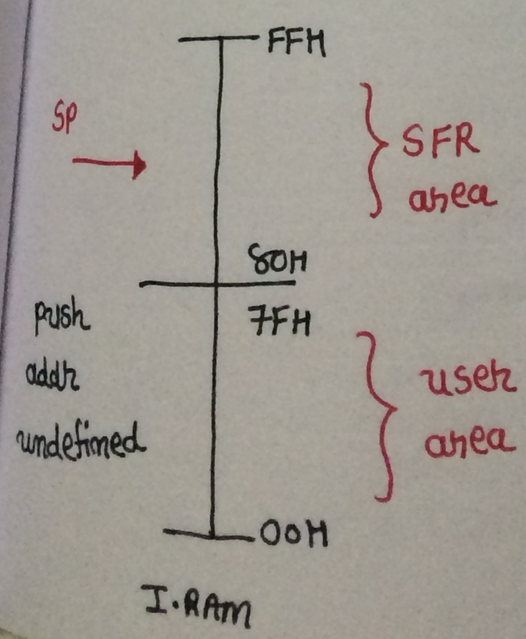
```
CSEG   AT   0H
mov    R0, #25H   // R0 = 25H
mov    R5, #15H   // R5 = 15H
mov    A, R0      // A = 25H
mov    00H, 05H   // R0 = 15H
mov    R5, A      // R5 = 25H
END
```

1



⇒ when sp is pointing

2



3