

Haresh Panchal

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Career Objective

A professional with 3.4+ years of technical experience in Embedded Firmware Development using various Technologies. I want to work for the company which offers me a challenging work, which is beneficial to both company and its clients. To employ my technical skills and work with my sincere efforts and dedication.

Technical Skill Sets

- **Software Programming Languages :** Embedded C, C, Data Structure, C++,(STL), Assembly Language, VB6, C#, Python.
- **Linux System Programming** : Linux System Programming (Basic File I/O, Threads, Signal Handling, Memory Management, IPC Mechanisms).
- **Operating System** :Windows, Linux, RTOS(FreeRTOS).
- **Simulation Tools** :CCSv8, MPLAB, e2Studio, CS+,LPCxPresso, Keil,IAR, Pycharm, Visual -Studio, Eclipse, VI editor, GCC Compiler, GDB Tool, Turbo C.
- **Protocol & Periperal** :I2C, SPI, UART, Modbus RTU, Modbus,TCP ,CAN, OpenCAN, PWM, ECAP, ADC, DAC, RTC, EEPROM.

Work Experience**1. AMTECH Electronics India Pvt Ltd. (Aug 2018 to Present)****Projects:**

- **Customize Dynamic Link Library for Modbus TCP and Modbus RTU.**
Programming Language : C++.
Appliation: DLL for Modbus RTU and Modbus TCP having customize response for Desktop Application.
- **Developed Graphical User Interface for all products with all versions.**
Software IDE:Visual Studio 2019.
Programming Language : C++.
Application: Developed desktop application as Modbus TCP and RTU master for interfacing all products with displaying parameters,Graphs,fault screen as well as import and export configuration file.
- **Development of Automatic Test bench system for testing and calibrating of Cards.**
Hardware: TMS320F28377D (TI DSP) control card.
Programming Language : C++.
IDE: Visual Studio 2019,CCS V8.

Application: Developed Desktop application for communicating with Control card and Test Card on Modbus RTU.
Application can tests and calibration of different module of Control , Power and Interface card.

Firmware: Developed firmware and communication protocol for below test and calibration process.

1. PWM Test
2. Power Supply Test
3. DC Voltage Calibration
4. Analog input and output in Voltage and Current form.
5. Current ADC Test.
6. Digital input and output
7. Gate Pulse tests
8. Short Circuit Test.

Developed different 17 Automatic Test Bench for testing and calibrating of cards of VFD.

- **Exploring TI DSP 320F28388D.Dual Core having connectivity manager.**

Hardware: 320F28388D TI DSP

Application: Exploring all communication module like modbus master , modbus server , modbus TCP slave, SPI ,I2C,customize open CAN frame analog and digital inputs and outputs using FSI.

- **Design and developed Seven segment LED and Graphical Display for VFD.**

Hardware: Reneesas controller, keypad matrix with Seven Segment Led Display

Application: Seven Segment Led and Graphical Display(MODBUS master) for control VFD with more than 2500 parameters.

- **Design and development of Analog and Digital Fiber Optic Link using Fast Serial Communication.**

Hardware: TI Controller . Analog and Digital inputs and outputs in current and voltage form.

Application: Making communication system for 4 analog inputs and 4 outputs (500 kHz bandwidth) and 8 Digital inputs and outputs using Fast Serial Communication Protocol. Implement modbus protocol for configure gain and bias for analog inputs and outputs Use CLA for achieving linearity less than 0.01%.

Training

- **Training on Embedded Systems as a Trainee Engineer at VECTOR-INDIA, Bangalore**
Embedded Training Project

Data Acquisition System on 8051 using I2C and SPI Protocols

Peripherals : DS 1307 (RTC), DS 1621 (Digital Thermometer), UART

Application : Collecting Data from Various Sensors and Displaying on LCD and Windows Hyper Terminal or Linux Platform

Education Qualification

- B.E. (Electical) from GTU in 2013
- H.S.C from GSHSEB in 2009
- S.S.C from GSHSEB in 2006

Strengths

Self Confidence | Learning Agility | Responsible Person | Punctuality | Team Work Skills

Personal Information

Name : Haresh Panchal
Father Name : Pravinbhai Panchal
DOB : 15th September 1990
Marital Status : Married
Languages Known : English, Hindi and Gujarati

Declaration

I hereby declare that the information's furnished above are complete and true to the best of my knowledge.

Place:

(Signature)

Date:

Haresh Panchal