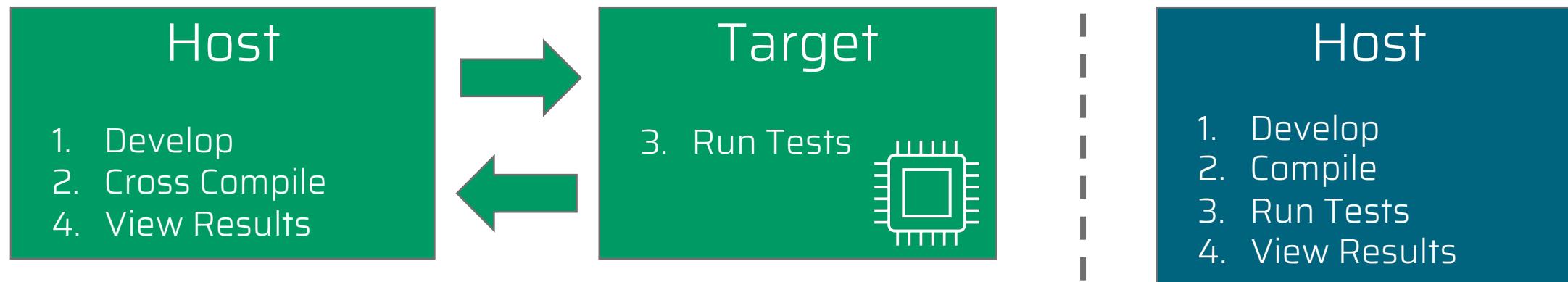




# The hidden risks in off-target testing

Requirements and workflows for safe and modern embedded development

Daniel Penning, embeff GmbH



## “On-Target” Tests

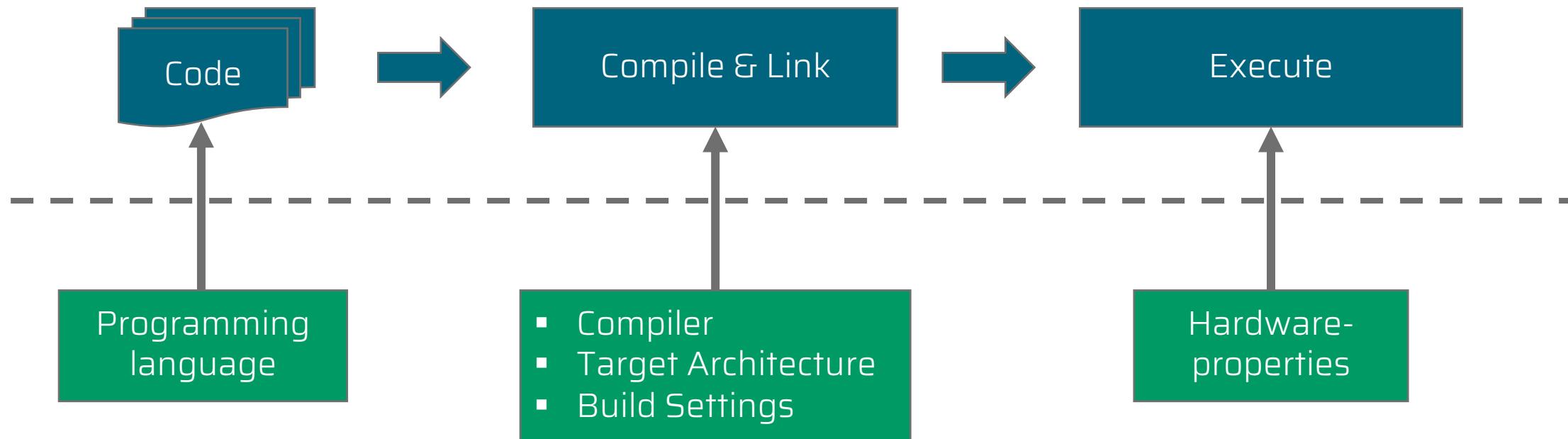
- slow
- difficult to automate

## “Off-Target” Tests

- fast
- easy to automate

MIND THE GAP

# What is causing the gap?



# (1) Different Evaluation Order

```
#include <iostream>

int fun1() { printf("fun1() \n"); return 0; }
int fun2() { printf("fun2() \n"); return 0; }

void foo(int x, int y) { printf("foo() \n"); }

int main() {
    foo(fun1(), fun2());
}
```

x86-64 with gcc 9.2

```
fun2()
fun1()
foo()
```

Cortex-M4 with arm-gcc-none-eabi 8

```
fun1()
fun2()
foo()
```

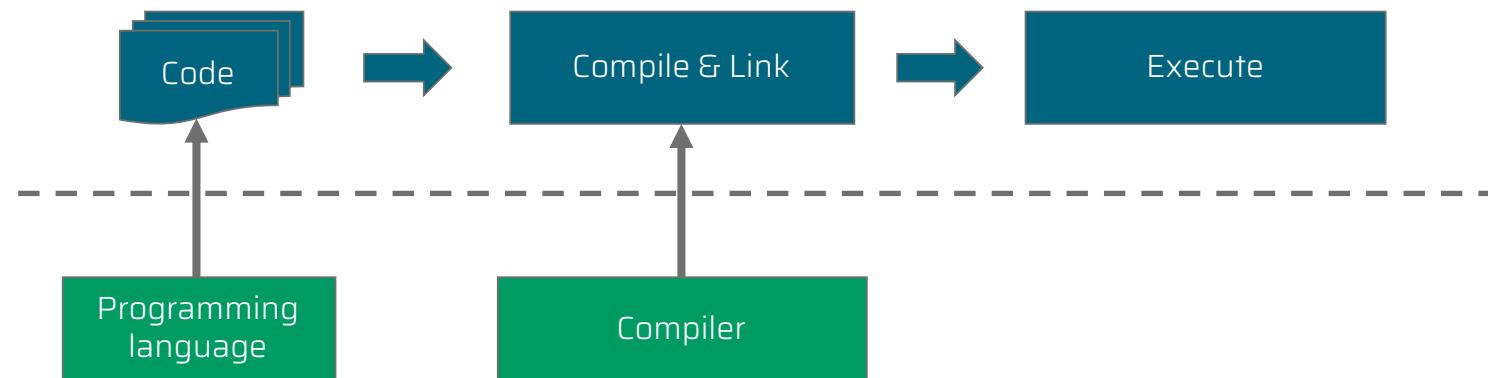
# (1) Different Evaluation Order - Why?

```
int main() {  
    foo(fun1(), fun2());  
}
```

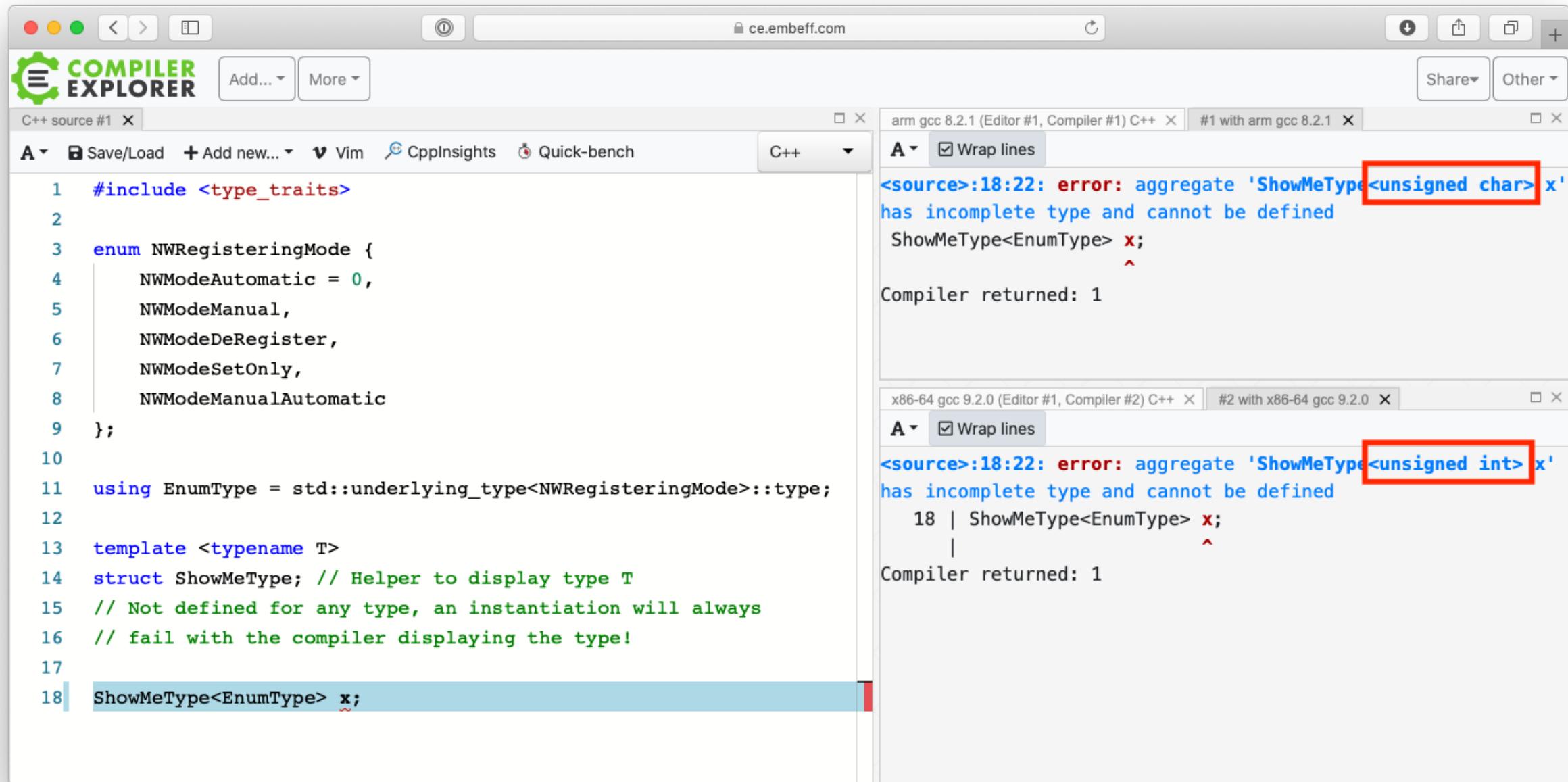
8. The order of evaluation of arguments is unspecified.  
All side effects of argument expression evaluations take effect before the function is entered.

C++98 Standard, 5.2.2 Function call [1]

**unspecified behavior**  
behavior, for a well-formed program construct and correct data, that depends on the implementation.



# (2) Different Enum Size



C++ source #1

```
1 #include <type_traits>
2
3 enum NWRegisteringMode {
4     NWModeAutomatic = 0,
5     NWModeManual,
6     NWModeDeRegister,
7     NWModeSetOnly,
8     NWModeManualAutomatic
9 };
10
11 using EnumType = std::underlying_type<NWRegisteringMode>::type;
12
13 template <typename T>
14 struct ShowMeType; // Helper to display type T
15 // Not defined for any type, an instantiation will always
16 // fail with the compiler displaying the type!
17
18 ShowMeType<EnumType> x;
```

arm gcc 8.2.1 (Editor #1, Compiler #1) C++ #1 with arm gcc 8.2.1

```
A  Wrap lines
<source>:18:22: error: aggregate 'ShowMeType<unsigned char> x' has incomplete type and cannot be defined
ShowMeType<EnumType> x;
^
Compiler returned: 1
```

x86-64 gcc 9.2.0 (Editor #1, Compiler #2) C++ #2 with x86-64 gcc 9.2.0

```
A  Wrap lines
<source>:18:22: error: aggregate 'ShowMeType<unsigned int> x' has incomplete type and cannot be defined
18 | ShowMeType<EnumType> x;
| ^
Compiler returned: 1
```

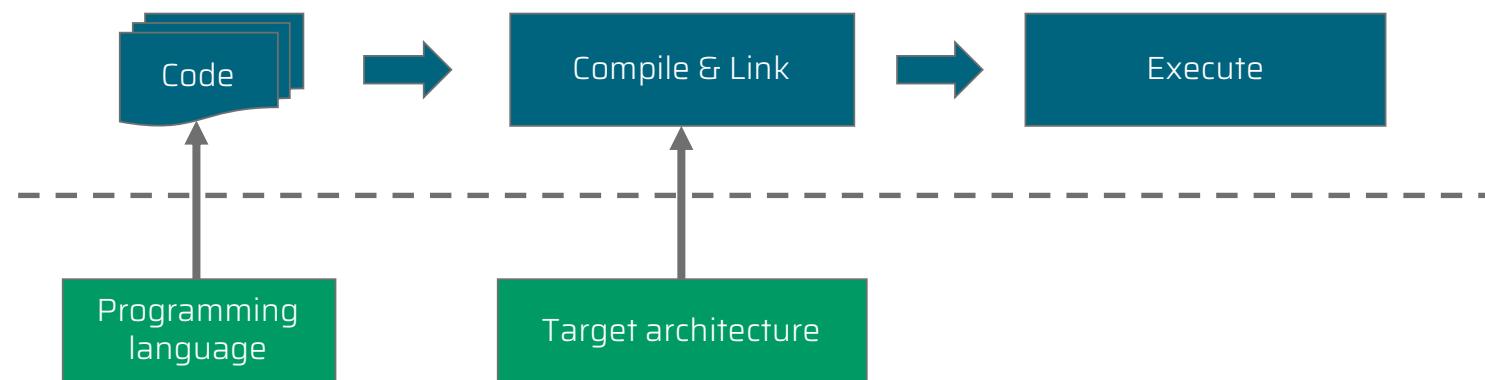
## (2) Different Enum Size - Why?

```
enum NWRegisteringMode {  
    NWModeAutomatic = 0,  
    NWModeManual,  
    NWModeDeRegister,  
    NWModeSetOnly,  
    NWModeManualAutomatic  
};
```

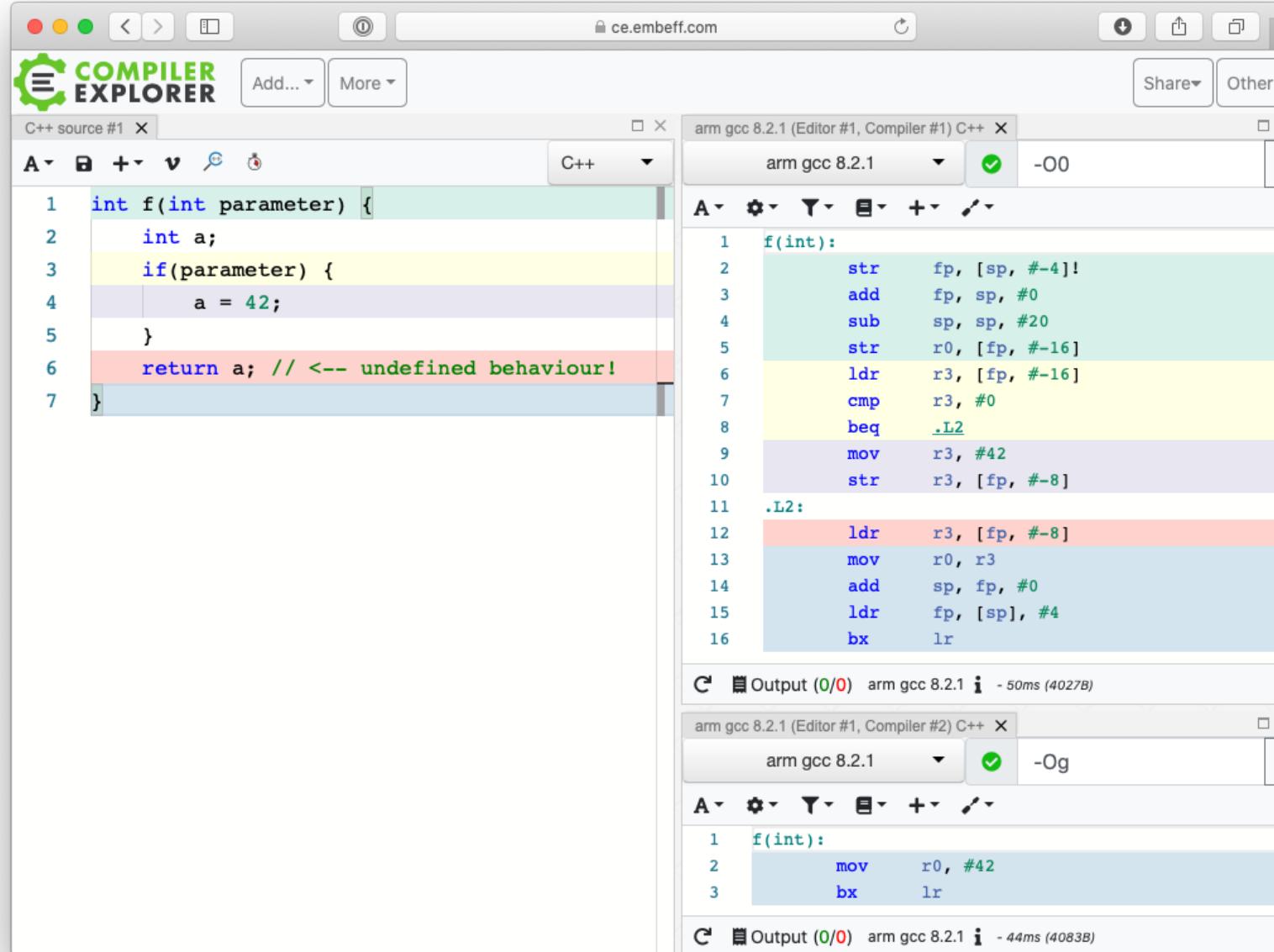
It is implementation-defined which integral type is used as the underlying type except that the underlying type shall not be larger than int unless the value of an enumerator cannot fit in an int or unsigned int.

C++17 Standard, 10.2 Enumeration declaration [2]

**implementation-defined behaviour**  
behaviour, for a well-formed program construct and correct data, that depends on the implementation and which is documented at each implementation.



# (3) Optimizer affects results



The screenshot shows the Compiler Explorer interface with two compiler runs side-by-side.

**Left Compiler (arm gcc 8.2.1, -O0):**

```
int f(int parameter) {
    int a;
    if(parameter) {
        a = 42;
    }
    return a; // <-- undefined behaviour!
}
```

**Right Compiler (arm gcc 8.2.1, -Og):**

```
f(int):
    str fp, [sp, #-4]!
    add fp, sp, #0
    sub sp, sp, #20
    str r0, [fp, #-16]
    ldr r3, [fp, #-16]
    cmp r3, #0
    beq .L2
    mov r3, #42
    str r3, [fp, #-8]
.L2:
    ldr r3, [fp, #-8]
    mov r0, r3
    add sp, fp, #0
    ldr fp, [sp], #4
    bx lr
```

**Output:**

```
C Output (0/0) arm gcc 8.2.1 - 50ms (4027B)
```

```
f(int):
    mov r0, #42
    bx lr
```

```
C Output (0/0) arm gcc 8.2.1 - 44ms (4083B)
```

## Unoptimized build

- parameter == 0:  
Return uninitialized stack memory
- parameter != 0:  
Return 42

## Optimized build

- Return 42

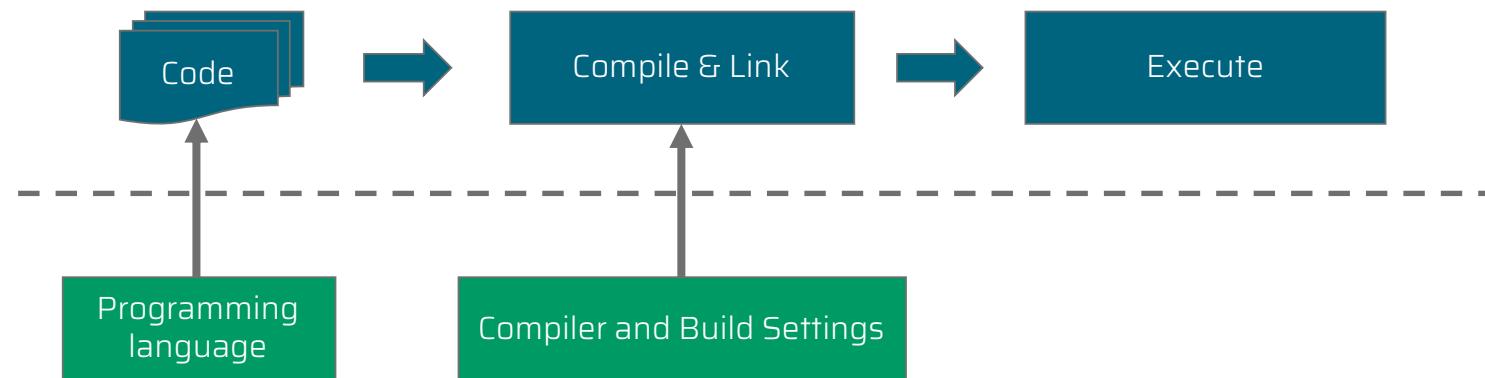
### (3) Optimizer affects results – Why?

```
int f(int parameter) {  
    int a;  
    if(parameter) { a = 42; }  
    return a; // <-- undefined behaviour!  
}
```

Undefined behaviour renders the entire program meaningless if certain rules of the language are violated.

[cppreference.com](http://cppreference.com)

**Undefined behaviour**  
behaviour for which this International Standard imposes no requirements



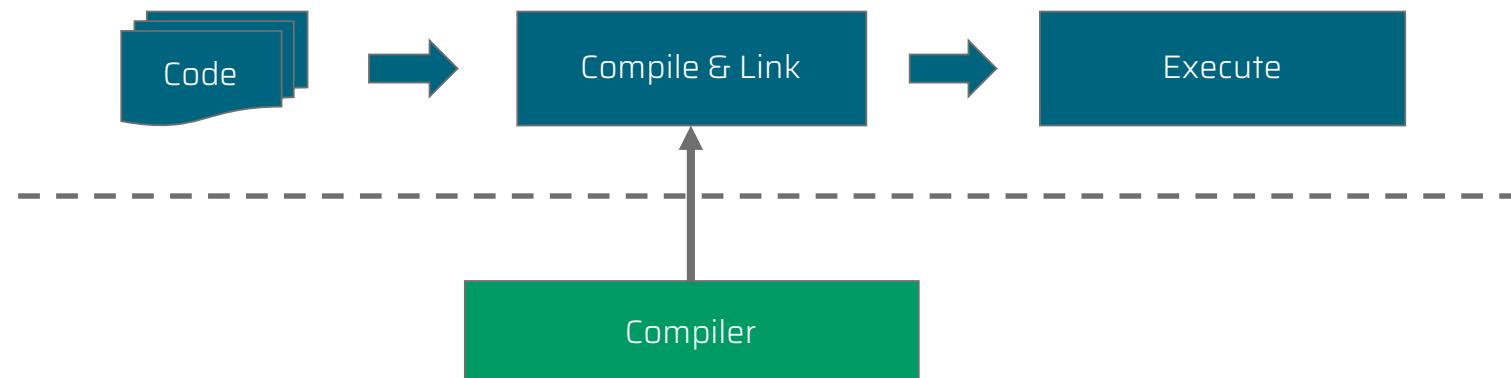
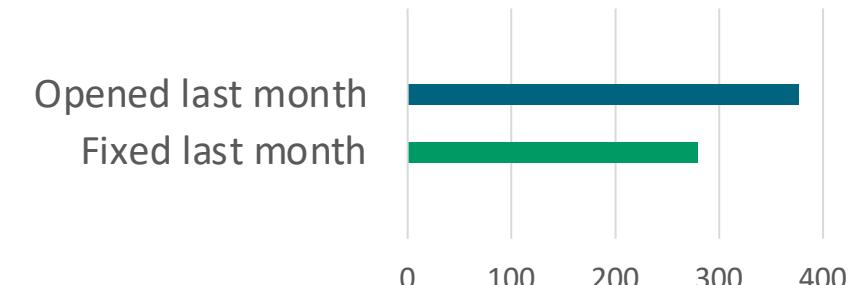
# (4) Toolchain Bugs - An example

## 4.9 series reproducibly corrupts register R7

Code below does not exhibit problem when compiled with 4.8 2014q3. Code trashes register R7 with 4.9 2015q1, q2, or q3 and the flagged braces are uncommented. Commenting out the flagged braces removes the problem with the 4.9 series.

GNU Arm gcc #1527413 [3]

GNU gcc Toolchain  
Total Bugs open: 14.318  
November 2020 [4]



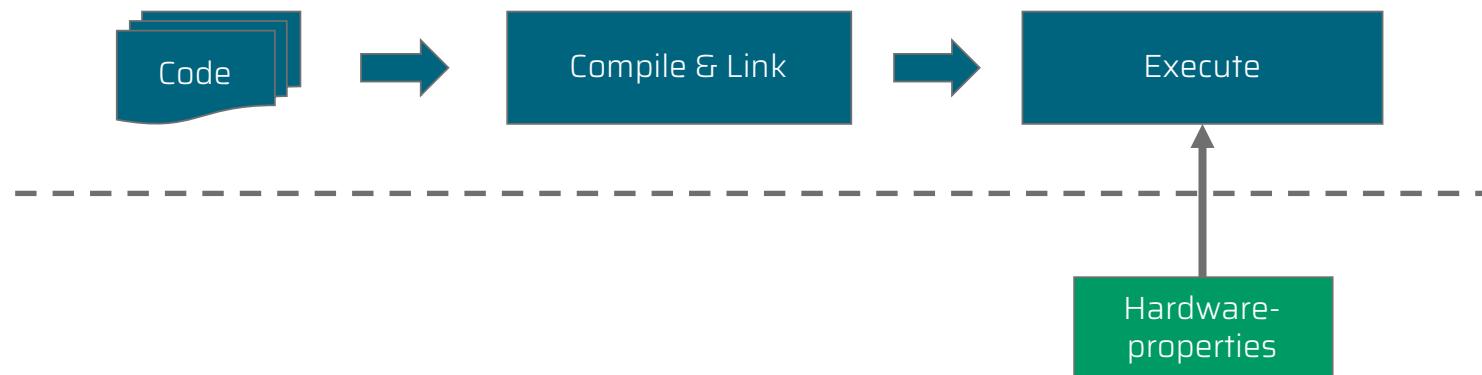
# (5) Hardware Bugs – An example

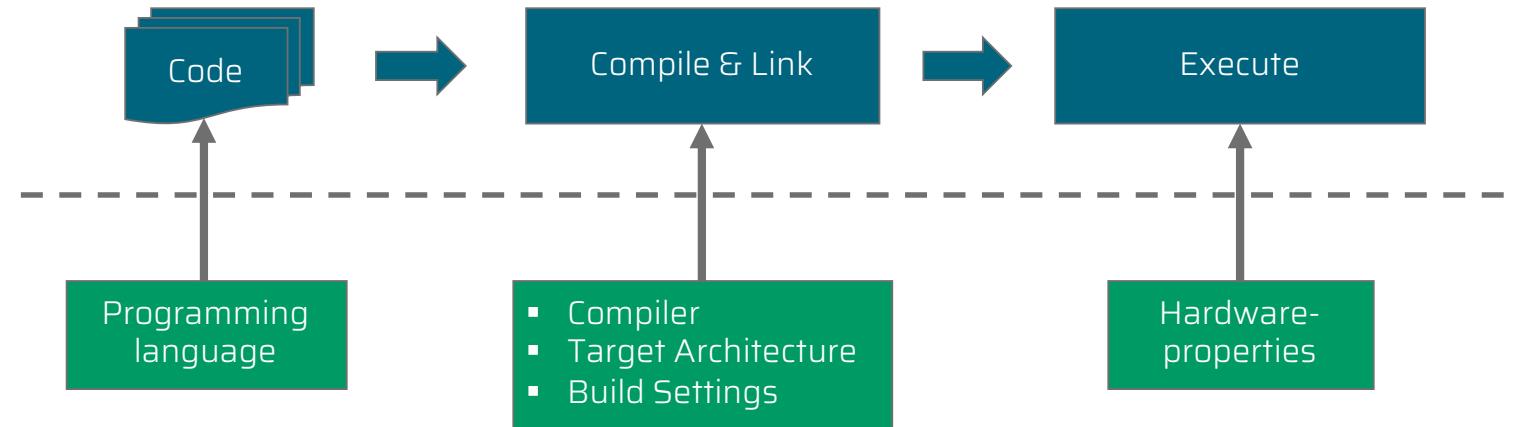
## Fused MAC instructions give incorrect results for rare data combinations

The Cortex-M4 processor includes optional floating-point logic which supports the fused MAC instructions (VFNMA, VFNMS, VFMA, VFMS). This erratum causes fused MAC operations on certain combinations of operands to result in either or both of the following:

- A result being generated which is one Unit of Least Precision (ULP) greater than the correct result.
- Inexact or underflow flags written to the Floating-point Status Control Register, FPSCR, incorrectly.

Arm Errata 839676 [5]





# Workflow recommendations

How to mitigate the off-target/on-target gap



## I) Prevention

- Use a high warning level
- Treat warnings as errors
  
- Use multiple compilers  
(e.g gcc, clang, Visual Studio)
  
- Use static analyzers



## II) Start Unit Testing

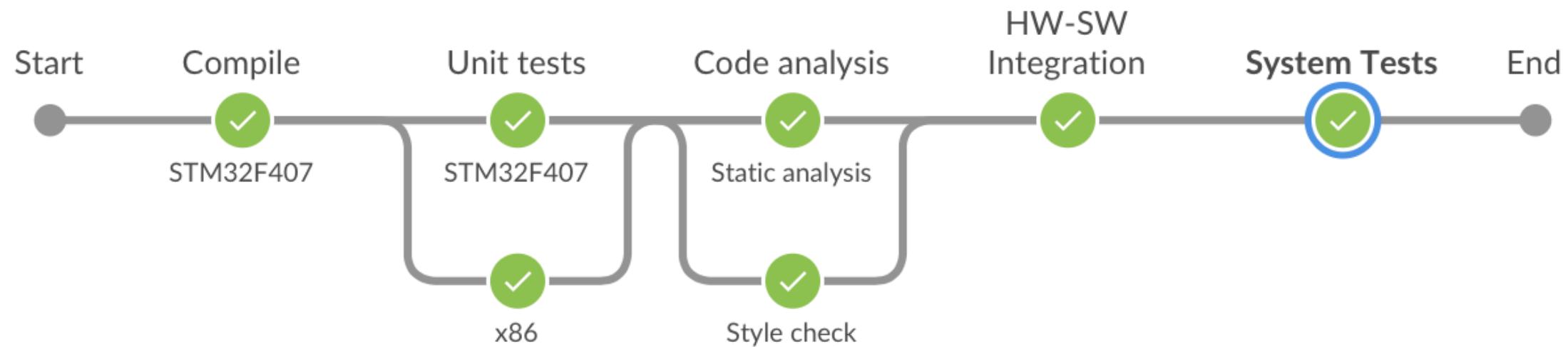
- Focus on areas with high density of logic



### III) Leverage Your Unit Tests

- Run them on-target
- Use build settings from production
- Test with unoptimized and optimized builds to expose undefined behavior („triangulate“ broken code)

# IV) Automate everything in your CI



# How to automate On-Target tests?



## The embeff ExecutionPlatform

- Supports all major testing frameworks
- Supports On-Target unittests
- Supports your specific MCU
- No more hardware needed
- Easy Continuous Integration setup

<https://embeff.com/executionplatform/>



# Summary

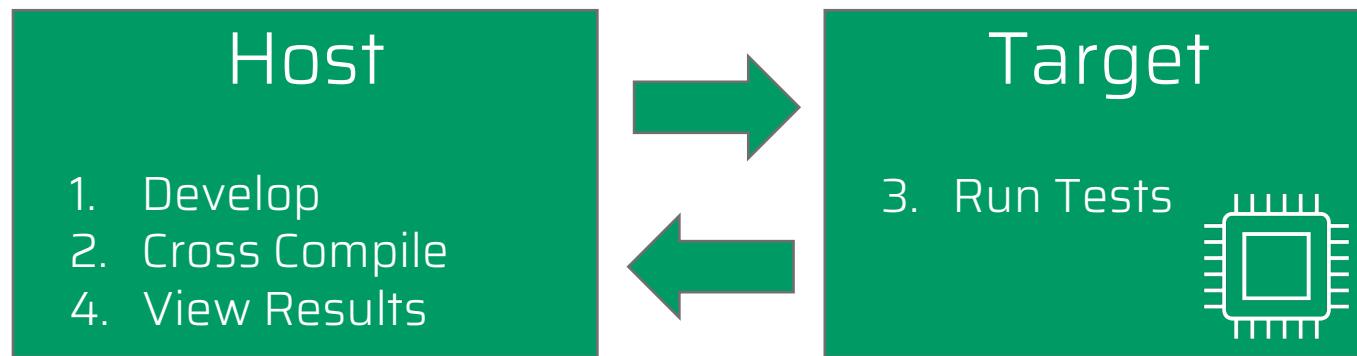
Problem	Mitigation
(1) Different Evaluation Order	Multiple Compilers, On-Target Tests
(2) Different Enum Size	Multiple Compilers, On-Target Tests
(3) Optimizer Affects Result	Warnings, Static Analyzer, On-Target Tests
(4) Toolchain Bug	On-Target Tests
(5) Hardware Bug	On-Target Tests



Prevention

Unit Testing

Leverage



## “On-Target” Tests

- slow
- difficult to automate
- reliable



## “Off-Target” Tests

- fast
- easy to automate
- unreliable



# Thanks for listening!



Making high quality  
embedded software  
commonplace.

### Embedded Software

- Real time
- Many target systems
- Low level code
- Quality standards

### Classical Software

- Automation
- Fast feedback
- Reusable libraries
- Sophisticated tooling

Contact  
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Phone +49 (451) 16088698

## [1] Free C++98 Standard Draft

- <http://www.lirmm.fr/~ducour/Doc-objets/ISO+IEC+14882-1998.pdf>

## [2] Size of enum implementation defined

- <https://timsong-cpp.github.io/cppwp/n4659/dcl.enum#7>

## [3] ARM gcc 4.9 series reproducibly corrupts Register R7

- <https://bugs.launchpad.net/gcc-arm-embedded/+bug/1527413>

## [4] GNU gcc Weekly Bug Summary

- <https://gcc.gnu.org/bugzilla/page.cgi?id=gcc/weekly-bug-summary.html>

## [5] Arm Cortex-M4 Errata

- [https://static.docs.arm.com/epm039104/30/Cortex-M4\\_Software\\_Developers\\_Errata\\_Note\\_v3.pdf](https://static.docs.arm.com/epm039104/30/Cortex-M4_Software_Developers_Errata_Note_v3.pdf)