

Timer prescaling and clear Timer on Compare (CTC)

Prescaling: Divides the clock by various powers of 2, thereby increasing the timer period.

The maximum prescaling value is 1024. That is achieved by setting CS10 and CS12.

$$TCCR1B = (1 \ll CS10);$$

$$TCCR1B = (1 \ll CS12);$$

This gives a timer period of  $1/16 \times 10^6 \times 1024 = 6.4 \times 10^{-5}$  sec (Per count)

$\therefore$  the timer will overflow every 65,535  $\times 6.4 \times 10^{-5}$  sec

$$= 4.194 \text{ seconds}$$

CTC mode

If we want our timer to trigger an interrupt after 1 second, say. Then we need to use clear timer on compare match mode.

Instead of counting until an overflow occurs, the timer compares its count to a value stored in a register. When they are equal, the timer can set a flag or trigger an interrupt.

To use CTC to trigger an interrupt after a specific time  $t$  ( $t \leq 4.194$  sec). We need to figure out how many



```
ISR ( Timer1-CompareA_vect ) {
    ?
    digitalWrite ( ledPin, !digitalRead ( ledPin ));
}
```

To trigger the call to a function at time intervals  $> 4$  seconds we can use a counter variable. For example, to trigger an interrupt of 10 seconds:

```
ISR ( Timer1-CompareA_vect ) {
    Second++;
    if ( seconds == 10 ) {
        seconds = 0;
        digitalWrite ( ledPin, !digitalRead ( ledPin ));
    }
}
```

Remember: seconds must be declared as volatile:

```
volatile int seconds
```