CSIIIO Systems Degamention

Sight Recap (will more dolail)

Note that the user has no access to the Instruction Register, the Memory Bus Register, and the Memory Address Register.

4.L. U. Arithretic Logie Unit

Arithetic Operators

Addition · Subtraction · Increment · Decrement

Modern All. s have many more!

Logic Operators

- · AND

- · CLEAR (G: zeros Le bits)

More here - he's going too fact to take it down

Well-constanted computers can perform an instruction in the time it would take light to travel the length of your fingurail.

Sometimes we need to successive flip-flogs, the output slows by powers of two, and we can get closes at different rates.

the clock is a constal in which the lattice structure has a regular oscillation. The oscillation is amplified and sensed to drive the clock.