CS1110
Systems Organisation

25/11/2015

The Exam (cont.)

Question 2 (a) → (c)

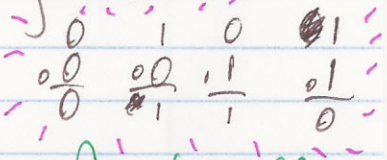
along these lines

6 (a) Two's Complement ; Negation ; etc.

8 (b) Implementing expressions with 1 gate (either NAND or NOR)

6 (c) Create Subtraction like we created a half-adder.

Question will say "half-subtractor".



↑ this question is unfair he says, so that's why he's telling us this.

Question 3 (a) → (d)

6 (a) Draw a circuit to implement ... and express as an equation. This will be familiar.

6 (b) MUX

4 (c) MUX – The new thing today.

4 (d) Prove

Question 4 (a) → (c)

8 (a) Given truth table get equation, simplify and implement

8 (b) Flip-flop and etc.

4 (c) Answer is six words long (~)