

Hand up lab books by next Friday.

Staple them all together if they're loose sheets.

Secretary's office.

They'll be available in the secretary's office for collection from early next week.

Years work will be available on the webpage before the exam.  
Out of 20%.

Wednesday's lecture will be entirely about the exam.

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## Instruction Set Architecture (ISA)

CISC: Complex Instruction Set Computer

e.g. Motorola 68k, Intel x86

suits coding by hand

Many types of instructions.

Not the most efficient in terms of hardware

RISC: Reduced Instruction Set Computer

- Introduced by IBM
- Typically used by microcontrollers
- Broke down multi-cycle instructions into multiple single-cycle instructions
- The clock rate can be increased because it typically depends on the longest-running instruction.
- Suits compiler optimization.