

Curriculum vitae

Umesh G

E-Mail ID: umeshgptan09@gmail.com

Mobile No: +91 9880630816

Career Objective:

To work in an organization where I can enhance my skills, knowledge and use them effectively to meet the organizational goals and as well as personal growth.

Educational Qualifications:

Educational Qualification	Name of the institution	University or board	Year of passing	Aggregate (%)
B.E. (ELECTRONICS & COMM. ENGG)	Ballari Institute of Technology and Management, Bellary	Visvesvaraya Technological University, Belgaum	2015	65.69%
12 th	Vasavi Composite Pu College, Bellary	Department of Pre-University education, Karnataka	2011	78.27%
10 th	Kanakanikethana Vidhya Mandira HighSchool, Tekkalakote	Karnataka Secondary Education Board, Karnataka	2009	82.24%

Technical Exposure:

- Technical Languages: Basics of C
- Basics of java

MINI PROJECT:

Electronics Eye: “The electronic eye which is generally fixed in the door or locker for the security purpose, when anybody tries to open the door or locker his or her shadow fall on the LDR and the circuit gets activated and produces a sound through the buzzer”.

MAJOR PROJECT:

TITLE: POWER ANALYSIS OF CMOS CELL AND ADIBATIC LOGICS

SOFTWARE USED: TANNAER EDA 13.0

Adiabatic process means transferring of energy without loss with the environment and these VLSI chips to achieve high system performance. The power saving of adiabatic circuit can reach more than 90% compared to conventional static CMOS logic. The clocking schemes and signal waveforms of adiabatic are different from those of standard CMOS circuits. This thesis work demonstrates the low power dissipation of Adiabatic Logic by presenting the results of designing various design/cell units employing Adiabatic Logic circuit techniques (an inverter, a two-input NAND gate, a two-input NOR gate, a two input XOR gate, a two-to-one multiplexer

and a one-bit carry Adder) This paper also investigates the different power delay product over the wide range of supply voltages. If number of transistor on the circuit increases power dissipation is less.

Personal Abilities:

- Can work efficiently in a group as well as individual take up the responsibilities.
- Good leadership qualities.
- A good team player.
- Passion for constant improvement.
- Ability to adjust in any kind of environment.
- Self-confidential and self-motive attitude.
- Zeal to learn new technologies.

Extra Curricular Activities:

- Participated in national level tech fest INSIGNIA'12 held at SDM Dharwad.
- Conducted a mini project on “**Electronic Eye**” and participated in exhibition at BITM in 2013.
- Runner up in cricket in 2012 and 2014 college competition.
- Participated in 5 days workshop entitled “**Cracking the programming interview**” by MINDSCULPTOR System held at Bangalore.

Hobbies:

- Playing cricket.
- Reading history books

Personal Details:

- Father's Name : Venkatesha G
- Mother's Name : Rajamma
- Date of Birth : 1st June 1993
- Sex : Male
- Nationality : Indian
- Languages Known : English, Kannada and Telugu
- Permanent Address : Umesha G
S/o Venkatesh G
Uppara Hosahalli (post),
Siruguppa (Tq), Bellary (Dist)
Karnataka, PIN: 583122

Declaration:

I do hereby declare that the information furnished above is true to the best of my knowledge.

Date:

Place:

Umesha G