

Parag C. Bari

Mb # 09579742749

09372467908

Permanent address:-

Plot no 2/C, Near lions hall
Adarsh Nagar
Jalgaon : 425002 (MH)

Temp. Address : (Present)

Ashoka Housing Sos ,
Chinchwad ,
Pune 410033

e-mail ID:- paragbari@gmail.com

Objective

Looking for opportunity to enhance more my career with a company , so I can turn my vision in to reality with in the profession direction to develop myself with growth of my working group.

• Experience

15 Feb 2007 – 30May 2007 | Supervisor (SW-3)

LG Electronics , India | MIDC, Ranjangaon, Pune

In LG Elect. Working as a supervisor (SW) under Quality Assurance department. I had work related to our ODD(Optical Disk Drive) production Outbox Assurance. We had a different tests to check the quality of our product. We are having a reliability test also.

My responsibility towards my department were as follow...

- ✓ To check the quality of product under ISO standards.
- ✓ Tack care of quality on production line.
- ✓ Tack care of different audits under pre design system.
- ✓ Also having a responsibility towards Reliability Chambers.

June 2012 | Electronic Engineer(Sr.)

Next Step Technologies , India | Chinchavad, Pune

In NST,I am Working as an Engineer for SPM &Andon system. Responsible as Electronics Engineer for various product development.

My responsibility towards my work as follow...

- ✓ Understanding the clients requirement for SPM & fulfill with help of my team.
- ✓ Working specialty with electronic module development.
- ✓ Working specialty for Andon system – TPM/TQM/lean manufacturing ,TT, wired-wireless system, Kaizen(having knowledge to develop Andon systems)
- ✓ Team lead for new projects.
- ✓ Installation & commissioning for SPM & Andon systems.
- ✓ Generating & maintaining technical documents such as product manuals.
- ✓ We design SPM for automobile sector for computerized final testing machines.
- ✓ Working on Andon system development and new projects for same.



Education

1. **BE** in Electronic & Telecommunication Engineering (**E&TC**)from “Pune University ”**Completed with First Class** .
(College-Sinhgad Institute of Tech. Lonavala, Pune(2011))
2. **Diploma** In Electronic & Telecommunication Engineering (**E&TC**) from “ MSBTE ”**Completed with First class**.
(College-SSBT’s New Polytechnic ,Jalgaon)
3. **SSC** examination from Pune Board with **First Class**.
(School-R. R. Vidhyaya , Jalgaon)
4. Diploma in Computer Repair &Assembly from Soft-Aid Institute, Jalgaon.
5. “ **VLSI-RN** ”A Certified **Advanced VLSI Design and Verification Training** ,
From - Maven Silicon VLSI Design and Training Center, Bangalore
Year: Feb2012
6. Willing to get admitted for “Exe. MBA- Process management” from Symbiosis ,pune.

Skills

- Working knowledge for PLC logic designing, Labview programming , Microcontroller logic designing.
- Logic development for systems.
- VLSI – VHDL , Verilog programming , simulation.
- Team work , Team lead, self-motivated.
- Better compatibility with Electronic and Computerized Equipment .
- Tracking ,hiking , Rock climbing.
- Working Language- English , Hindi , Marathi.
(Open to learn international languages.)

Engineering Project

- **Ultrasonic based RADAR System.**

This is my BE project. Purpose of this project is to overcome disadvantages of conventional RADAR technology by using a Ultrasonic advantages. We got first position in project exhibition also did a Paper-Presentation on same. Our project is selected as a Practical equipment for academic subject. It's based on ARM Processor and Ultrasonic sensor.

- **DTMF Remote Control system.**

This is my Diploma project .Purpose of project is giving convenience to user to operate equipment with help of telephone. It's a ON/OFF application using telephone. It's based on Controller and DTMF IC.

- **Hardware-Software Co-design.**

This is my research project with my Prof. V V Deotare from my college SIT, Lonavala. My contribution for this project is to make documentation & Proposal and gain more and more knowledge. It is based on FPGA Xilinx.

- **SPI Controller Core - Designing**

HDL -Verilog

HVL: SystemVerilog

EDA Tools: ModelSim, Questa -- Verification Platform

Description :The SPI Controller Core is an interface between wishbone compatible Master Device and SPI interface Slave device.

Industrial Project

1. CDI / RR Auto-test bench.

CDI is a two-wheeler's electronic unit responsible for generating spark. We designed the auto-test bench for this unit with simulation bike to read various readings as Current , voltage , Spark angle as mention in costumers specification sheet.. We generate these test bench with static as well dynamic source.

2. Magneto Auto-test bench.

Magneto set is a two-wheeler's mechatronic unit responsible for generating Pulsar waveform for CDI and AC voltage generation for RR. We designed the auto-test bench for this unit with simulation bike to read various readings as Current , voltage , Spark angle & read pulsar & Excitor waveforms. as mention in costumers specification sheet.. We generate these test bench with static as well dynamic source.

3. Andon system 7segment display development.

Andon systems started from Pull cord system to Plant information system , QRS system, Display systems for Production data and so the management can use this data to barnstorm on problems and find out solutions to reduce TT & bottleneck problems.

4. Plant information system project under Andon system.

This is combination of different Andon system tools (QRS , Display system , Wired-wireless systems) to keep an eye on production data , process. It starts from getting Actual & Rejection signals from machine to display on local screen on machine as well Office display and store data in server to analysis as per user requirement.

Achievements

- Participated **forRoBoCoN 2011 at Balewadi Stadium, Pune** as a Team Leader to represent our college team first time.
- Event Management for College Functions as “Mutation_2011” which is a Project exhibition for Engineering students.
- Event Management for Adventure Sport in college Function.
- Working on our BE-Project deal as a Practical Kit.
- **Workshop** on “Embed Design” at SIT , by **IIT Pavae**.
- **Workshop** on “Robocon for Robotics” at MAE, by Robocon India
- Working for **NEF EndurO_3** as a Marshal.(Its Most favorite adventure race organizer in Pune for the K2S & Enduro3 race.)
- Also working with “ **Itant Outdoor** ” an adventure club in Pune.

• **Reference**

On Request.