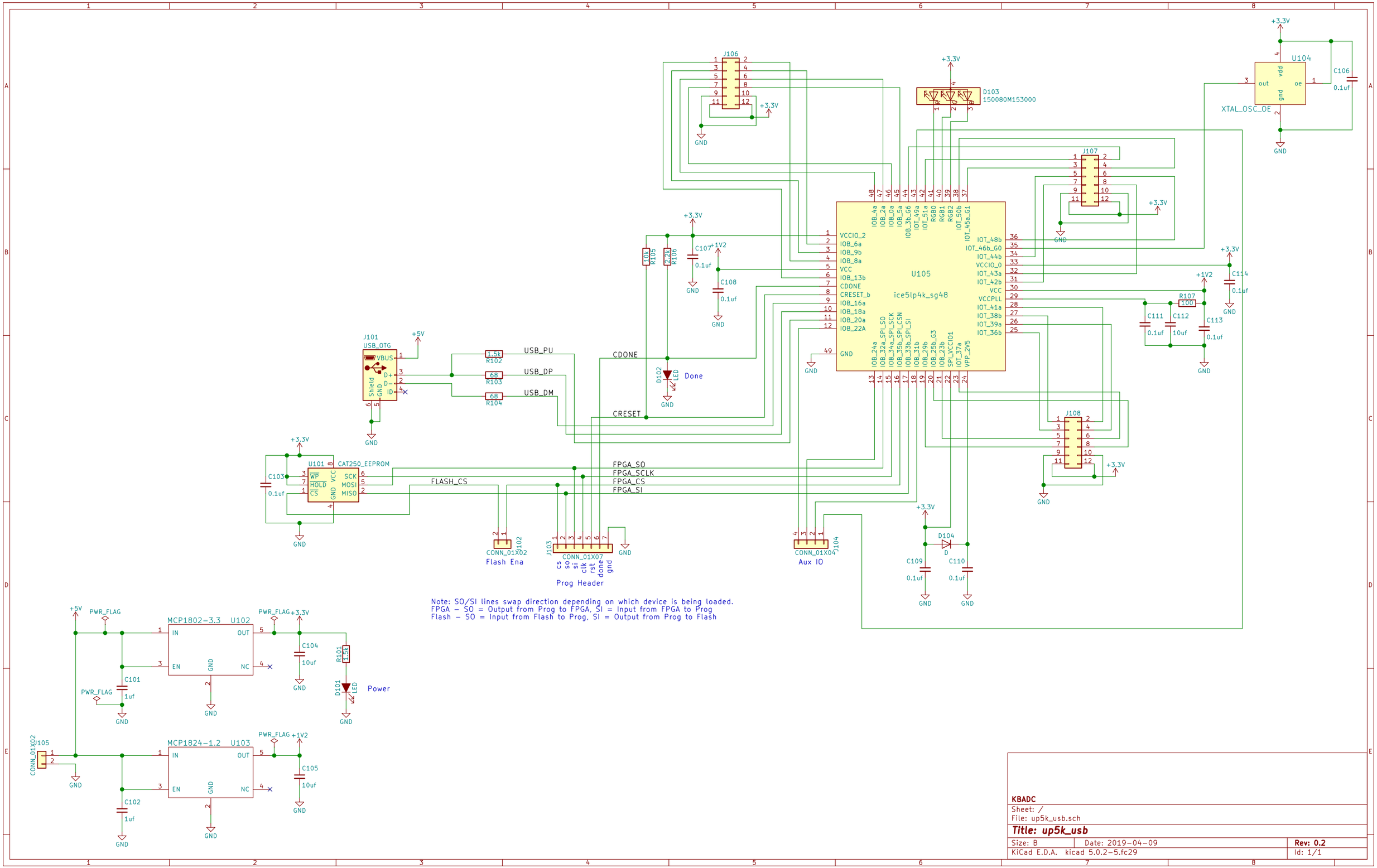
U105
ice5lp4k_sg48
U104
XTAL_OSC_OE
D103
150080M153000
J106
J107
J108
J101
USB_OTG
USB_PU
USB_DP
USB_DM
CDONE
CRESET
Done
FPGA_SO
FPGA_SCLK
FPGA_CS
FPGA_SI
FLASH_CS
U101
CAT250_EEPROM
J102
CONN_01X02
Flash Ena
J103
CONN_01X07
Prog Header
J104
CONN_01X04
Aux IO
Note: SO/SI lines swap direction depending on which device is being loaded.
FPGA - SO = Output from Prog to FPGA, SI = Input from FPGA to Prog
Flash - SO = Input from Flash to Prog, SI = Output from Prog to Flash
U102
MCP1802-3.3
U103
MCP1824-1.2
Power
J105
CONN_01X02
KBADC
Sheet: /
File: up5k_usb.sch
Title: up5k_usb
Size: B
Date: 2019-04-09
Rev: 0.2
KiCad E.D.A. kicad 5.0.2-5.fc29
Id: 1/1