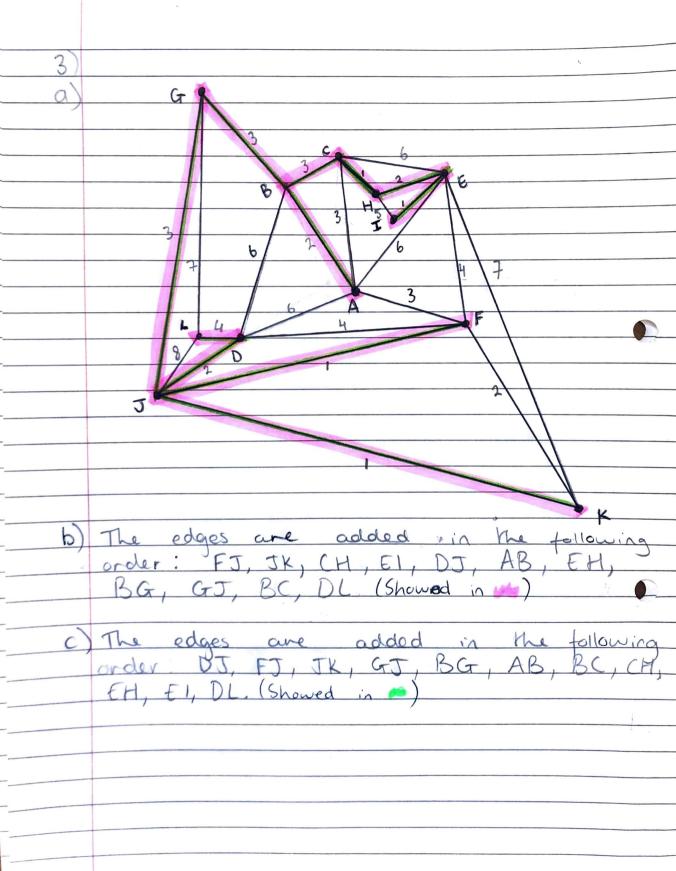


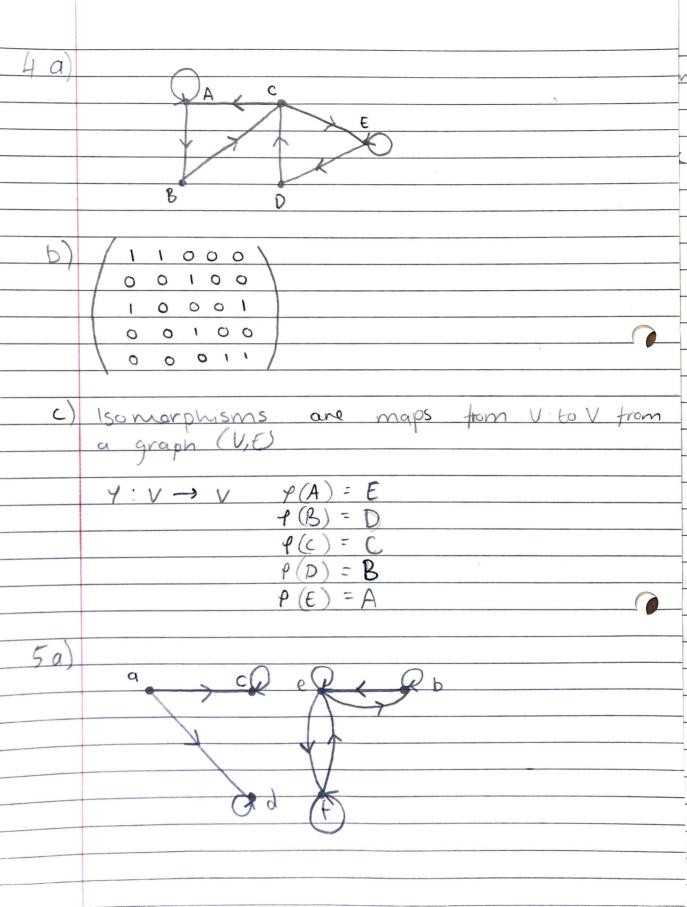
9) (2-regular) and Ee, d, c, h} and the edges between them (2-regular) and the edges between them and the edges between h) Isomorphisms are maps from V to V for a graph (V, E) $\varphi: V \to V \qquad \varphi(a) = b$ P(b) = a P(C) = C P (d) = d P (e) = R P (+) = + 4 (9) = 9 9 (h) = h i) No, it's not unique because as for A example we could have y (a) = a W(6) = b W(C)= C W (d) = d w(e) = e W(+): 9 W(9)= TV (h) = h W(i) = 1 4(K) = K

i) The graph is connected as there is a walk from every vertex to every other vertex. K) deg h = 5 and deg j = 3, so we have two wertices of odd degree, whereas the nest of the vertices have even degrees deg a = deg b = deg g = deg t = deg d = deg k By collary in betwee 37, This graph D) Since not all vertices have even degrees which is a ressury condition for the existance of an Fulerian circuit (Covollary 2 in lecture 35), this graph does not have an Enterior circuit. m) No, as we would have to pass through vertexs h, e, and c twice. n) It is not a tree as it contains circuits as for example hkjh, aboa and gteg.

We have to prove an equivalence, which we will do by proving each implication in hurn. >" The graph (V, E) is a tree. In lecture 39 we proved for any two vertices in the graph (V, E), there is exactly one path between them. Vu, v ∈ V s.t. u≠ v and u, v are nonadjacent, adding the edge uv creates a second unique path from a to V. UV E E, so we denote the graph (V, E,). In lecture 34, we proved, for any graph (V,E) if there is more then one unique path between any two vertices in the graph (V, E) then the graph contains a simple circuit for the graph (V, E,), there are two unique paths from i to v, so the graph at least one simple circuit. Assumn adding contains uv to (V, E) creates two circuits, for this to be true (V,E) must contain a circuit before us is added > this is a contradiction because (V, E) is a tree so it is acyclic. This means adding an edge between any two vertices in (V, E) creates exactly one circuit. - "Adding an edge between any two vertices in (V, E) creates exactly one circuit. Assume (V,E) has two separate connected components. We have two sets V, and Vz with V, AVz = & and VIUVZ = V. Au, v eV s.t u x V and u eV, veVz. There dosen't I a path between u and V they are in separate connected components. - because Adding the edge us creates one path between u and v. for a circuit to be created. There

needs to be at least two unique paths fromu to v => = This is a contradiction becauseadding an edge creates a grant. Thus (V,E) is a connected component. Assumn (V, E) has a circuit. Yu, v EV s.t u, v are nonadjacent and have two unique paths u to v. Adding an edge us creates 3 unique paths from u to v and 2 circuits => (= because adding en edge between any two vertices-(V, E) is acyclic. (V, E) is connected and acyclic so (V, E) is a tree.





5 b) No, for R to be an equivalence relation it must be reflexive, symmetric and transitive. a is not related to a so R is not reflexive. Also it is not symmetric because a relates d but d dosen't relate d. Also it is not transitive because frelates e and e relates b but f dose not relate b. c) (a,a) (d,a) (c,a) (f,b) (b,t) (d,c) (c,d)