

# **LP0404N3T5G**

# 20V, P-Channel (D-S) MOSFET

#### 1. FEATURES

• VDS = -20V

RDS(ON) $\leq$ 0.48 $\Omega$ ,VGS@-4.5V,IDS@-780mA

 $RDS(ON) \leq 0.67\Omega, VGS@-2.5V, IDS@-660mA$ 

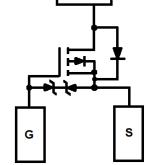
 $RDS(ON) \leq 0.95\Omega, VGS@-1.8V, IDS@-100mA$ 

 $RDS(ON) \leq 2.2\Omega, VGS@-1.5V, IDS@-100mA$ 

- Super high density cell design for extremely low RDS(ON).
- Exceptional on-resistance and maximum DC current capability.
- We declare that the material of product compliance with RoHS requirements and Halogen Free.
- S- prefix for automotive and other applications requiring unique site and control change requirements; AEC-Q101 qualified and PPAP capable.



- Power Management in Note book
- Portable Equipment
- Battery Powered System



SOT883

#### 3. DEVICE MARKING AND ORDERING INFORMATION

Device	Marking	Shipping
LP0404N3T5G	T5	10000/Tape&Reel

#### 4. MAXIMUM RATINGS(Ta = 25°C)

Parameter	Symbol	Limits	Unit
Drain-to-Source Voltage	VDSS	-20	V
Gate-to-Source Voltage	VGS	±6	V
Drain Current (Note 1)	ID		А
Steady State	ا ا	-1.4	

Note 1: Surface Mounted on 1" x 1" FR4 Board.



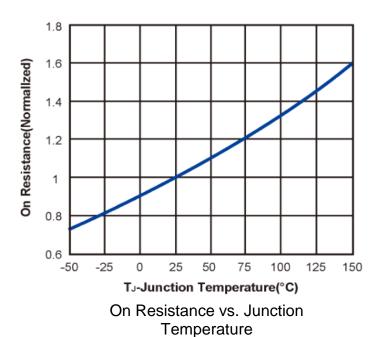
## 5. ELECTRICAL CHARACTERISTICS (Ta= 25°C)

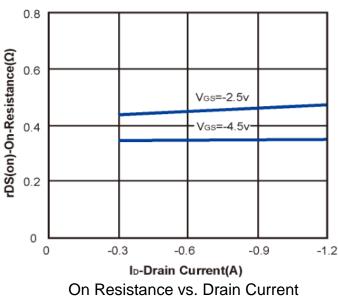
Characteristic	•	Symbol	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage		V(BR)DSS	-20	_	_	V
(VGS = 0V, ID =-250uA)		V(BK)D33	-20		-	V
Gate Threshold Voltage		VGS(th)	-0.4	_	-1.2	V
(VDS =VGS , ID =-250µA )		VOO(tii)	-0.4	_	-1.2	V
Gate Leakage Current		IGSS	_	_	±10	μA
(VDS =0V, VGS =±4.5V )		1000			±10	μΑ
Zero Gate Voltage Drain Current		IDSS	_	_	-1	μA
(VDS =-16V, VGS =0V )		1000			ı	μΑ
Drain-Source On-Resistance			_	_	0.48	
(VGS=-4.5V,ID=-780mA)				-	0.40	Ω
Drain-Source On-Resistance		RDS(ON)	_	0.67	0.67	
(VGS=-2.5V,ID=-660mA)					0.07	
Drain-Source On-Resistance			_	_	0.95	
(VGS=-1.8V,ID=-100mA) Drain-Source On-Resistance					0.00	
			_	_	2.2	
(VGS=-1.5V,ID=-100mA)					2.2	
Diode Forward Voltage		VSD	_	_	-1.2	V
(IS =-350mA, VGS =0V)		102				•
Dynamic						
Total Gate Charge	(VDS =-16V,	Qg	-	2.8	-	
Gate-Source Charge	VGS =-4.5V, ID	Qgs	-	2.1	-	nC
Gate-Drain Charge	=-200mA)	Qgd	-	0.5	-	
Turn-On Delay Time	(VDD =-10V, RL =50Ω,VGEN =-	td(on)	-	51.3	-	
Rise Time		tr	-	24.2	-	ns
Turn-Off Delay Time	5V,RG =10Ω,ID	td(off)	-	246	-	
Fall Time	=-200mA)	tf	-	81.2	-	
Input Capacitance	(VDS = -16 V,	Ciss	-	152	-	
Output Capacitance	VGS = 0 V, f = 1	Coss	-	18.5	-	pF
Reverse Transfer Capacitance	MHz)	Crss	-	6	-	

Note 2: Pulse test; pulse width  $\leq$  300µs, duty cycle  $\leq$  2%.



#### 6. ELRCTRICAL CHARACTERISTICS CURVES





Ciss

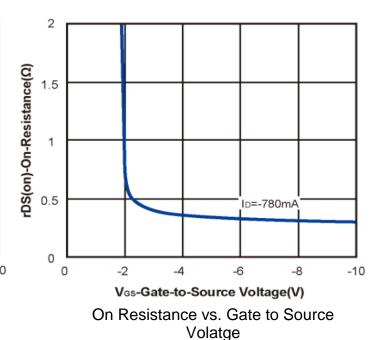
120

Coss

Coss

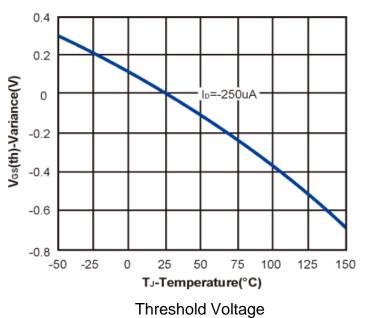
Vos-Dranin-to-Source Voltage(V)

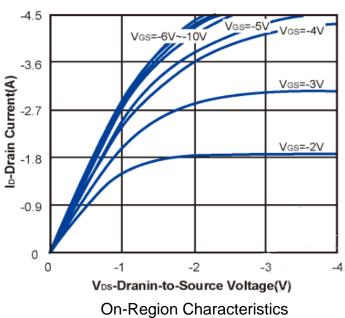
Capacitance

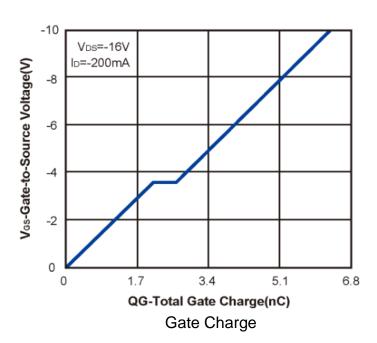


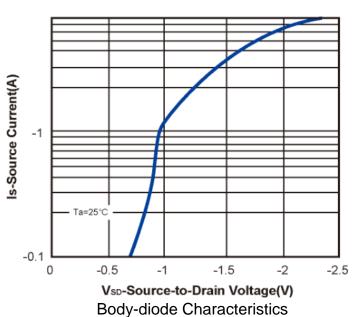


# 6. ELRCTRICAL CHARACTERISTICS CURVES(Con.)



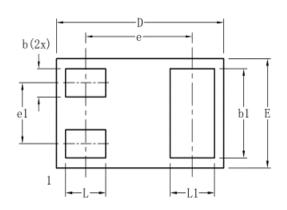


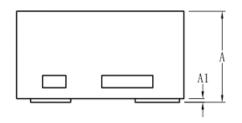






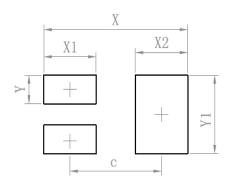
### **7.OUTLINE AND DIMENSIONS**





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DIM	MIN	TYP	MAX
D	1.05	1.00	0.95
Е	0.65	0.60	0.50
е	-	0.64	-
e1	-	0.34	-
L	0.19	0.24	0.29
L1	0.22	0.27	0.32
b	0.10	0.15	0.20
b1	0.44	0.49	0.54
Α	0.43	0.48	0.53
A1	0	-	0.05
All Dimensions in mm			

# **8.SOLDERING FOOTPRINT**



Dimensions	(mm)
С	0.70
X	1.10
X1	0.40
X2	0.40
Y	0.20
Y1	0.55

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