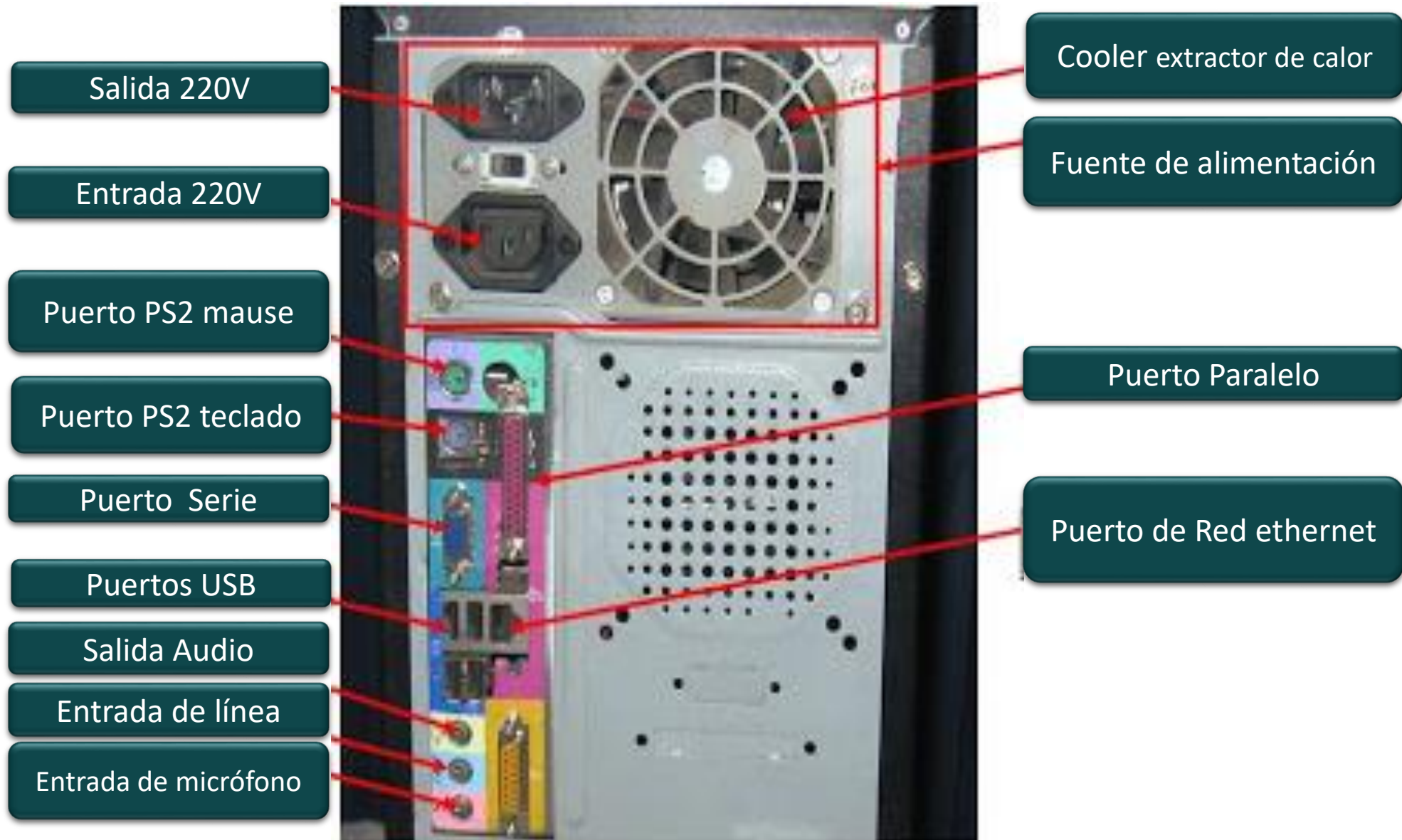


- ✓ Arquitectura básica
- ✓ Los 5 componentes
  - La CPU
  - La memoria
  - El bus de datos
  - Dispositivos de entrada
  - Dispositivos de salida



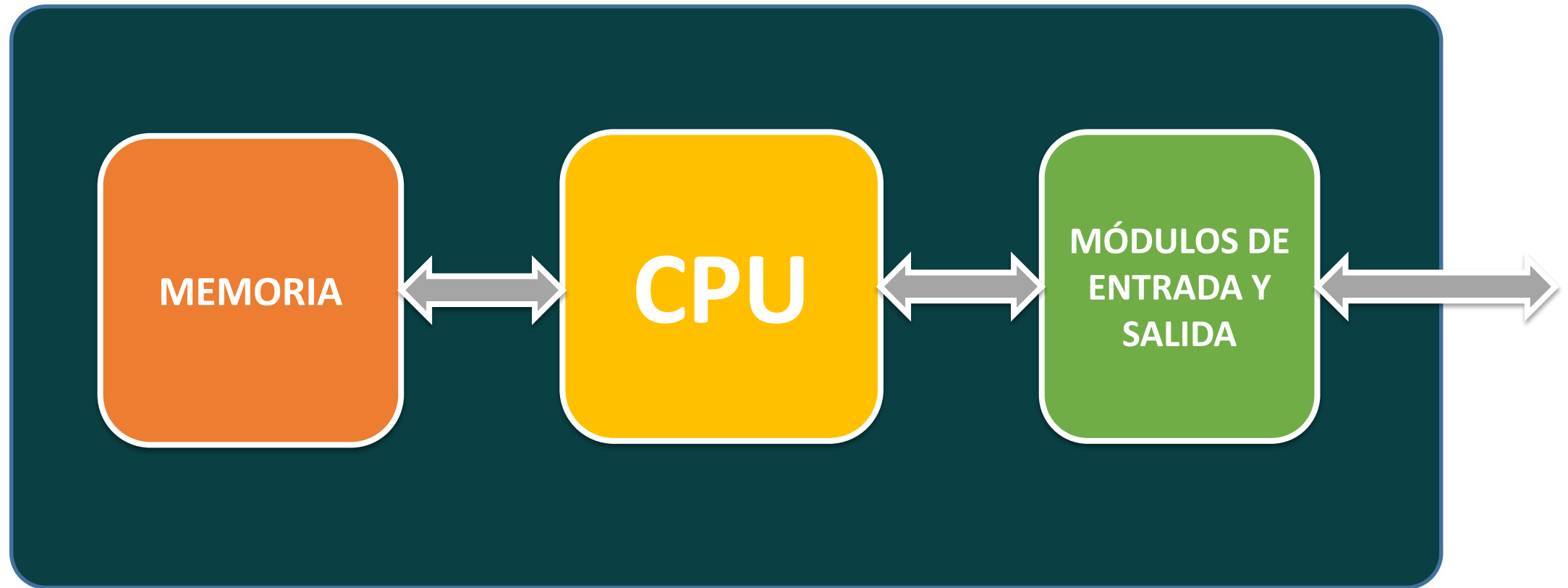


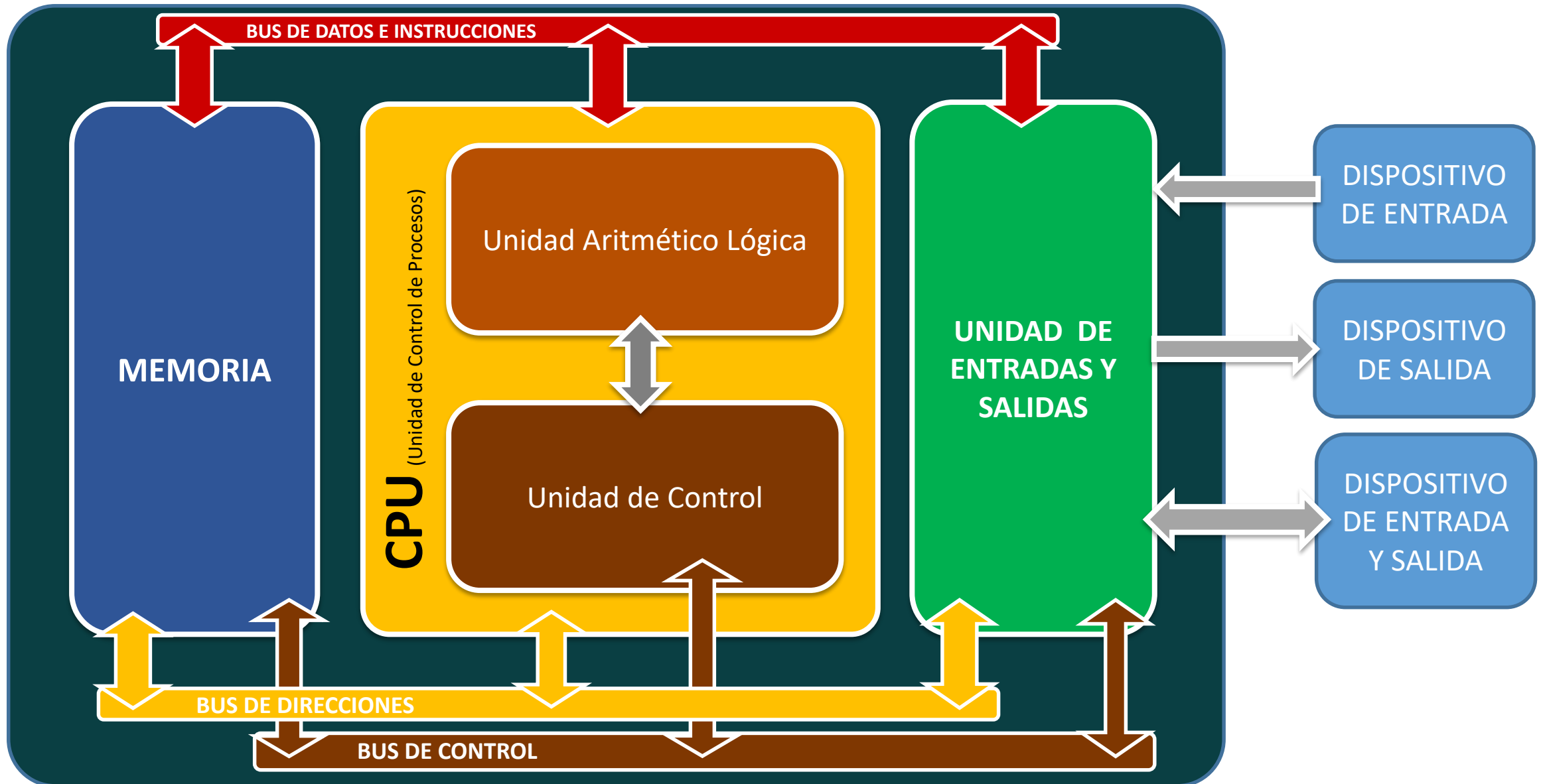


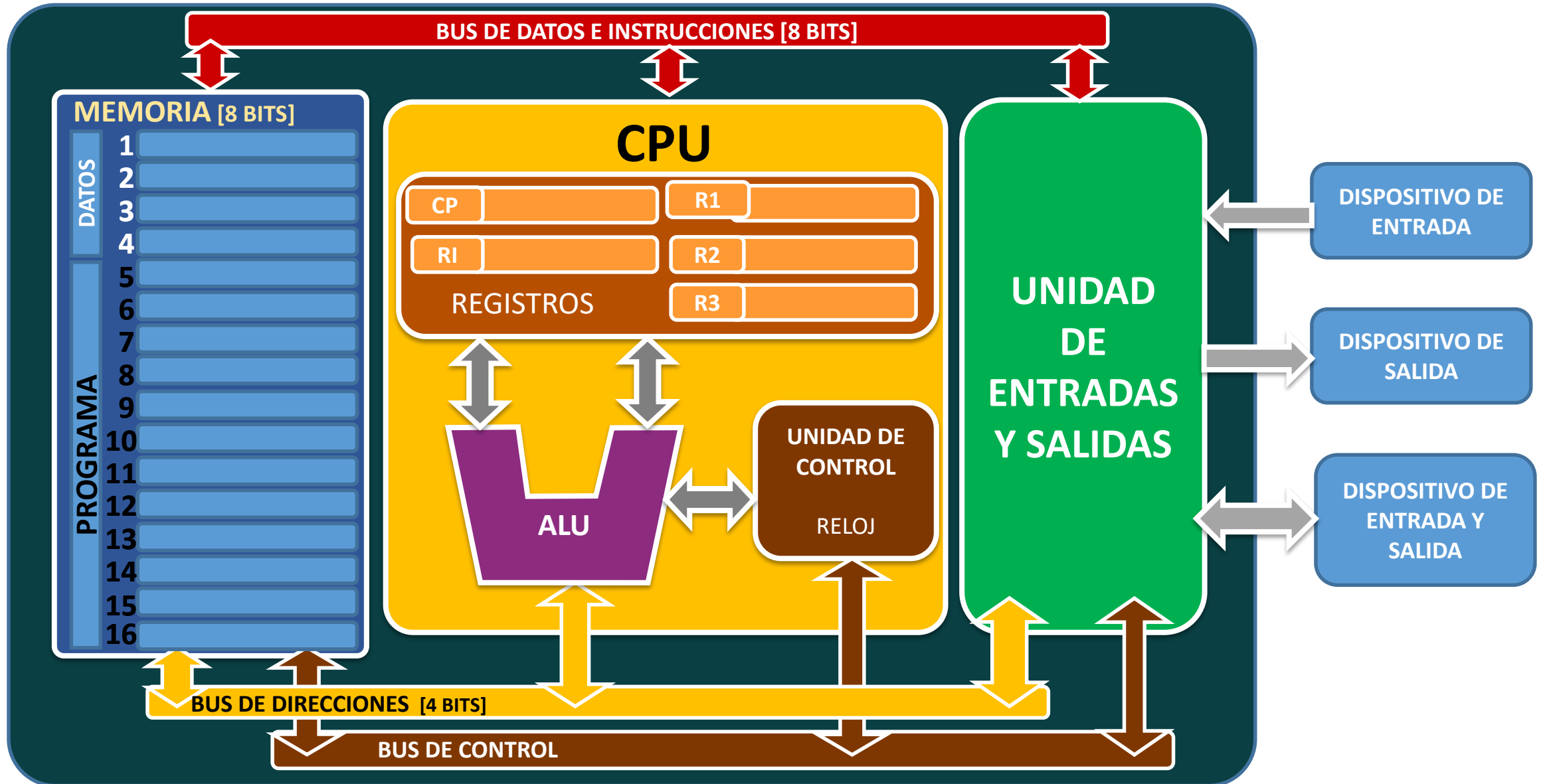


**SOFTWARE**

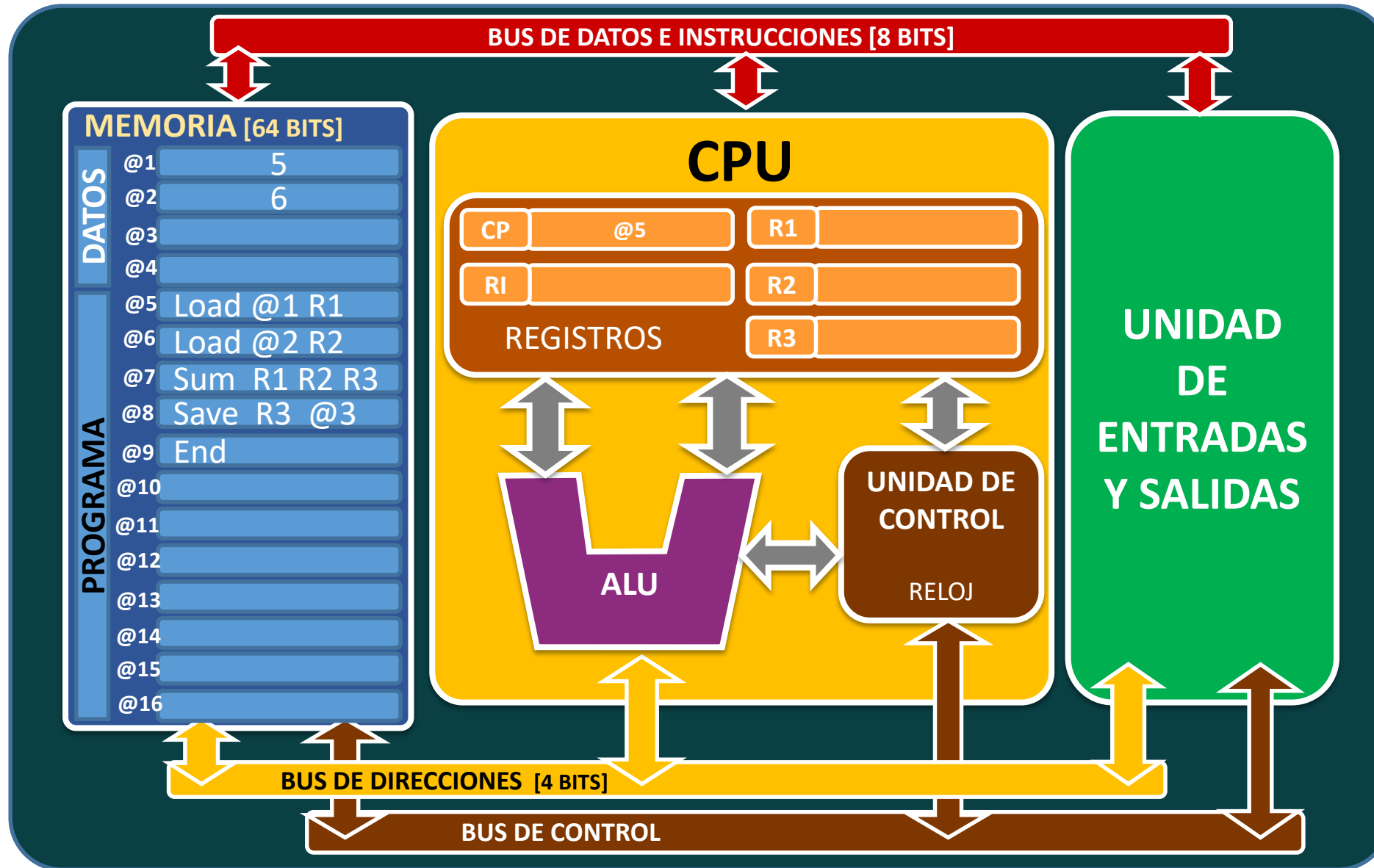
**HARDWARE**











Programa que suma  
2 Nros

Load @1 R1  
Load @2 R2  
Sum R1 R2 R3  
Save R3 @3  
End

Set Instrucciones:  
Load  
Save  
Sum  
End

# Simulador: <https://www.vnmsim.app/es-es>

The screenshot displays the vnmsim.app/es-es web simulator interface. The browser address bar shows the URL. The simulator has a dark-themed top menu with icons for 'Nuevo proyecto', 'Abrir desde archivo', 'Guardar proyecto', 'Muestras', 'Estadística', 'Ayuda', and 'Ajustes'.

On the left, the assembly code is listed:

```

0 // X + Y = Z
1 LOD X
2 ADD Y
3 STO Z
4 HLT
  
```

Next to the code is a register window showing the following values:

X	5
Y	6
Z	11
W	0
T0	0

Below the registers is a '+' button and a vertical stack of memory locations.

The main area on the right shows a hardware diagram titled 'Addition' with the subtitle 'Realizado por 2024-03-19'. The diagram illustrates the internal components of the simulator:

- IR (Instruction Register):** Contains the instruction 'HLT'.
- Decoder:** Receives the instruction from the IR.
- ALU (Arithmetic Logic Unit):** A trapezoidal block with inputs 5 and 6, and an output labeled '='.
- ACC (Accumulator):** A register showing the value 11.
- Data bus:** Connects the IR, Decoder, ALU, and RAM.
- Add. bus:** Connects the ALU to the PC and RAM.
- PC (Program Counter):** Shows the value 0.
- RAM (Random Access Memory):** A vertical stack with a value of 1.

At the bottom of the interface, there is a control bar with icons for play, stop, step, and a progress indicator.