L1 Track Trigger Latency

Pixels in L1 ("pull")
+6 µs

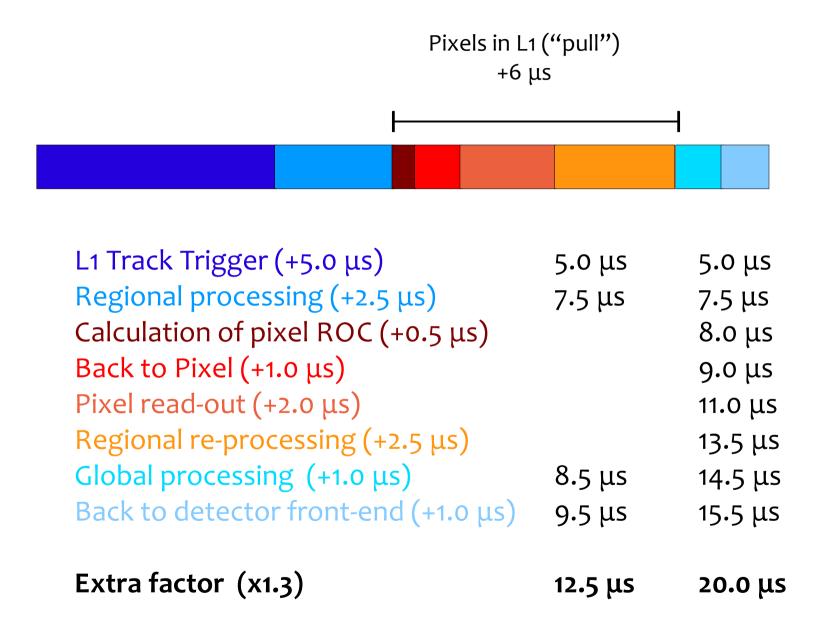|		
L1 Track Trigger (+5.0 µs)	5.0 µs	5.0 µs
Regional processing (+2.5 µs)	7.5 µs	7.5 µs
Calculation of pixel ROC (+0.5 µs)		8.0 µs
Back to Pixel (+1.0 µs)		9.0 µs
Pixel read-out (+2.0 µs)		11.0 µs
Regional re-processing (+2.5 µs)		13.5 µs
Global processing (+1.0 µs)	8.5 µs	14.5 µs
Back to detector front-end (+1.0 µs)	9.5 µs	15.5 µs
Extra factor (x1.3)	**12.5 µs**	**20.0 µs**

based on W.Smith https://indico.cern.ch/event/378538/contribution/13/material/slides/1.pdf