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So we implemented **TRAC**, which  
transforms DAFSMs in a DSL to specify DAFSMs  
verifies well-formedness condition relying on the SMT solver Z3

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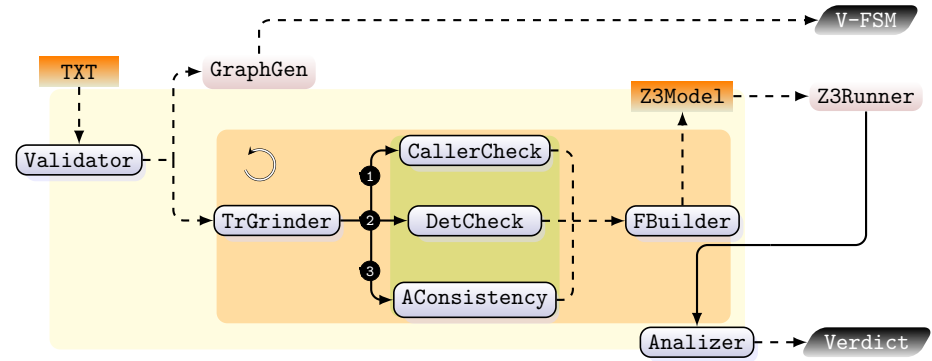
└ Verification

Verification

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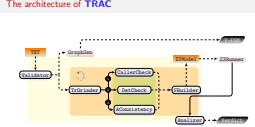
# The architecture of TRAC



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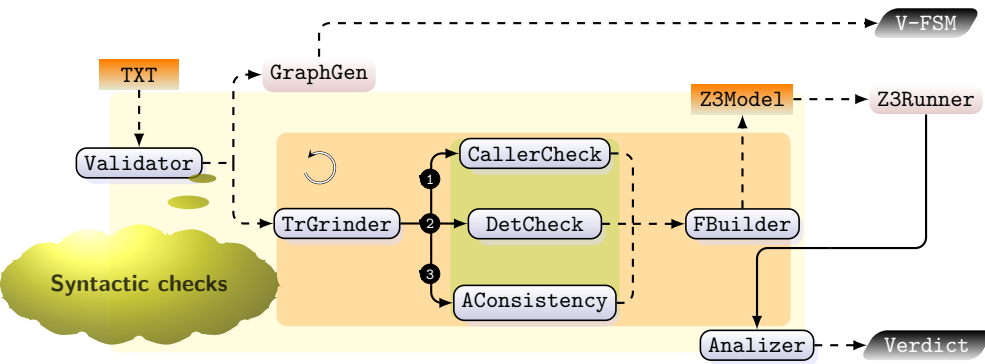
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## The architecture of TRAC



the architecture of **TRAC** is compartmentalised into two principal modules: parsing and visualisation (yellow box) and **TRAC**'s core (orange box). The latter module implements well-formedness check (green box). Solid arrows represent calls between components while dashed arrows data IO.

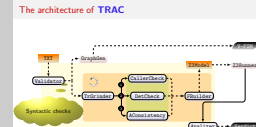
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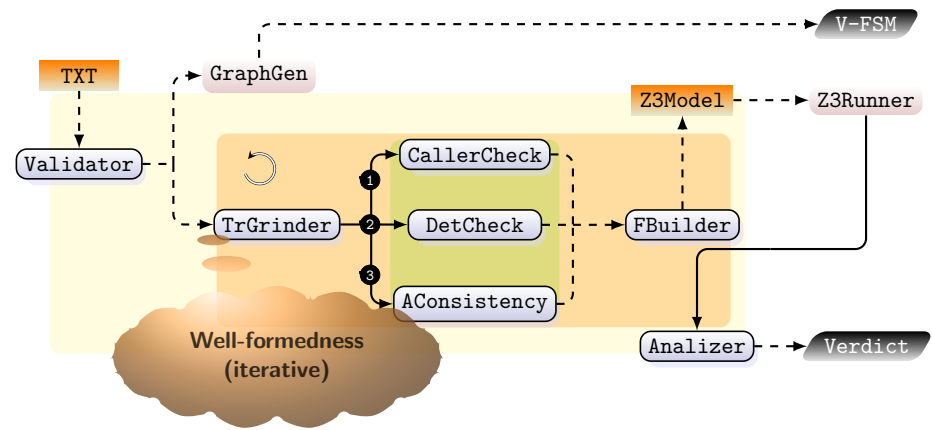
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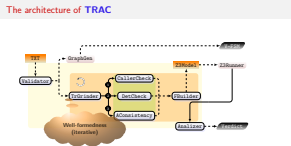
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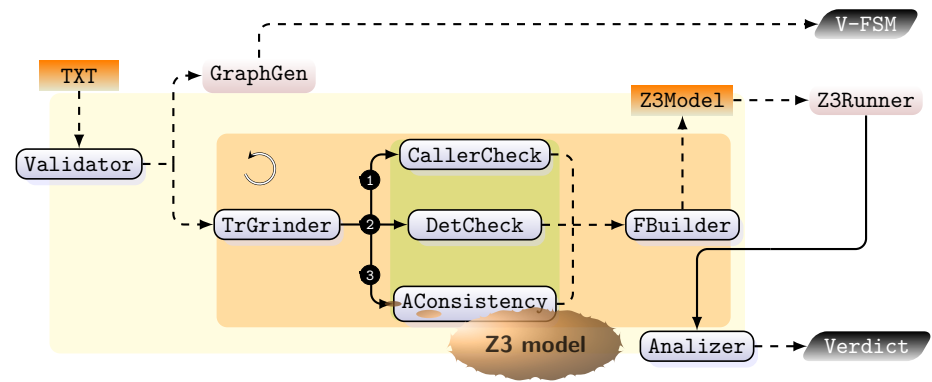
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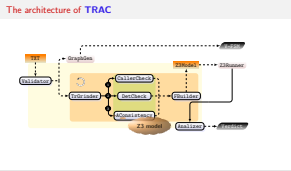


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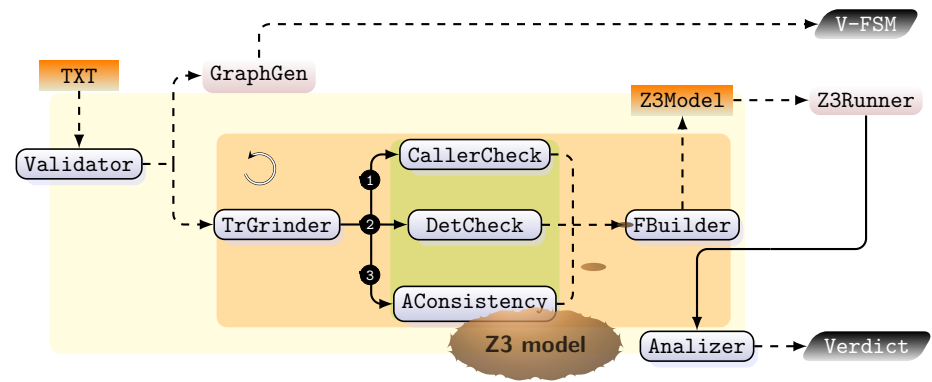
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## The architecture of TRAC

AConsistency (arrow 3) to generate a Z3 formula which holds if, and only if, the transtion is consistent.



# The architecture of TRAC



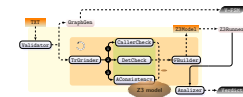
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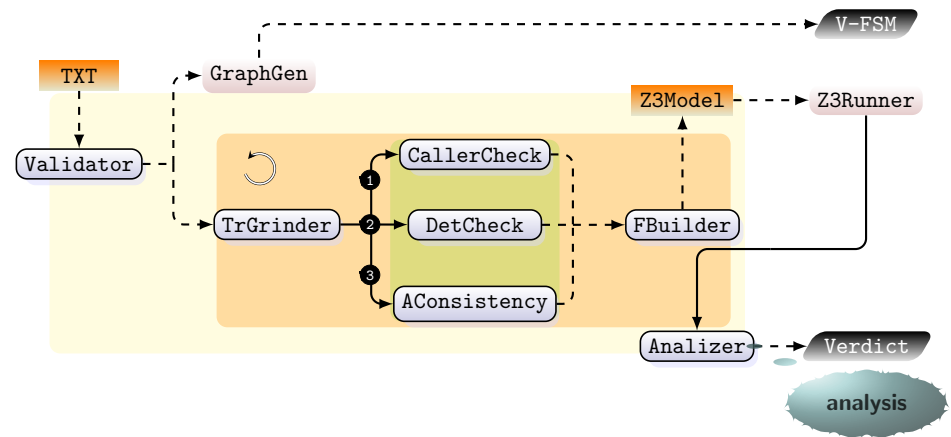
## The architecture of TRAC

computes the z3 f.la equivalent to the conjunction of the outputs which is then passed to a Z3 engine to check its satisfiability

The architecture of TRAC



# The architecture of TRAC

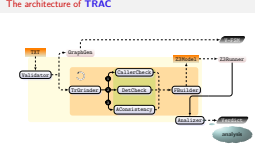


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## The architecture of TRAC

Finally, the Analyzer component that diagnoses the output of Z3 and produces a Verdict which reports (if any) the violations of well-formedness of the DAFSM in input.



# Installation

Detailed instructions at <https://github.com/loctet/TRAC>

Dependencies: Java RE (to render DAFSM graphically) & Python 3.6 or later

```
pip install z3-solver matplotlib numpy plotly pandas networkx
```

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└─ Installation

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<https://doi.org/10.5281/zenodo.10996456>

