	Study		Implementation					
Feature	Verification requirements	Owner	Verification development	Testcases	Owner	Dev. Status	Start dev	End dev
Which part is being tested:	What is end goal of the test:		What will be checked and what is the expected result:					
Counter	Counter to start counting when button is pressed , that equals to "load " in the tested model	Emilija	There is an extra input signal "click" in definition, but when load is activated we receive a transition on the positive edge of the clock	counter_activation	Emilija	passed	22.01.2021	10.2.2021
Counter	Counter to get reset	Emilija	After reseting the counter is starting from 0 as expected	counter_reset	Emilija	passed	22.01.2021	10.2.2021
Counter	Counter to send when signal is low	Emilija	After the "load" input/ signal is low, there is a incrementation on the counter	counter_stop	Emilija	unpassed	25.01.2021	10.2.2021
Counter	Counter is supposed have limited storage	Emilija	When counter is full it needs to be reseted, but it doesn't have any limitation assign to it	counter_storage	Emilija	unpassed	25.01.2021	10.2.2021
Counter	Counter to have all it signals properly set	Emilija	There is a count signal that is supposed to count how many numbers are currently in the counter , but the signal doesn't drive any logic	count_signal	Emilija	unpassed	25.01.2021	10.2.2021