

## Lab 3 Tests Screenshots

### Register File Tests

	Msgs	
+ /reg_file/data_in	FFFFFFFF	FFFFFFFF
+ /reg_file/clock	0	
+ /reg_file/data_out	00000000	00000000
+ /reg_file/readnotwrite	0	FFFFFFFF
+ /reg_file/reg_number	05	05

### DLX Register Tests

	Msgs	
+ /dlx_register/in_val	00000001	0000000A
+ /dlx_register/clock	1	
+ /dlx_register/out_val	00000001	00000000