```
# Tiempo=0 ns | UART_valid=x | write_addr=     0 | uart_data=0xxx |
LEDR=0xxxxxxxxx
# Tiempo=10 ns | UART_valid=0 | write_addr=     0 | uart_data=0x00 |
LEDR=0000000000
# ======================================
# Inicio de simulaciÃ³n
# ======================================
#
# Enviando bytes de prueba...
# Tiempo=1397650 ns | UART_valid=1 | write_addr=     0 | uart_data=0x00 |
LEDR=0100000000
# Tiempo=1397670 ns | UART_valid=0 | write_addr=     1 | uart_data=0x00 |
LEDR=1000000000
# Tiempo=1494110 ns | UART_valid=1 | write_addr=     1 | uart_data=0x01 |
LEDR=1100000001
# Tiempo=1494130 ns | UART_valid=0 | write_addr=     2 | uart_data=0x01 |
LEDR=1000000001
# Tiempo=1590590 ns | UART_valid=1 | write_addr=     2 | uart_data=0x02 |
LEDR=1100000010
# Tiempo=1590610 ns | UART_valid=0 | write_addr=     3 | uart_data=0x02 |
LEDR=1000000010
# Tiempo=1687070 ns | UART_valid=1 | write_addr=     3 | uart_data=0x03 |
LEDR=1100000011
# Tiempo=1687090 ns | UART_valid=0 | write_addr=     4 | uart_data=0x03 |
LEDR=1000000011
# Tiempo=1783550 ns | UART_valid=1 | write_addr=     4 | uart_data=0x04 |
LEDR=1100000100
# Tiempo=1783570 ns | UART_valid=0 | write_addr=     5 | uart_data=0x04 |
LEDR=1000000100
# Tiempo=1880030 ns | UART_valid=1 | write_addr=     5 | uart_data=0x05 |
LEDR=1100000101
# Tiempo=1880050 ns | UART_valid=0 | write_addr=     6 | uart_data=0x05 |
LEDR=1000000101
# Tiempo=1976510 ns | UART_valid=1 | write_addr=     6 | uart_data=0x06 |
LEDR=1100000110
# Tiempo=1976530 ns | UART_valid=0 | write_addr=     7 | uart_data=0x06 |
LEDR=1000000110
# Tiempo=2072990 ns | UART_valid=1 | write_addr=     7 | uart_data=0x07 |
LEDR=1100000111
# Tiempo=2073010 ns | UART_valid=0 | write_addr=     8 | uart_data=0x07 |
LEDR=1000000111
# Tiempo=2169470 ns | UART_valid=1 | write_addr=     8 | uart_data=0x00 |
LEDR=1100000000
# Tiempo=2169490 ns | UART_valid=0 | write_addr=     9 | uart_data=0x00 |
LEDR=1000000000
# Tiempo=2265950 ns | UART_valid=1 | write_addr=     9 | uart_data=0x01 |
LEDR=1100000001
# Tiempo=2265970 ns | UART_valid=0 | write_addr=    10 | uart_data=0x01 |
LEDR=1000000001
# Tiempo=2362430 ns | UART_valid=1 | write_addr=    10 | uart_data=0x02 |
LEDR=1100000010
```

```
# Tiempo=2362450 ns | UART_valid=0 | write_addr=    11 | uart_data=0x02 |
LEDR=1000000010
# Tiempo=2458910 ns | UART_valid=1 | write_addr=    11 | uart_data=0x03 |
LEDR=1100000011
# Tiempo=2458930 ns | UART_valid=0 | write_addr=    12 | uart_data=0x03 |
LEDR=1000000011
# Tiempo=2555390 ns | UART_valid=1 | write_addr=    12 | uart_data=0x04 |
LEDR=1100000100
# Tiempo=2555410 ns | UART_valid=0 | write_addr=    13 | uart_data=0x04 |
LEDR=1000000100
# Tiempo=2651870 ns | UART_valid=1 | write_addr=    13 | uart_data=0x05 |
LEDR=1100000101
# Tiempo=2651890 ns | UART_valid=0 | write_addr=    14 | uart_data=0x05 |
LEDR=1000000101
# Tiempo=2748350 ns | UART_valid=1 | write_addr=    14 | uart_data=0x06 |
LEDR=1100000110
# Tiempo=2748370 ns | UART_valid=0 | write_addr=    15 | uart_data=0x06 |
LEDR=1000000110
# Tiempo=2844830 ns | UART_valid=1 | write_addr=    15 | uart_data=0x07 |
LEDR=1100000111
# Tiempo=2844850 ns | UART_valid=0 | write_addr=    16 | uart_data=0x07 |
LEDR=1000000111
# Tiempo=2941310 ns | UART_valid=1 | write_addr=    16 | uart_data=0xaa |
LEDR=1110101010
# Tiempo=2941330 ns | UART_valid=0 | write_addr=    17 | uart_data=0xaa |
LEDR=1010101010
# Tiempo=3037790 ns | UART_valid=1 | write_addr=    17 | uart_data=0x55 |
LEDR=1101010101
# Tiempo=3037810 ns | UART_valid=0 | write_addr=    18 | uart_data=0x55 |
LEDR=1001010101
# Tiempo=3134270 ns | UART_valid=1 | write_addr=    18 | uart_data=0xff |
LEDR=1111111111
# Tiempo=3134290 ns | UART_valid=0 | write_addr=    19 | uart_data=0xff |
LEDR=1011111111
# Tiempo=3230750 ns | UART_valid=1 | write_addr=    19 | uart_data=0x00 |
LEDR=1100000000
# Tiempo=3230770 ns | UART_valid=0 | write_addr=    20 | uart_data=0x00 |
LEDR=1000000000
#
# ========================================
# Bytes enviados correctamente
# ========================================
#
# Valor final de write_addr:     20
# Ã□ltimo byte en LEDs: 0x00
#
# ========================================
# SimulaciÃ³n completada
# ========================================
# ** Note: $finish    : C:/Sistema embebido proyecto
final/sistema_embedido/fpga_top_tb.v(155)
#    Time: 3250330 ns  Iteration: 0  Instance: /fpga_top_tb
```