INPUTS				OUTPUTS		
Signal	**FPGA Pin No.**	**FPGA Signal**		**Signal**	**FPGA Pin No.**	**FPGA Signal**
clock	PIN_Y2	CLOCK_50		aIsNeg	PIN_H15	LEDR[17]
				bIsNeg	PIN_F15	LEDR[14]
A[0]	PIN_AD26	SW[6]		zIsNeg	PIN_J17	LEDR[8]
A[1]	PIN_AB26	SW[7]				
A[2]	PIN_AC25	SW[8]		inst_out[2]	PIN_E19	LEDR[2]
A[3]	PIN_AB25	SW[9]		inst_out[1]	PIN_F19	LEDR[1]
A[4]	PIN_AC24	SW[10]		inst_out[0]	PIN_G19	LEDR[0]
A[5]	PIN_AB24	SW[11]				
				fifo_full	PIN_E21	LEDG[0]
B[0]	PIN_AB28	SW[0]		fifo_empty	PIN_E22	LEDG[1]
B[1]	PIN_AC28	SW[1]				
B[2]	PIN_AC27	SW[2]		zTens[6]	PIN_U24	HEX1[6]
B[3]	PIN_AD27	SW[3]		zTens[5]	PIN_U23	HEX1[5]
B[4]	PIN_AB27	SW[4]		zTens[4]	PIN_W25	HEX1[4]
B[5]	PIN_AC26	SW[5]		zTens[3]	PIN_W22	HEX1[3]
				zTens[2]	PIN_W21	HEX1[2]
inst[0]	PIN_AB23	SW[12]		zTens[1]	PIN_Y22	HEX1[1]
inst[1]	PIN_AA24	SW[13]		zTens[0]	PIN_M24	HEX1[0]
inst[2]	PIN_AA23	SW[14]				
				zOnes[6]	PIN_H22	HEX0[6]
rstSw	PIN_Y23	SW[17]		zOnes[5]	PIN_J22	HEX0[5]
				zOnes[4]	PIN_L25	HEX0[4]
button	PIN_R24	KEY[3]		zOnes[3]	PIN_L26	HEX0[3]
rd_wr_sw	PIN_Y24	SW[16]		zOnes[2]	PIN_E17	HEX0[2]
				zOnes[1]	PIN_F22	HEX0[1]
				zOnes[0]	PIN_G18	HEX0[0]
				aTens[6]	PIN_AA14	HEX7[6]
				aTens[5]	PIN_AG18	HEX7[5]
				aTens[4]	PIN_AF17	HEX7[4]
				aTens[3]	PIN_AH17	HEX7[3]
				aTens[2]	PIN_AG17	HEX7[2]
				aTens[1]	PIN_AE17	HEX7[1]
				aTens[0]	PIN_AD17	HEX7[0]
				aOnes[6]	PIN_AC17	HEX6[6]
				aOnes[5]	PIN_AA15	HEX6[5]
				aOnes[4]	PIN_AB15	HEX6[4]
				aOnes[3]	PIN_AB17	HEX6[3]
				aOnes[2]	PIN_AA16	HEX6[2]
				aOnes[1]	PIN_AB16	HEX6[1]
				aOnes[0]	PIN_AA17	HEX6[0]

bTens[6]	PIN_AH18	HEX5[6]
bTens[5]	PIN_AF18	HEX5[5]
bTens[4]	PIN_AG19	HEX5[4]
bTens[3]	PIN_AH19	HEX5[3]
bTens[2]	PIN_AB18	HEX5[2]
bTens[1]	PIN_AC18	HEX5[1]
bTens[0]	PIN_AD18	HEX5[0]
bOnes[6]	PIN_AE18	HEX4[6]
bOnes[5]	PIN_AF19	HEX4[5]
bOnes[4]	PIN_AE19	HEX4[4]
bOnes[3]	PIN_AH21	HEX4[3]
bOnes[2]	PIN_AG21	HEX4[2]
bOnes[1]	PIN_AA19	HEX4[1]
bOnes[0]	PIN_AB19	HEX4[0]