

- a. Atomicity + Consistency
- b. The system experienced a violation because it failed to implement an atomic transaction. It ended up booking in multiple steps. The driver acceptance and wallet deduction were written to the database, however, before the final ride confirmation was completed the server crashed leaving only the transaction partially executed. This violates Atomicity as the process did not adhere to an "all or none" rule, and it infringes on consistency because funds were withdrawn without a valid trip being established.
- c. The system ought to initially secure the required information via appropriate locking mechanisms. Within the transaction block, log the driver's agreement, subtract from the account, and then finalize the reservation. These modifications remain hidden until the transaction is committed. Should the server fail prior to commitment, everything is reversed, preventing any incomplete alterations. Upon successful commitment, durability ensures the changes persist post-failure.
- d. Such problems can undermine user confidence, decrease reservations and lead to greater monetary setbacks from reimbursements and disputes. They also escalate customer service expenses, harm the brand's image, and could result in oversight or judicial repercussions.

Q2.

$$P_1 = 100, \text{ sold} = 3, \text{returned} = 2$$

Non serial:

1. R1 ( $\theta = 100$ )      3. W1 ( $\theta = 97$ )

2. R2 ( $\theta = 100$ )      4. W2 ( $\theta = 102$ )

b.

$$R1(\theta) \rightarrow W2(\theta) : T_1 \rightarrow T_2$$

$$R2(\theta) \rightarrow W1(\theta) : T_2 \rightarrow T_1$$

$$W1(\theta) \rightarrow W2(\theta) : T_1 \rightarrow T_2$$

c. The graph has a loop (cycle). Plan is not conflict serializable.

d.  $T_1 \rightarrow T_2$  (serial Execution)

$$R1(100) \rightarrow W1(97)$$

$$R2(97) \rightarrow W2(99) \quad \text{Final Quantity} = 99.$$

$T_2 \rightarrow T_1$  (serial Execution)

$$R2(100) \rightarrow W2(102)$$

$$R1(102) \rightarrow W1(99) \quad \text{Final Quantity} = 99$$

e. These irregularities may result in wrong inventory counts, causing excess sales, order cancellations, unhappy customers, higher reimbursements and lost income. In the long run, this erodes the business's standing and user loyalty.

Q3

Schedule 1 :

i.  $r_4(a)$  before  $w_1(a)$  :  $T_4 \rightarrow T_1$

$r_1(b)$  before  $w_2(b)$  :  $T_1 \rightarrow T_2$

$r_2(c)$  before  $w_3(c)$  :  $T_2 \rightarrow T_3$

$r_3(d)$  before  $w_4(d)$  :  $T_3 \rightarrow T_4$

$T_4 \rightarrow T_1 \rightarrow T_2 \rightarrow T_3 \rightarrow T_4$

Therefore cycle exists.

2.  $S_1 \rightarrow$  Not conflict serializable

3. None Exists

Schedule 2 :

i.  $w_4(d)$  before  $r_3(d)$   $T_4 \rightarrow T_3$

$w_3(c)$  before  $r_2(c)$   $T_3 \rightarrow T_2$

$w_2(b)$  before  $r_1(b)$   $T_2 \rightarrow T_1$

$r_4(a)$  before  $w_1(a)$   $T_4 \rightarrow T_1$

2. Yes.  $S_2$  is conflict serializable

3.  $T_4 \rightarrow T_3 \rightarrow T_2 \rightarrow T_1$

Q4

Schedule experiences a lost update problem. Both transactions ( $T_1, T_2$ ) read the same initial value (2000) before either one writes back its update.

$$T_1 : 2000 - 300 = 1700$$

$$T_2 : 2000 - 800 = 2800$$

$T_1$  is overwritten by  $T_2$  because there was no synchronization or locking to prevent both transactions from modifying the same data.

z. 2800 (Rs)

3. case 1:

$$T_1 \rightarrow T_2$$

$$\text{After } T_1 = 2000 - 300 = 1700$$

$$\text{After } T_2 = 1700 + 800 = 2500$$

case 2 :

$$T_2 \rightarrow T_1$$

$$\text{Start } 2000$$

$$\text{After } T_2 = 2000 + 800 = 2800$$

$$\text{After } T_1 = 2800 - 300 = 2500$$

Q5

Conflicts and Edges :

$w_1(n) \rightarrow r_3(x) : T_1 \rightarrow T_3$

$w_1(n) \rightarrow r_2(n) : T_1 \rightarrow T_2$

$r_2(n) \rightarrow w_3(n) : T_2 \rightarrow T_3$

There is no cycle , conflict serializable

Equivalent serial order:  $T_1 \rightarrow T_2 \rightarrow T_3$

b. Conflicts and Edges

Precedence Edges

$r_1(n) \rightarrow w_3(n) : T_1 \rightarrow T_3$

$T_1 \rightarrow T_3$

$r_3(n) \rightarrow w_1(n) : T_3 \rightarrow T_1$

$T_3 \rightarrow T_1$

$w_3(n) \rightarrow w_1(n) : T_3 \rightarrow T_1$

cycle exists , Not conflict serializable .

c. Conflicts and Edges

Precedence

$r_2(n) \rightarrow w_3(n) : T_2 \rightarrow T_3$

$T_2 \rightarrow T_3$

$r_2(n) \rightarrow w_1(n) : T_2 \rightarrow T_1$

$T_2 \rightarrow T_1$

$w_3(n) \rightarrow r_1(n) : T_3 \rightarrow T_1$

$T_3 \rightarrow T_1$

No cycle , conflict serializable

Equivalent serial Order :  $T_2 \rightarrow T_3 \rightarrow T_1$

d. Conflict and Edges .

Precedence Edges

$r_2(n) \rightarrow w_1(n)$        $T_3 \rightarrow T_1$

$T_3 \rightarrow T_2$

$r_2(n) \rightarrow w_3(n)$        $T_2 \rightarrow T_3$

$T_1 \rightarrow T_3$

$r_2(n) \rightarrow w_1(n)$        $T_2 \rightarrow T_1$

$T_2 \rightarrow T_3$

$r_1(n) \rightarrow w_3(n)$        $T_1 \rightarrow T_3$

$T_2 \rightarrow T_1$

Cycle Exists Non conflict Serializable .