

1 Instruction Set Reference List

		31	25	21	17	13	0
Formats	r-type:	opcode	rd	rs1	rs2	0	
	i-type:	opcode	rd	rs	18-bit immediate		
	l-type:	opcode	rd	22-bit immediate			

Instructions	NOP	000000	0			
	SET	000001	rd	rs	18-bit immediate	
	LOAD	000010	rd	22-bit immediate		
	MOV	000011	rd	rs	0	
	FADD	000011	rd	rs1	rs2	0
	FSUB	000100	rd	rs1	rs2	0
	NEG	000101	rd	rs1	0	0

NOP No operation

Opcode: 000000
 Syntax: NOP
 Purpose: Perform no operations.

SET Set register to floating-point value

Opcode: 000001
 Syntax r-type: SET, rd, #<32-bit FP value>
 Purpose: Assign a 32-bit floating point value to rd.
 Operation: $rd \leftarrow \text{FPvalue}$

LOAD Load value from memory

Opcode: 000010
 Syntax r-type: LOAD, rd, rs
 Purpose: Assign rd the value from the memory address in rs.
 Operation: $rd \leftarrow M[rs]$

STORE Store value to memory

Opcode: 000011
 Syntax r-type: STORE, rd, rs
 Purpose: Assign memory location specified in rd to value in rs.
 Operation: $M[rd] \leftarrow rs$

MOVE Copy value from a register to another

Opcode: 000100
 Syntax r-type: MOVE, rd, rs
 Purpose: Assign rd the value in rs.
 Operation: $rd \leftarrow rs$

FADD

Add 32-bit floating-point value

Opcode:	000101
Syntax r-type:	FADD, rd, rs1, rs2
Syntax i-type:	FADD, rd, rs1, <32-bit ? immediate>
Purpose:	Performs addition on two 32-bit floating-point values from rs1 and rs2, or an immediate in place of rs2, and stores the result in rd.
Operation:	$rd \leftarrow rs1 + rs2$ or $rd \leftarrow rs1 + \text{immediate}$
Condition Codes:	$\frac{N \ Z \ V}{x \ - \ x}$

FSUB

Subtract 32-bit floating-point values

Opcode:	000110
Syntax r-type:	FSUB, rd, rs1, rs2
Syntax i-type:	FSUB, rd, rs1, <32-bit ? immediate>
Purpose:	Performs subtraction on two 32-bit floating-point values from rs1 and rs2, or an immediate in place of rs2, and stores the result in rd.
Operation:	$rd \leftarrow rs1 - rs2$ or $rd \leftarrow rs1 - \text{immediate}$
Condition Codes:	$\frac{N \ Z \ V}{x \ - \ x}$

FNEG

Negate a 32-bit floating-point value

Opcode:	000111
Syntax:	FNEG, rd, rs
Purpose:	Performs negation on a 32-bit floating-point value from rs and stores the result in rd.
Operation:	$rd \leftarrow -rs$
Condition Codes:	$\frac{N \ Z \ V}{x \ - \ x}$

FMUL

Multiply two 32-bit floating-point values

Opcode:	001000
Syntax r-type:	FMUL, rd, rs1, rs2
Syntax i-type:	FMUL, rd, rs1, <32-bit ? immediate>
Purpose:	Performs multiplication on two 32-bit floating-point values from rs1 and rs2, or an immediate in place of rs2, and stores the result in rd.
Operation:	$rd \leftarrow rs1 * rs2$ or $rd \leftarrow rs1 * \text{immediate}$
Condition Codes:	$\frac{N \ Z \ V}{x \ - \ x}$

FDIV

Divide two 32-bit floating-point values

Opcode:	001001
Syntax r-type:	FDIV, rd, rs1, rs2
Syntax i-type:	FDIV, rd, rs1, <32-bit ? immediate>
Purpose:	Performs division on two 32-bit floating-point values from rs1 and rs2, or an immediate in place of rs2, and stores the result in rd.
Operation:	$rd \leftarrow rs1 \div rs2$ or $rd \leftarrow rs1 \div \text{immediate}$
Condition Codes:	$\frac{N \ Z \ V}{x \ - \ x}$

FLOOR

Compute the floor function

Opcode:	001010
Syntax r-type:	FLOOR, rd, rs
Purpose:	Rounds the value in rs to the nearest lowest integer and stores the result in rd.
Operation:	$rd \leftarrow \lfloor rs \rfloor$
Condition Codes:	$\frac{N \ Z \ V}{x \ - \ x}$