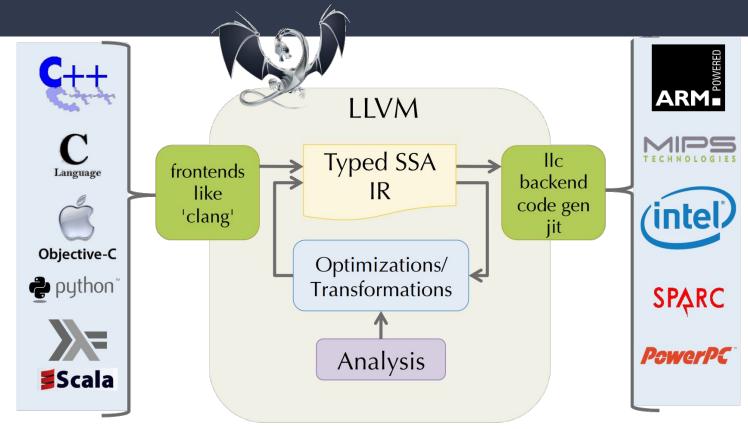
Fuzzing LLVM for Miscompiles

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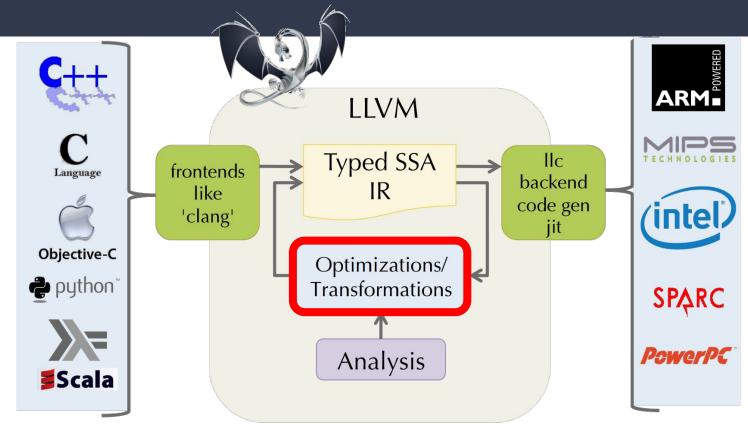


LLVM in One Slide



Zdancewic

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The Problem

When our compiler optimizes code, how do we know that generated machine code or executable is correct?

In short, we are taught to trust that the compiler is correct.

Usually, if there's a bug, it's the programmer's fault, not the compiler's.

But this isn't always the case...

There are two types of compiler bugs...

Compiler Crashes

When you try to compile your code, the compiler crashes and no output is produced.

Fairly common.

Easy to find with fuzzing.

Compiler Miscompiles

When you try to compile your code, incorrect output is generated and no warnings/errors are given.

Not as common and very dangerous. These are the bugs we want to find.

So how do we use fuzzing to find miscompiles?

- 1. Compile two versions of the code: optimized and not
- 2. Feed each version the same input and run them
- 3. Check if the output is the same
- 4. Repeat

If the output differs, we've found a miscompile!

Fuzzing the LLVM Loop Vectorizer

 Easy to trigger; you just need a loop

Why the Loop Vectorizer?

Fairly complicated optimization

Easy to test

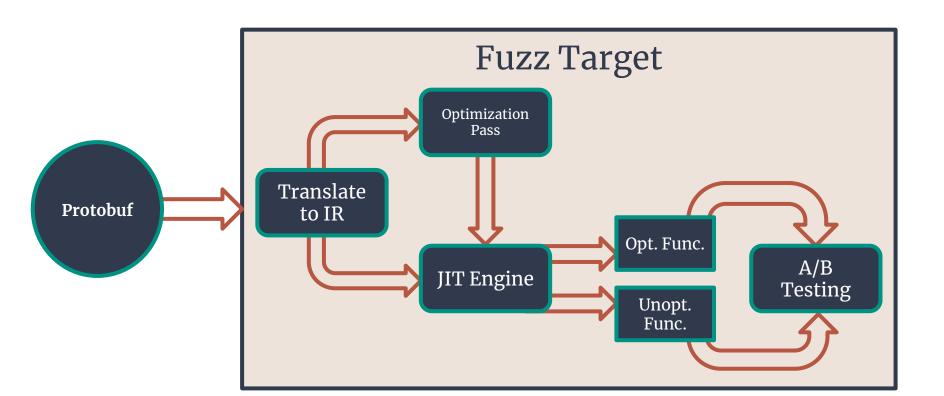
Existing Technology

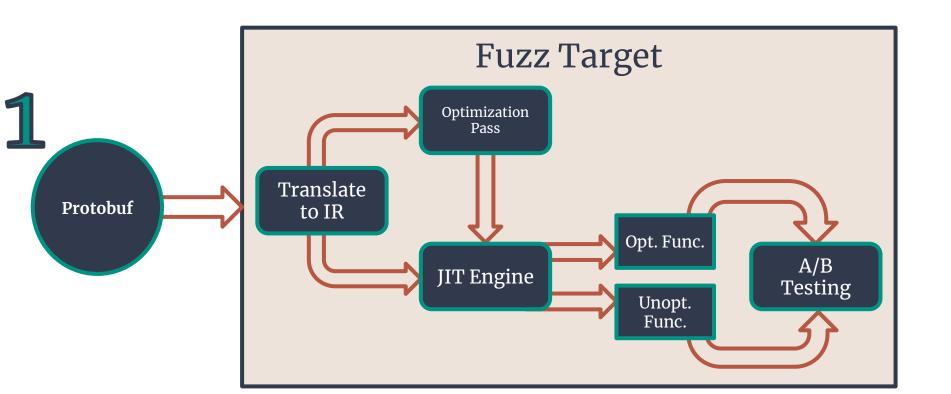
 Define a subset of C++ as a protobuf

Bypass lexing/parsing stage

Only finds compiler crashes, not miscompiles

Overview of the LLVM Proto Fuzzer





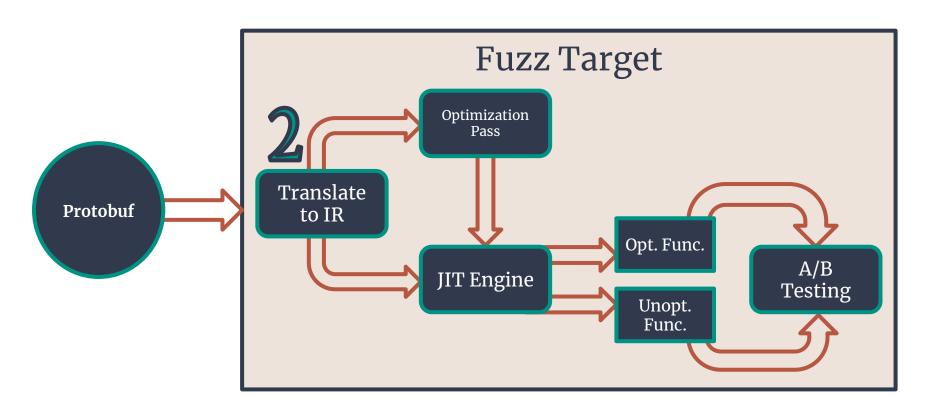
First Step: A New Protobuf

cxx-loop-proto

```
message Const {
                            message BinaryOp {
                                                           message Rvalue {
  required int32 val = 1;
                              enum Op {
                                                             oneof rvalue oneof {
                                PLUS = 0;
                                                               Const cons = 1;
                                MINUS = 1;
                                                               BinaryOp binop = 2;
message VarRef {
                                MUL = 2;
                                                               VarRef varref = 3;
 // Add an enum for each
                                XOR = 3:
array in function signature
                                AND = 4:
 enum Arr {
                                OR = 5;
   ARR A = 0;
                                EQ = 6;
                                                           message AssnStmt {
   ARR B = 1;
                                NE = 7:
                                                             required VarRef varref = 1;
   ARR C = 2;
                                LE = 8:
                                                             required Rvalue rvalue = 2;
                                GE = 9;
 required Arr arr = 1;
                                LT = 10;
                                GT = 11;
                                                           message Statement {
                                                             required AssnStmt assignment = 1;
                              required Op op = 1;
                              required Rvalue left = 2;
                              required Rvalue right = 3;
                                                           message StatementSeg {
                                                             repeated Statement statements = 1;
                                                           message LoopFunction {
                                                             required StatementSeq statements = 1;
```

 Create a protobuf to represent a subset of C++ (and LLVM IR) that is loop "friendly"

 Want generated code to be more likely to stress the loop vectorizer



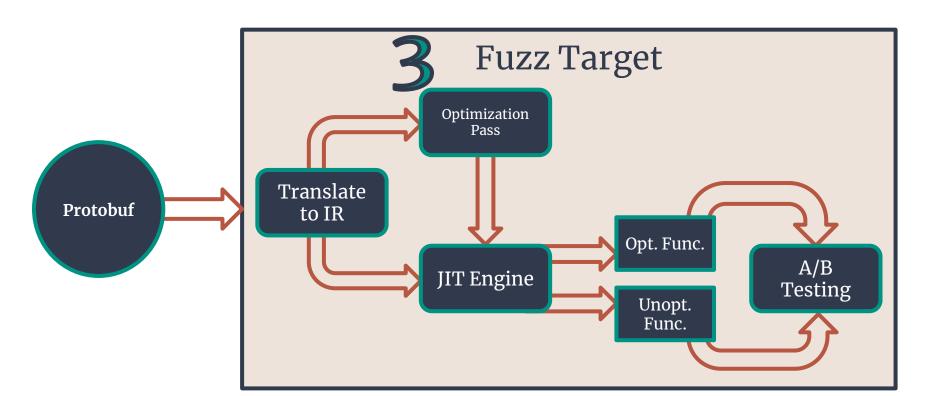
Second Step: Produce Valid LLVM IR

loop-proto-to-llvm

```
target triple = "x86 64-unknown-linux-gnu"
define void @foo(i32* %a, i32* %b, i32* %c, i64 %s) {
%1 = icmp sqt i64 %s, 0
br i1 %1, label %start, label %end
start:
br label %loop
end:
ret void
loop:
      = phi i64 [ %ctnew, %loop ], [ 0, %start ]
%var0 = getelementptr inbounds i32, i32* %b, i64 %ct
store i32 1, i32* %var0
%var1 = getelementptr inbounds i32, i32* %a, i64 %ct
store i32 1, i32* %var1
%var2 = getelementptr inbounds i32, i32* %b, i64 %ct
%var3 = load i32, i32* %var2
%var4 = getelementptr inbounds i32, i32* %b, i64 %ct
store i32 %var3, i32* %var4
%ctnew = add i64 %ct, 1
%j = icmp eq i64 %ctnew, %s
br i1 %j, label %end, label %loop, !llvm.loop !0
!0 = distinct !{!0, !1, !2}
!1 = !{!"llvm.loop.vectorize.enable", i1 true}
!2 = !{!"llvm.loop.vectorize.width", i32 64}
```

 Along the same lines of proto-to-cxx, generate valid LLVM IR function from a protobuf

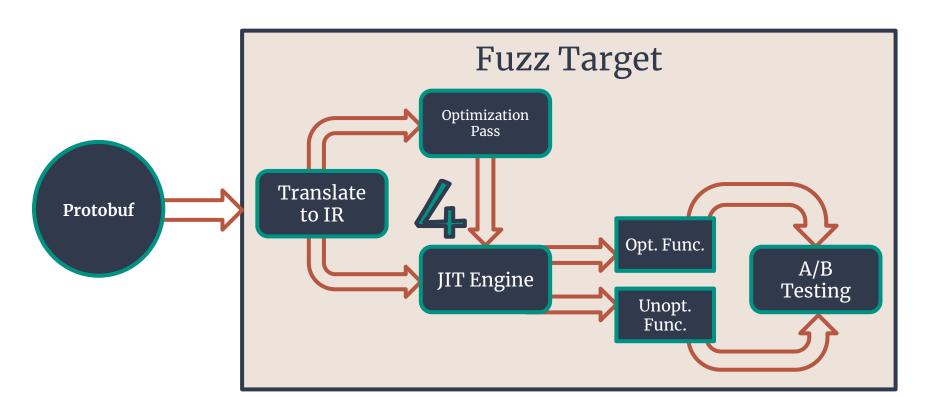
 Convert protobuf to LLVM IR before passing it to fuzz target



Third Step: Run the Optimization Pass

 Before compiling the IR code, run a loop vectorize pass over one of the versions

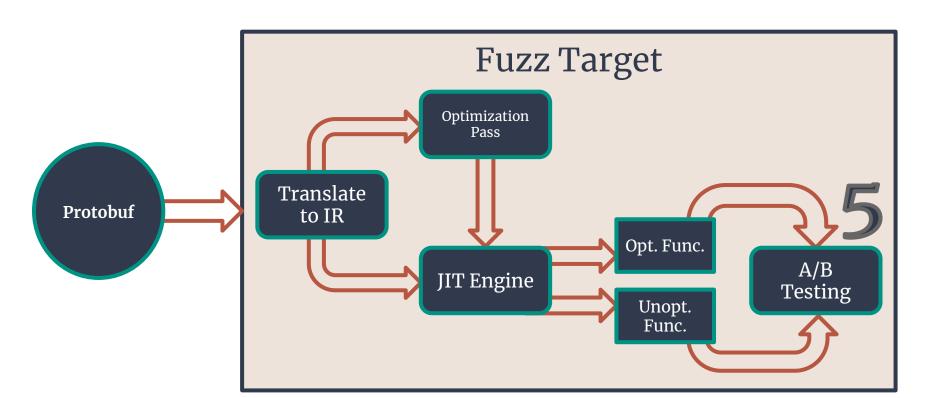
 We have two versions of the IR code: optimized and unoptimized



Fourth Step: Compile the IR Code

 Use LLVM's JIT engine to compile both versions of the IR

Obtain a function
 pointer to each of the
 compiled functions



Fifth Step: Run the Functions

 Build a suite of inputs that both functions will be executed on

 Run each function on the same input and check that they have the same output

Making Sure it Works: Introducing a Bug

We want to make sure the fuzzer is achieving good coverage over the loop vectorizer code.

So, we introduce a bug into the vectorizer and use our fuzzer to find it.

What's Next?

- Find some bugs!!!!
- Extend the protobuf
 - More loops
 - Nested loops
 - Conditionals
- Add more optimization passes

Thanks to...

- Kostya
- Matt
- And the rest of the Dynamic Tools team