	OPCODE																				
	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
HALT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
ADD	0	0	0	1	DST				SRC1 0					0	0	0	SRC2				
SUB	0	0	1	0	DST				SRC1 0				0	0	0	0	SRC2				
AND	0	0	1	1	DST				SRC1 0				0	0	0	0	SRC2				
OR	0	1	0	0	DST				SRC1 0				0	0	0	0	SRC2				
XOR	0	1	0	1	DST				SRC1 0				0	0	0	0	SRC2				
ADDI	0	0	0	1	DST				SRC1 1				IMM								
SUBI	0	0	1	0	DST				SRC1 1				IMM								
ANDI	0	0	1	1	DST				SRC1 1				IMM								
ORI	0	1	0	0	DST				SRC1 1				IMM								
XORI	0	1	0	1	DST					S	RC1		1	IMM							
LD	0	1	1	0	DST				ADDR												
ST	0	1	1	1		SF	RC		ADDR												
JUMP	1	0	0	0					ADDR												
PUSH	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	SRC1				
POP	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	SRC1				
BE	1	0	1	1		RE	G1		REG2					ADDR							
BNE	1	1	0	0	DST				REG2					ADDR							
	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
RESERVED	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	