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in LinkedIn

COVER LETTER

I have collected many experiences which can only be learned the hard way.

EDUCATION

Gazi University Bachelor of Engineering / Electrical and Electronics Engineering 09/2015 - 09/2020

 Graduation Project: RF Energy Harvesting Circuit Design With Cockroft-Walton Voltage Multiplier

Hacettepe University Master of Science / Engineering Management **WORK EXPERIENCE**

02/2025 - Now

> ASELSAN A.Ş.

o Electro-Optical System Design Engineer

10/2024 - Now

- ✓ Image quality analysis, dedectors, electronics, control systems, optics
- ✓ Amelation

o Hardware Design Engineer

10/2020 - 10/2024

- ✓ Schematic & PCB layout design with FPGAs, SoCs and Microprocessors for high-end military systems
- ✓ Extensive knowledge & design experience with Aiglex 7, ArriaX SoC & FPGA, CycloneV, i.MX 8M Plus, Atom x6425E
- ✓ Design experience & knowledge on high-speed serial interfaces (PCIe, USB 3.0, SFP+, GbE, SATA, SRIO etc.), external memory interfaces (DDR3, LPDDR4, SSD, eMMC etc.), digital video interfaces (SDI, CameraLink, HDMI, DP etc.) and other serial communication interfaces (I2C, SPI, UART etc.)
- ✓ Pre-layout signal integrity analysis with HyperLynx SI
- ✓ Custom board verification and bring up
- ✓ Amelation

o Candidate Engineer

12/2019 - 09/2020

✓ Schematic design of a common image processing board consisting Cyclone V FPGA with PADS Professional

o Intern 07/2019 - 08/2019

- ✓ Schematic & layout design of a 2 layer PCB with MSP430 microcontroller using Altium Designer,
- ✓ Implement simple C codes in MSP430 with Code Composer Studio.

> TRT Studios and Program Transmission Systems Department

08/2018 - 09/2018

o Intern

✓ Maintenance of the servers

PATENTS

English

TR 2022 005680 – Elektro-Optik Sensör Birimleri İçin Modüler Akıllı Elektronik Kart Mimarisi

LANGUAGE SKILLS
Turkish Native

Advanced