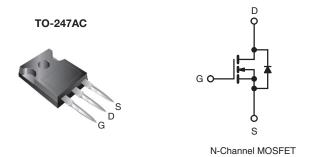


Power MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	20	200		
R _{DS(on)} (Ω)	V _{GS} = 10 V	V _{GS} = 10 V 0.055		
Q _g (Max.) (nC)	23	230		
Q _{gs} (nC)	4	42		
Q _{gd} (nC)	1-	110		
Configuration	Sin	Single		



FEATURES

- Dynamic dV/dt Rating
- Repetitive Avalanche Rated
- Isolated Central Mounting Hole
- Fast Switching
- Ease of Paralleling
- Simple Drive Requirements
- Compliant to RoHS Directive 2002/95/EC



DESCRIPTION

Third generation Power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-247AC package preferred commercial-industrial applications where higher power levels preclude the use of TO-220AB devices. The TO-247AC is similar but superior to the earlier TO-218 package because of its isolated mouting hole. It also provides greater creepage distance between pins to meet the requirements of most safety specifications.

ORDERING INFORMATION		
Package	TO-247AC	
Lead (Pb)-free	IRFP260PbF	
Lead (FD)-life	SiHFP260-E3	
SnPb	IRFP260	
JIII D	SiHFP260	

PARAMETER			SYMBOL	LIMIT	UNIT
Drain-Source Voltage			V_{DS}	200	.,
Gate-Source Voltage			V_{GS}	± 20	V
Continuous Dunin Comment	\/ -+ 10\/	T _C = 25 °C		46	
Continuous Drain Current	V _{GS} at 10 V	T _C = 100 °C	I _D	29	Α
Pulsed Drain Current ^a			I _{DM}	180	
Linear Derating Factor				2.2	W/°C
Single Pulse Avalanche Energy ^b			E _{AS}	1000	mJ
Repetitive Avalanche Currenta			I _{AR}	46	А
Repetitive Avalanche Energy ^a			E _{AR}	28	mJ
Maximum Power Dissipation $T_C = 25 ^{\circ}\text{C}$			P_{D}	280	W
Peak Diode Recovery dV/dt ^c			dV/dt	5.0	V/ns
Operating Junction and Storage Temperature Range			T _J , T _{stg}	- 55 to + 150	°C
Soldering Recommendations (Peak Temperature) for 10 s				300 ^d	
Marathar Tarra			10	lbf ⋅ in	
Mounting Torque	6-32 or M3 screw			1.1	N⋅m

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11). b. $V_{DD}=50~V$, starting $T_J=25~^{\circ}C$, $L=708~\mu H$, $R_g=25~\Omega$, $I_{AS}=46~A$ (see fig. 12). c. $I_{SD}\leq 46~A$, dl/dt $\leq 230~A/\mu s$, $V_{DD}\leq V_{DS}$, $T_J\leq 150~^{\circ}C$. d. 1.6 mm from case.

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply



THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum Junction-to-Ambient	R _{thJA}	-	40	
Case-to-Sink, Flat, Greased Surface	R _{thCS}	0.24	-	°C/W
Maximum Junction-to-Case (Drain)	R _{thJC}	-	0.45	

PARAMETER	SYMBOL	TEST	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static					•	•	
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0$	V, I _D = 250 μA	200	-	-	V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference t	o 25 °C, I _D = 1 mA	-	0.24	-	V/°C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V$	_{GS} , I _D = 250 μA	2.0	-	4.0	V
Gate-Source Leakage	I _{GSS}	V _G	_S = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	l	V _{DS} = 20	00 V, V _{GS} = 0 V	-	-	25	
Zero Gate voltage Drain Current	I _{DSS}	V _{DS} = 160 V, V	_{'GS} = 0 V, T _J = 125 °C	-	-	250	μA
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 28 A ^b	-	-	0.055	Ω
Forward Transconductance	9 _{fs}	V _{DS} = 5	0 V, I _D = 28 A ^b	24	-	-	S
Dynamic							
Input Capacitance	C _{iss}	V	_{GS} = 0 V,	-	5200	-	
Output Capacitance	C _{oss}	V	$_{0S} = 25 \text{ V},$	-	1200	-	pF
Reverse Transfer Capacitance	C _{rss}	f = 1.0 MHz, see fig. 5		-	310	-	
Total Gate Charge	Q_g	V _{GS} = 10 V		-	-	230	
Gate-Source Charge	Q_{gs}			-	-	42	nC
Gate-Drain Charge	Q_{gd}			-	-	110	
Turn-On Delay Time	t _{d(on)}			-	23	-	
Rise Time	t _r	V _{DD} = 1	00 V, I _D = 46 A,	-	120	-	ns
Turn-Off Delay Time	t _{d(off)}		$\Omega_0 = 2.1 \ \Omega$, see fig. 10^{b}	-	100	-	
Fall Time	t _f			-	94	-	1
Internal Drain Inductance	L_{D}	Between lead, 6 mm (0.25") fro	m	-	5.0	-	-11
Internal Source Inductance	L _S	package and ce die contact	nter of	_	13	-	- nH
Drain-Source Body Diode Characteristic	s				•	•	
Continuous Source-Drain Diode Current	Is	MOSFET symbol showing the	MOSFET symbol showing the		-	46	A
Pulsed Diode Forward Current ^a	I _{SM}	integral reverse p - n junction diode		-	-	180	^
Body Diode Voltage	V_{SD}	$T_J = 25$ °C, Is	$_{S} = 46 \text{ A}, V_{GS} = 0 \text{ V}^{b}$	-	-	1.8	V
Body Diode Reverse Recovery Time	t _{rr}	T 25 °C l -	46 A dl/dt = 100 A/vah	-	390	590	ns
Body Diode Reverse Recovery Charge	Q _{rr}	$T_J = 25 ^{\circ}\text{C}, I_F = 46 \text{A}, dI/dt = 100 \text{A/}\mu\text{s}^b$		-	4.8	7.2	μC
Forward Turn-On Time	t _{on}	Intrinsic turn	on time is negligible (turn	-on is do	minated b	ov I s and	T D)

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. Pulse width \leq 300 μ s; duty cycle \leq 2 %.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

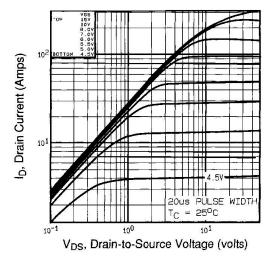
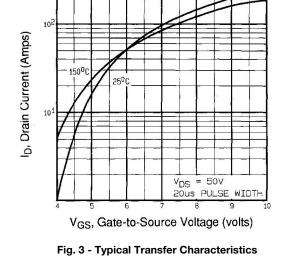


Fig. 1 - Typical Output Characteristics, T_C = 25 °C



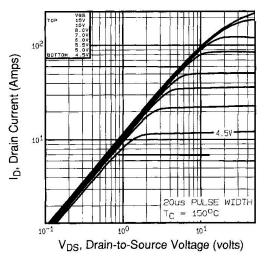


Fig. 2 - Typical Output Characteristics, T_C = 150 °C

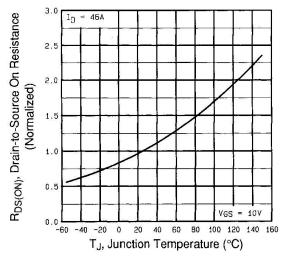


Fig. 4 - Normalized On-Resistance vs. Temperature



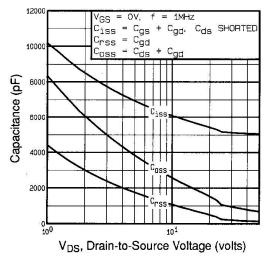


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

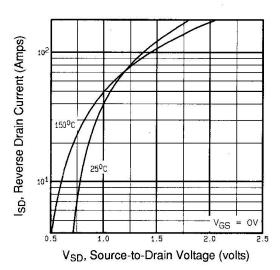


Fig. 7 - Typical Source-Drain Diode Forward Voltage

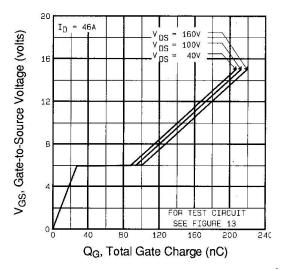


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

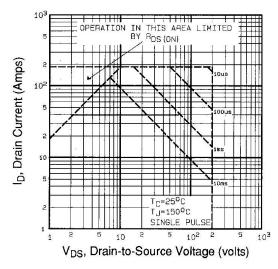


Fig. 8 - Maximum Safe Operating Area





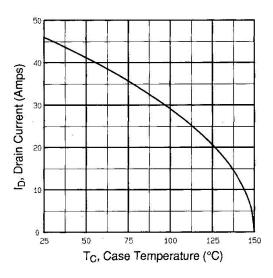


Fig. 9 - Maximum Drain Current vs. Case Temperature

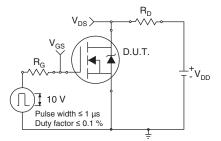


Fig. 10a - Switching Time Test Circuit

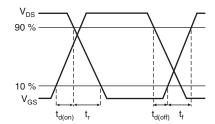


Fig. 10b - Switching Time Waveforms

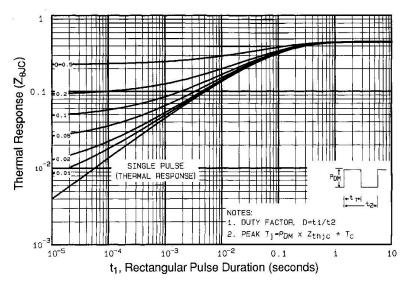
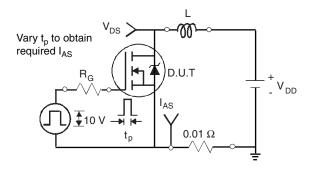


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case





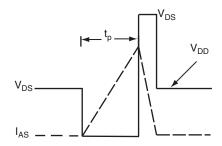


Fig. 12a - Unclamped Inductive Test Circuit

Fig. 12b - Unclamped Inductive Waveforms

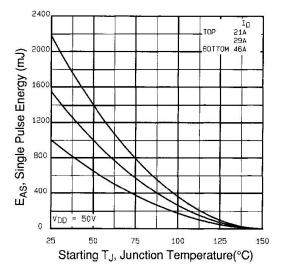


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

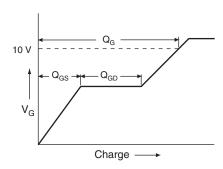


Fig. 13a - Basic Gate Charge Waveform

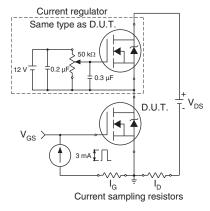
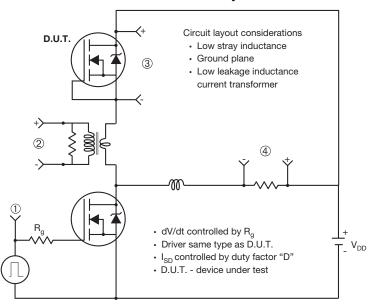


Fig. 13b - Gate Charge Test Circuit



Peak Diode Recovery dV/dt Test Circuit



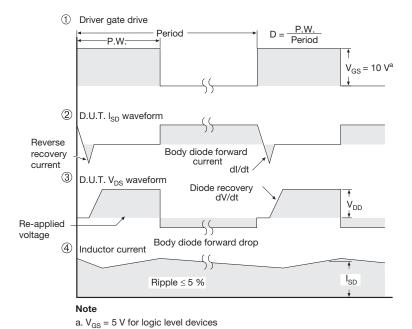


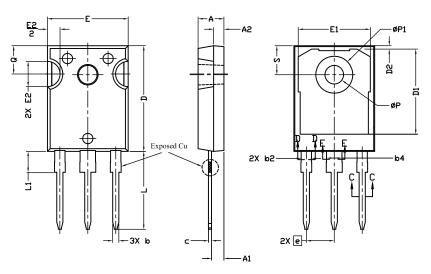
Fig. 14 - For N-Channel

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?91215.

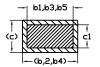


TO-247AC (High Voltage)

VERSION 1: FACILITY CODE = 9







Section C--C, D--D, E--E

	MILLIN		
DIM.	MIN.	MAX.	NOTES
Α	4.83	5.21	
A1	2.29	2.55	
A2	1.50	2.49	
b	1.12	1.33	
b1	1.12	1.28	
b2	1.91	2.39	6
b3	1.91	2.34	
b4	2.87	3.22	6, 8
b5	2.87	3.18	
С	0.55	0.69	6
c1	0.55	0.65	
D	20.40	20.70	4

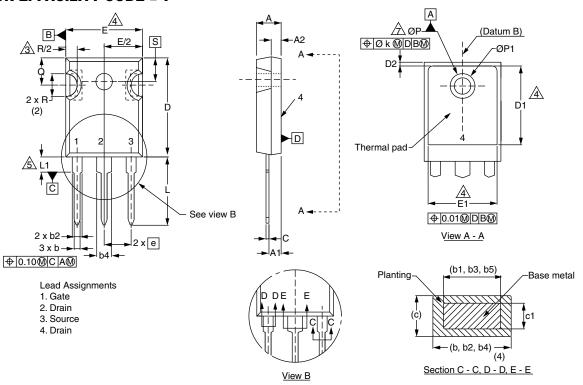
MILLIMETERS		
MIN.	MAX.	NOTES
16.25	16.85	5
0.56	0.76	
15.50	15.87	4
13.46	14.16	5
4.52	5.49	3
5.44	BSC	
14.90	15.40	
3.96	4.16	6
3.56	3.65	7
7.19 ref.		
5.31	5.69	
5.54	5.74	
	MIN. 16.25 0.56 15.50 13.46 4.52 5.44 14.90 3.96 3.56 7.19	MIN. MAX. 16.25 16.85 0.56 0.76 15.50 15.87 13.46 14.16 4.52 5.49 5.44 BSC 14.90 15.40 3.96 4.16 3.56 3.65 7.19 ref. 5.31 5.69

- (1) Package reference: JEDEC® TO247, variation AC
- (2) All dimensions are in mm
- (3) Slot required, notch may be rounded
- (4) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm per side. These dimensions are measured at the outermost extremes of the plastic body
- (5) Thermal pad contour optional with dimensions D1 and E1
- (6) Lead finish uncontrolled in L1
- $^{(7)}$ Ø P to have a maximum draft angle of 1.5° to the top of the part with a maximum hole diameter of 3.91 mm
- (8) Dimension b2 and b4 does not include dambar protrusion. Allowable dambar protrusion shall be 0.1 mm total in excess of b2 and b4 dimension at maximum material condition

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VERSION 2: FACILITY CODE = Y



	MILLIMETERS		
DIM.	MIN.	MAX.	NOTES
Α	4.58	5.31	
A1	2.21	2.59	
A2	1.17	2.49	
b	0.99	1.40	
b1	0.99	1.35	
b2	1.53	2.39	
b3	1.65	2.37	
b4	2.42	3.43	
b5	2.59	3.38	
С	0.38	0.86	
c1	0.38	0.76	
D	19.71	20.82	
D1	13.08	-	

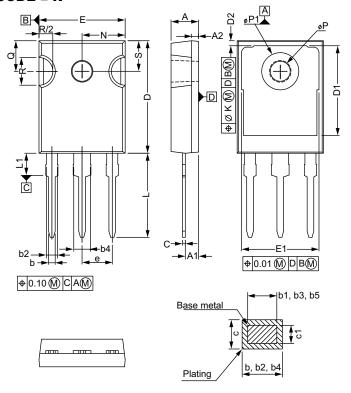
	MILLIN		
DIM.	MIN.	MAX.	NOTES
D2	0.51	1.30	
Е	15.29	15.87	
E1	13.72	-	
е	5.46	BSC	
Øk	0.254		
L	14.20	16.25	
L1	3.71	4.29	
ØР	3.51	3.66	
Ø P1	-	7.39	
Q	5.31	5.69	
R	4.52	5.49	
S	5.51 BSC		

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c

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VERSION 3: FACILITY CODE = N



	MILLIMETERS		
DIM.	MIN.	MAX.	
Α	4.65	5.31	
A1	2.21	2.59	
A2	1.17	1.37	
b	0.99	1.40	
b1	0.99	1.35	
b2	1.65	2.39	
b3	1.65	2.34	
b4	2.59	3.43	
b5	2.59	3.38	
С	0.38	0.89	
c1	0.38	0.84	
D	19.71	20.70	
D1	13.08	-	

	MILLIMETERS		
DIM.	MIN.	MAX.	
D2	0.51	1.35	
E	15.29	15.87	
E1	13.46	-	
е	5.46	BSC	
k	0.2	54	
L	14.20	16.10	
L1	3.71	4.29	
N	7.62 BSC		
Р	3.56	3.66	
P1	=	7.39	
Q	5.31	5.69	
R	4.52	5.49	
S	5.51	BSC	

ECN: E20-0545-Rev. F, 19-Oct-2020

DWG: 5971

- ⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")



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Vishay

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